

The Array of Saturated Gain Avalanche Diode concept

Fabrice Retiere (TRIUMF)



Work done within the PHoton for Astro-particle and Applied Research (PHORWARD) group at TRIUMF



Special thanks for the development of this concept to Serge Charlebois (U.



rd simulation work but Tristan Sullivan and Sam de Jong (U. Victoria)

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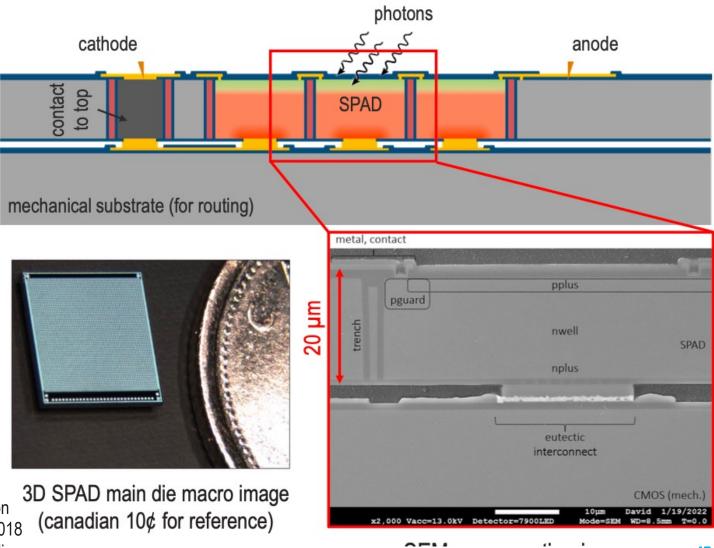
Motivation, 4-Dimensional detection

- Charged particle tracking with position resolution in the range of 0(50um) and timing resolution 0(20ps)
 - Position resolution has been available for a while with silicon pixel detectors
 - The game changing proposal is adding the timing resolution
- Enable identification of individual collision
- Enable tracking and particle identification by time of flight with the same detector

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Building upon Photon to Digital Converter

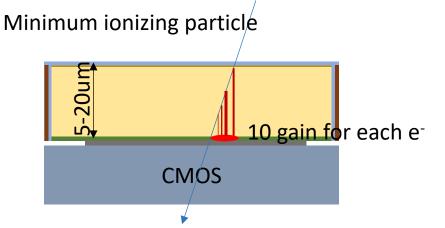
- PDC developed for Astroparticle physics
- "Front-side" illuminated single photon detector
- Designed at U.Sherbrooke (QC, Canada)
- Built at Teledyne-DALSA (QC, Canada)



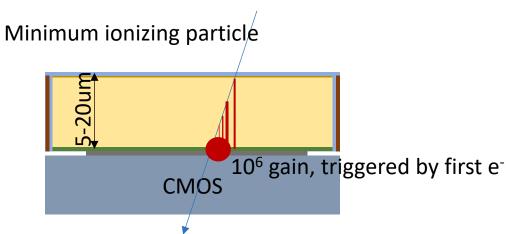
Parent, Samuel, et al. "Single photon avalanche diodes and vertical integration process for a 3D digital SiPM using industrial semiconductor technologies." 2018 IEEE Nuclear Science Symposium and Medical Imaging Conference Proceedings (NSS/MIC). IEEE, 2018.



Hybrid LGAD or hybrid SPAD



- Trench isolated hybrid LGAD
 - All ionization electrons contribute to signal formation
 - Signal on the order of 10⁴ e-



- Single Photon Avalanche Diode
 - Only first e- to reach the high field region does something
 - Low fluctuation
 - Signal on the order of 10⁶



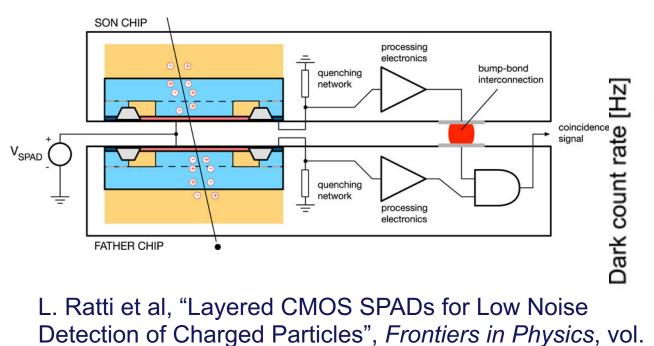


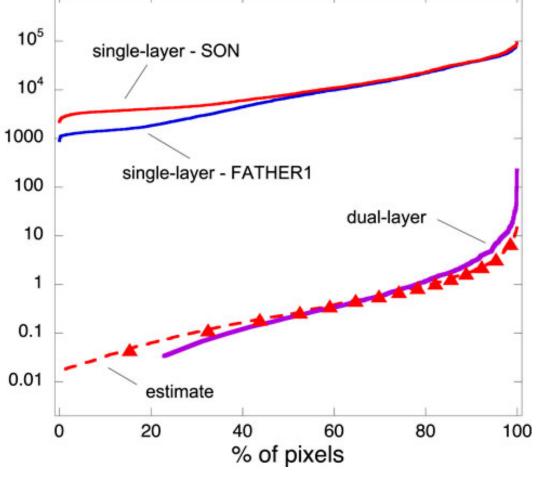
SPAD vs LGAD

- SPAD
 - Timing down to 10ps or better. Fire on single electron
 - No or very small contribution of the ionization fluctuation to timing resolution
 - High intrinsic gain 10⁵ to 10⁷, simplifying timing electronics
 - No energy deposition information
 - Same signal for 1 electrons or 20,000 electrons
 - Swamped by thermal noise ~50kHz/mm² at room temperature
 - Drops by roughly factor 10 every 20 degrees K/C.
- LGAD
 - Timing limited by energy loss fluctuations and electronics performance
 - Need low noise electronics
 - Can measure number of e- deposited albeit with some fluctuations
 - Not impacted by thermal noise



The complicated way around the dark noise issue





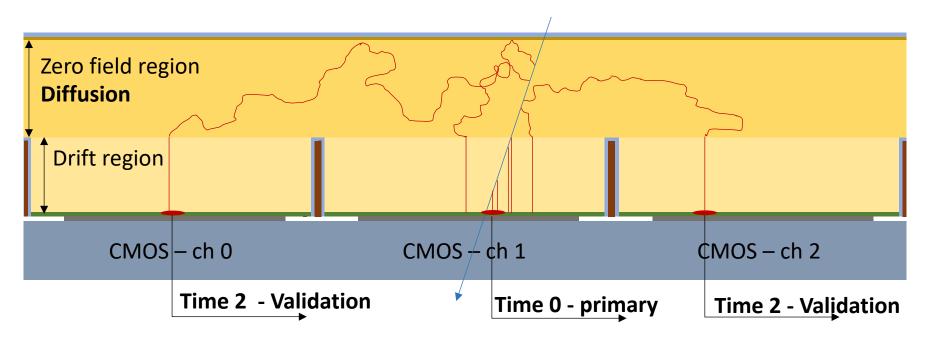
8, 2021.

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The ASGAD concept

- Geiger-mode avalanche diode region -> excellent timing
- Diffusion region for validating signal due to particle
 - ~300 e- per um produced by a MIP so need 0.1-1% diffusion probability to the neighbor for the validation signal to be always there



November 2023

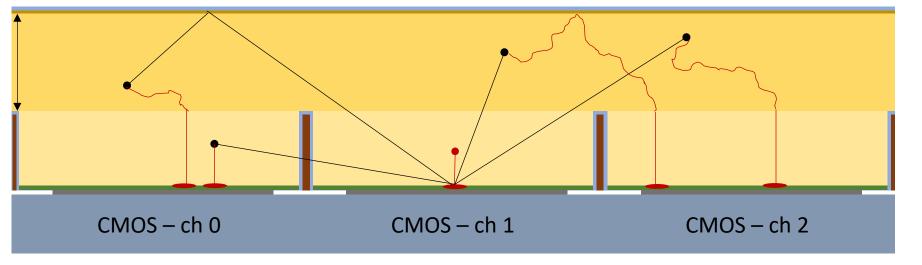
Particle tag = Primary * validation 1 * validation 2

Discovery, accelerated

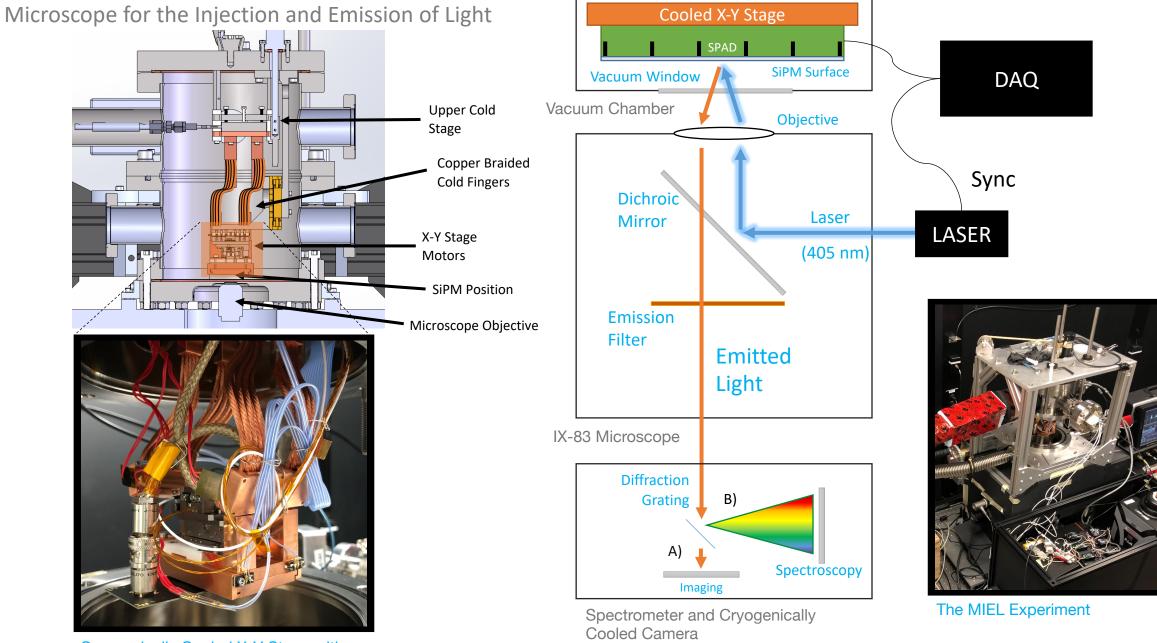


Easy? ... Not so fast

- Thermal background made worse by photon production in avalanches
 - Photons produced per avalanche 1-1,000
 - Electrons produced by photon absorption fire neighboring diodes



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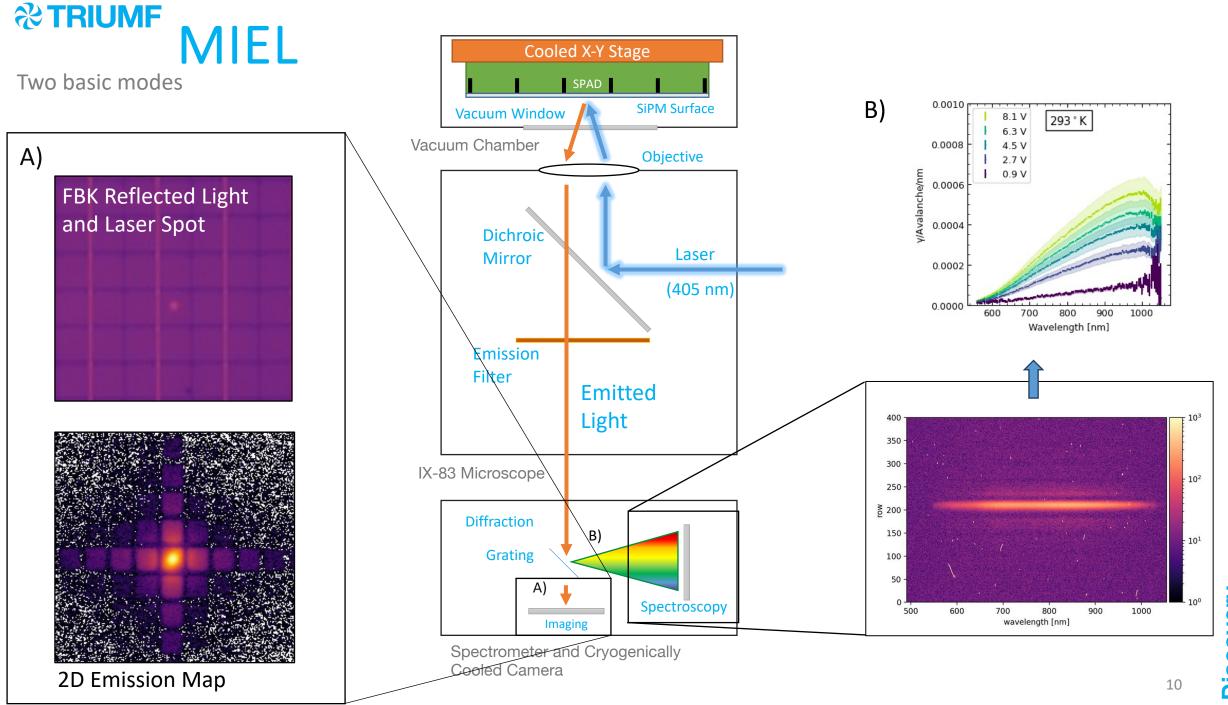


Cryogenically Cooled X-Y Stage with SiPM Mounted

Disco di accelerati

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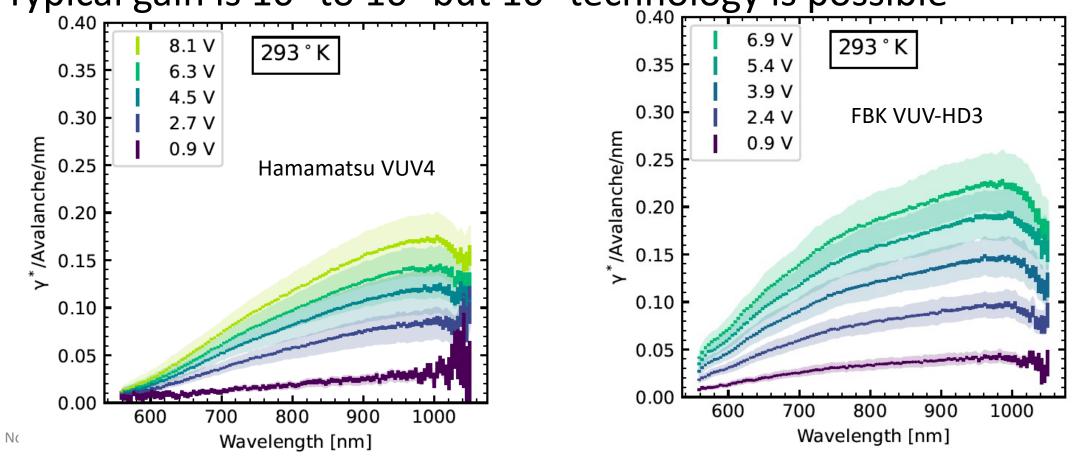
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Photon production in avalanches and transport measured for VUV SiPMs

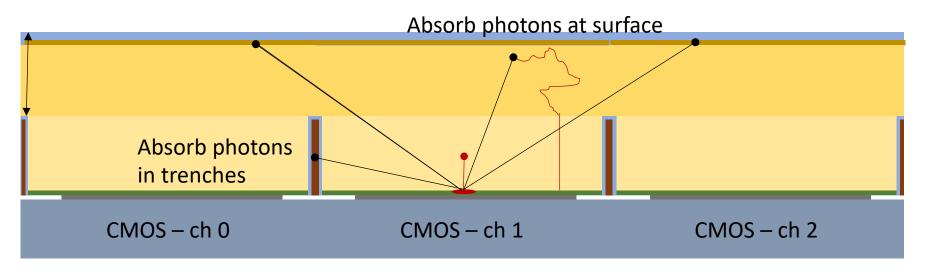
Key parameter is about 1 photon produced per 10⁵ electrons Typical gain is 10⁶ to 10⁷ but 10⁵ technology is possible



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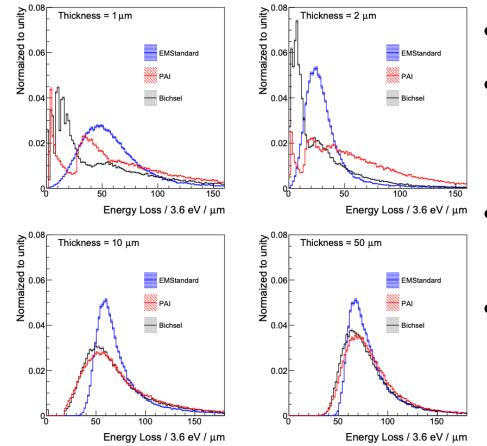
Mitigation strategies

- Minimize diffusion region thickness \rightarrow limited by efficiency requirements
- Absorb photons at front and back surface
- Absorb photons in trenches
 - The smaller the pitch the better
- Minimize production by running at lowest possible gain
 - Gain scales by diode capacitance so the smaller the better



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Optimization of performances



The Impact of Incorporating Shell-corrections to Energy Loss in Silicon

Fuyue Wang^{1,2}, Su Dong³, Benjamin Nachman², Maurice Garcia-Sciveres², and Qi Zeng³

November 2023

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²Physics Division, Lawrence Berkeley National Laboratory, Berkeley, CA 94704, USA
³SLAC National Accelerator Laboratory, Stanford University, Menlo Park, CA 94025, USA

- Diffusion region thickness 5-20 um
- Avalanche region thickness ~1um
 - Guarantee >1e- in high field region (mean free path 0.26um)
- Drift region thickness ~10um
 - Absorbs photons from avalanche
- Pitch for optimizing signal to noise
 - Though small pitch means \$\$ to achieve electronics performance with limited area
 - Though rectangular geometry possible

Quanta-Level Avalanche Diode Simulator (QLADS)

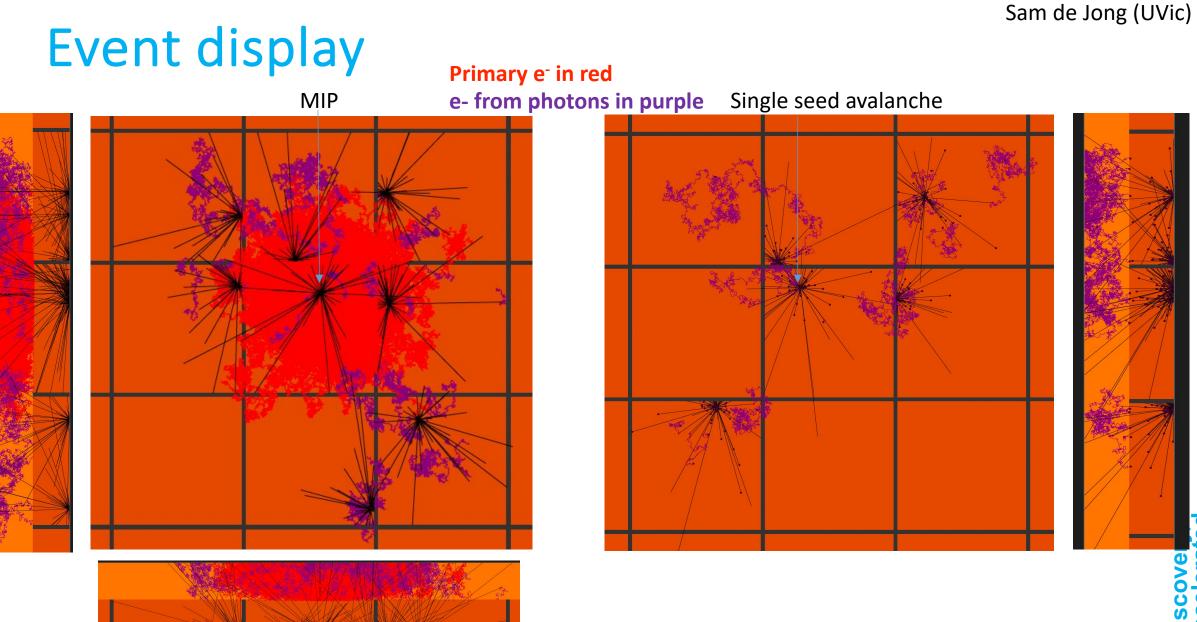
- Mixing charge carrier and photon transport not currently available in TCAD programs
- Use Monte-Carlo techniques for all transport processes
- Aim is to make this package open source
- Development team
 - Concept: FR & Juan-Pablo Yanez (U. Alberta, AB, Canada)
 - Development: Sam De Jong and Tristan Sullivan (U. of Victoria, BC, Canada)

Quanta-Level Avalanche Diode Simulator (QLADS) for ASGAD

- Charge particle simulation
 - Use Bichsel cross-section for 45 GeV/c pions
 - Normal incidence randomly over all area
- Dark noise
 - Assess the probability distribution function for the number of avalanches produced by 1 seed avalanche
 - Use 50 Hz/mm² for scale

- Electron transport
 - Random walk
 - Diffusion constant = 3.9 um²/ns
 - Electron life time = 100 ns
 - No explicit EField
- Photon from avalanches
 - Vary average number of photons per avalanche + Poisson statistics
 - Use measured spectrum
 - Track photons but absorb on all surfaces
 - Simplest but not most conservative





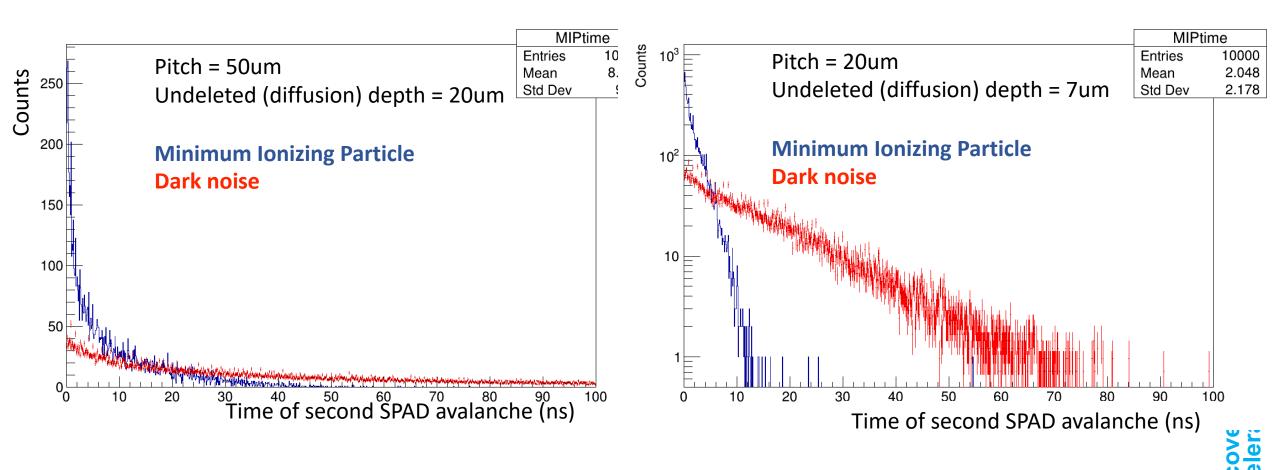
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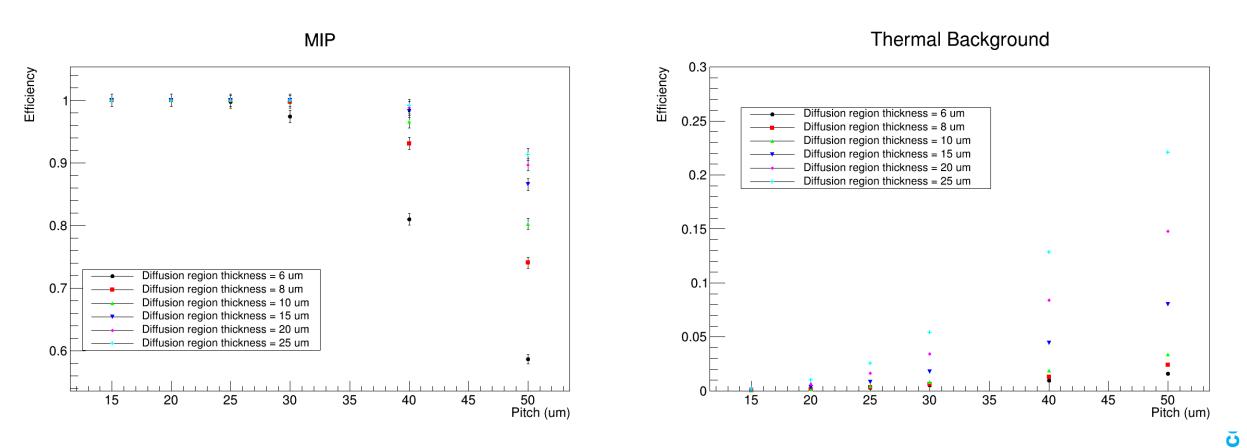


Diffusion time

Tristan Sulivan (Uvic)



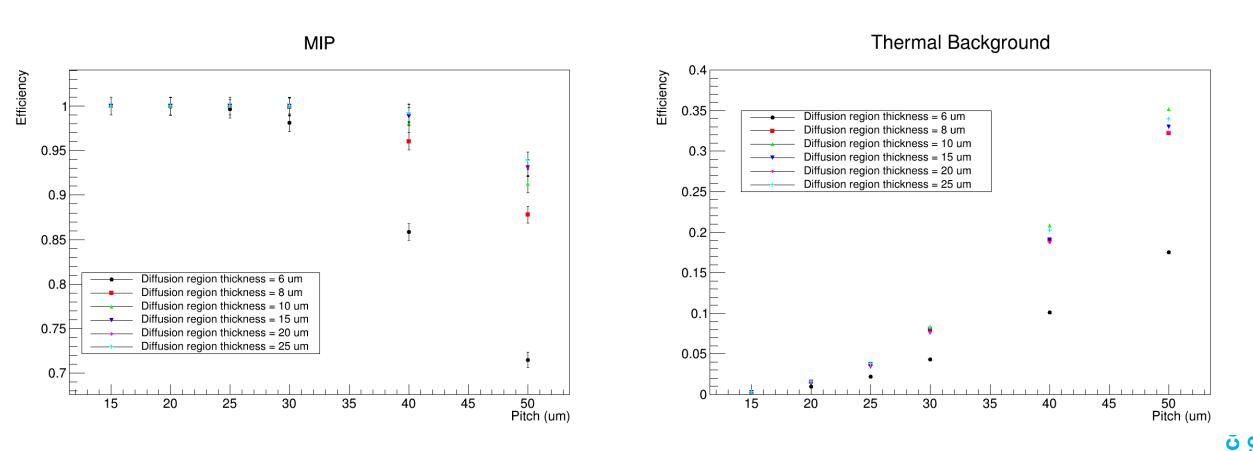
Full transparent/absorbing back surface



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"Fresnel" reflection on back surface

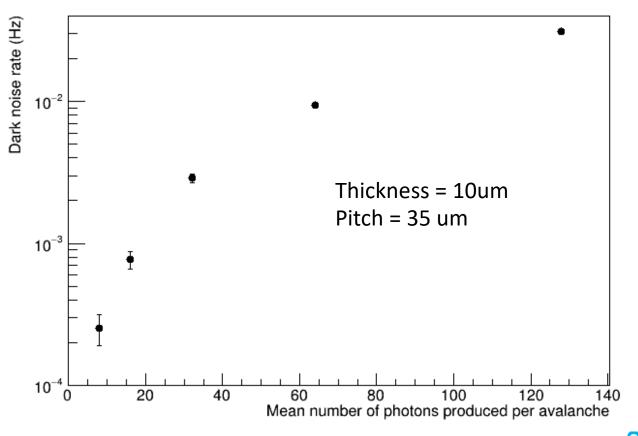


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Concerns

- Performances very sensitive to mean number of photons produced per avalanche
- Back surface must not reflect photons
- Timing
 - Diffusion is fairly slow
 - It takes up to 40ns to fire 2 Diodes
 - Time to trigger all the diodes can be 100ns
- Radiation damage
 - Similar to SiPMs so probably not so good



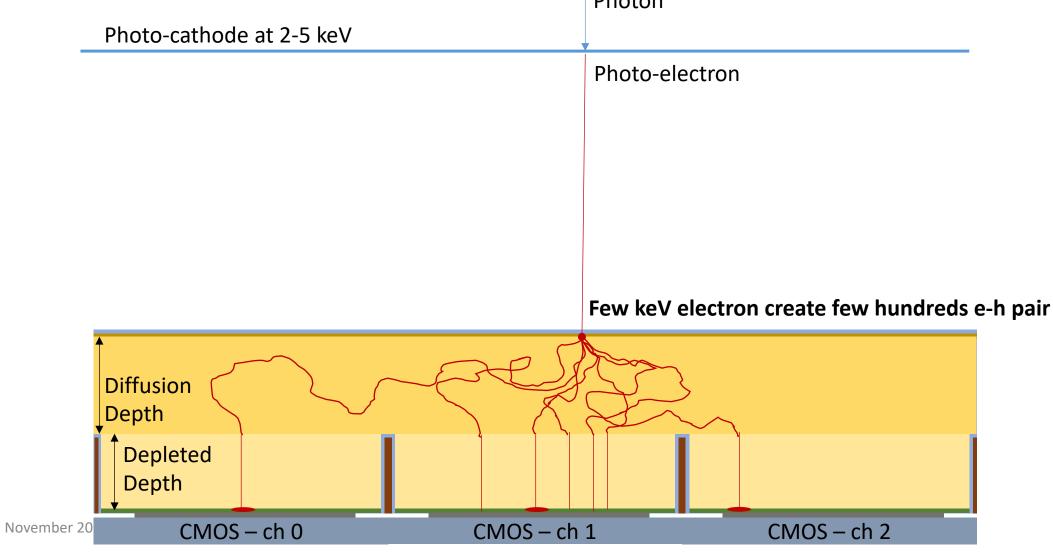
Summary and way forward

- Why ASGAD?
 - Timing, timing, timing. ASGAD only need to sweep one e- to high field region
 - 10 ps is the current frontier but more work on Efield engineering + CMOS can continue to bring this down
 - Sub-1ps is on the horizon
 - Thermal noise appears manageable

• Prototyping

- Teledyne-DALSA as part of Backside illuminated sensor development
 - Will happen but probably slowly due to resource competition with FSI SPAD
- ... Interested partners?
 - **FBK**, We could start by making an analog prototype
 - Used old SiPMs? 10 years ago life time of carrier was long in bulk
 - Was changed to limit delayed crosstalk

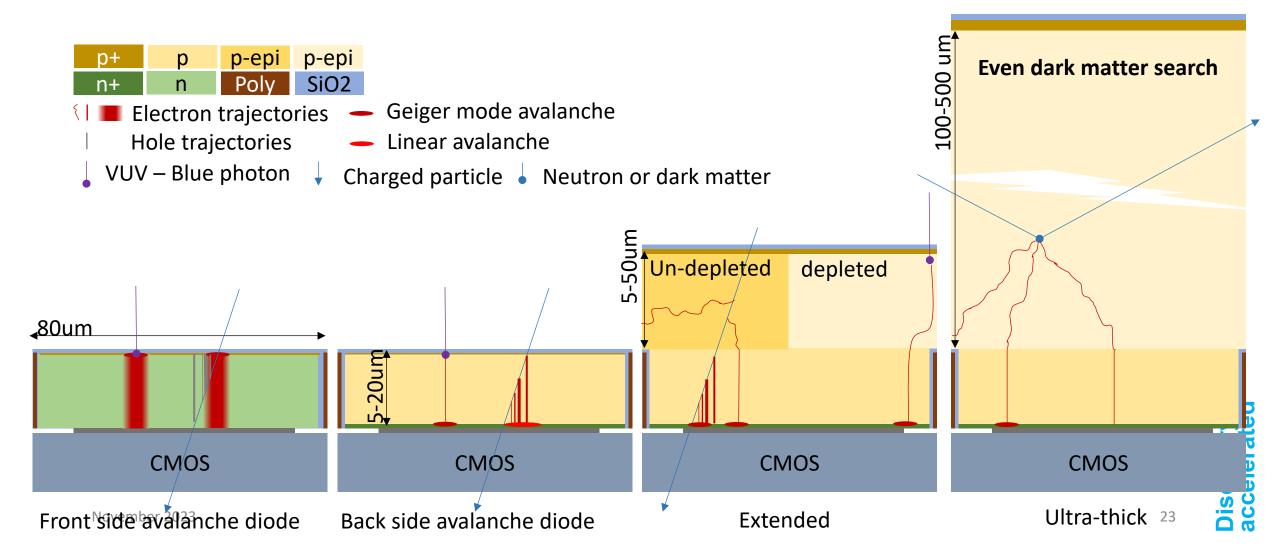
Beyond ASGAD – Digital Hybrid Photodetector



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Beyond ASGAD – Back-side illuminated for everything



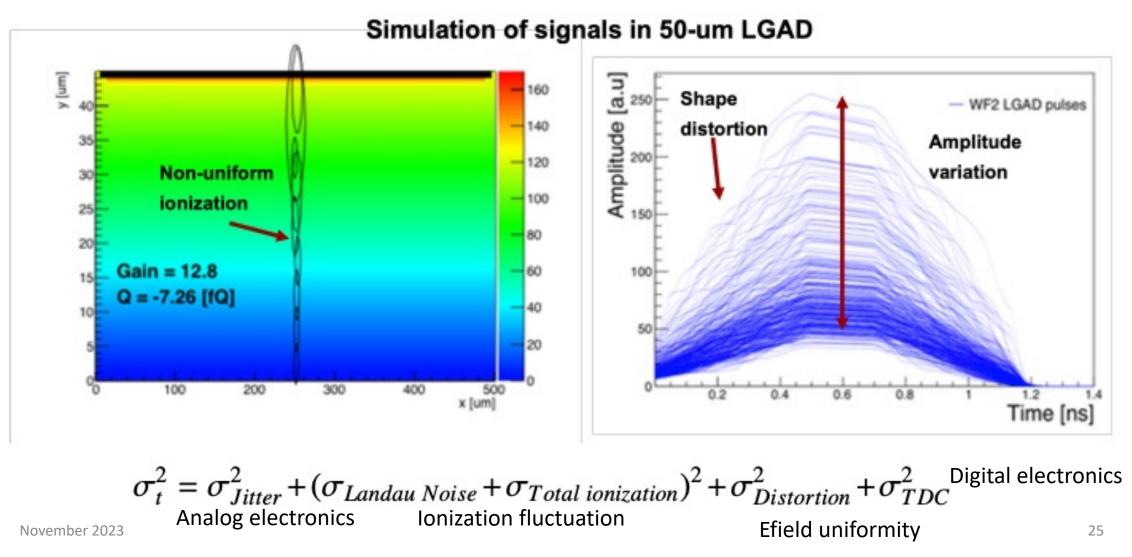
It does not always rain in Vancouver Join our team!

4D Tracking: Present Status and Perspective

N. Cartiglia^{a,*}, R. Arcidiacono^{a,b}, M. Costa^{a,c}, M. Ferrero^{a,b}, G. Gioachin^c, M. Mandurrino^a, L. Menzio^a, F. Siviero^a, V. Sola^{a,c}, M. Tornago^{a,c}

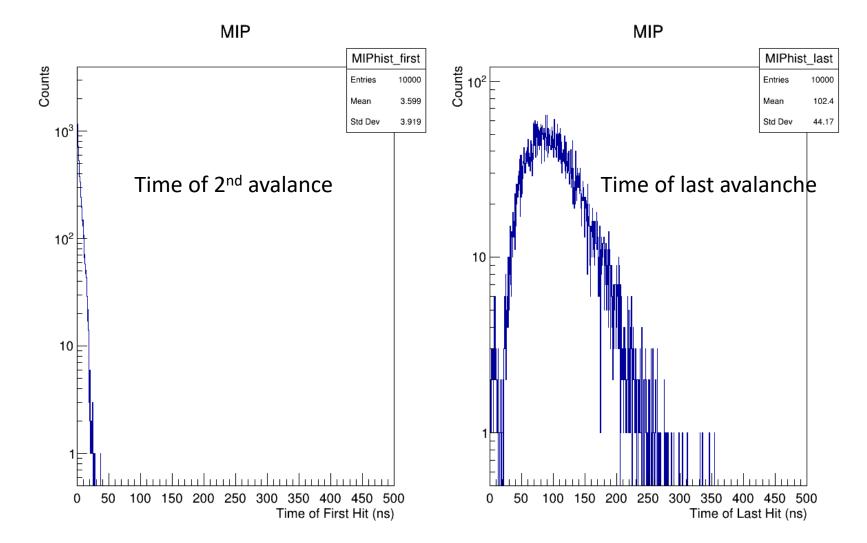
^aINFN, Torino, Italy ^bUniversità del Piemonte Orientale, Italy ^cUniversità di Torino, Torino, Italy

LGAD, the 4D leading technology





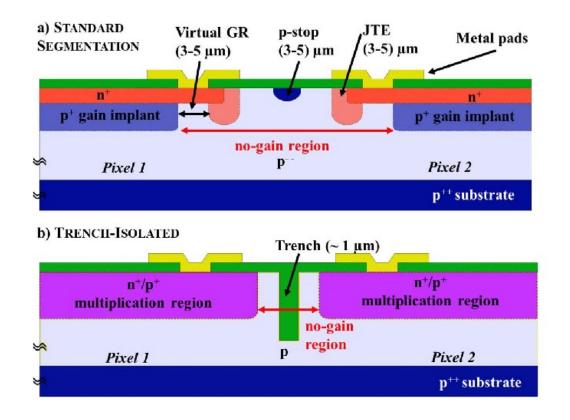
Timing delay



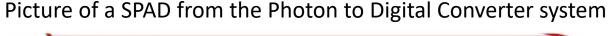
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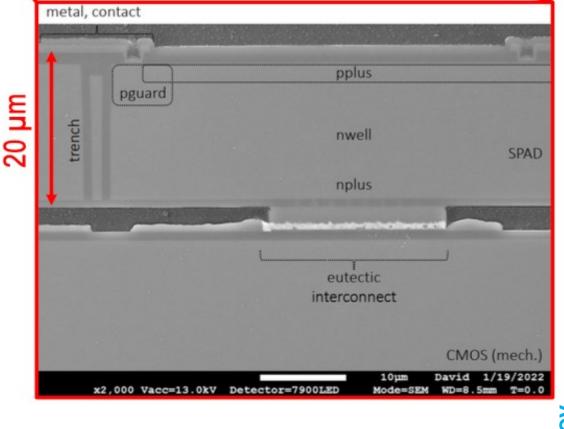
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From trench isolated LGAD to Single Photon Avalanche Diode



G. Paternoster et al. <u>10.1109/LED.2020.2991351</u>

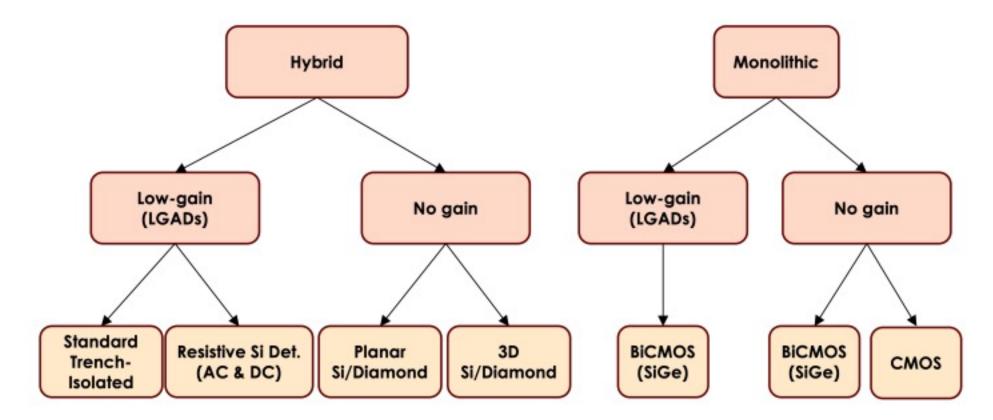




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The technology landscape ... so far

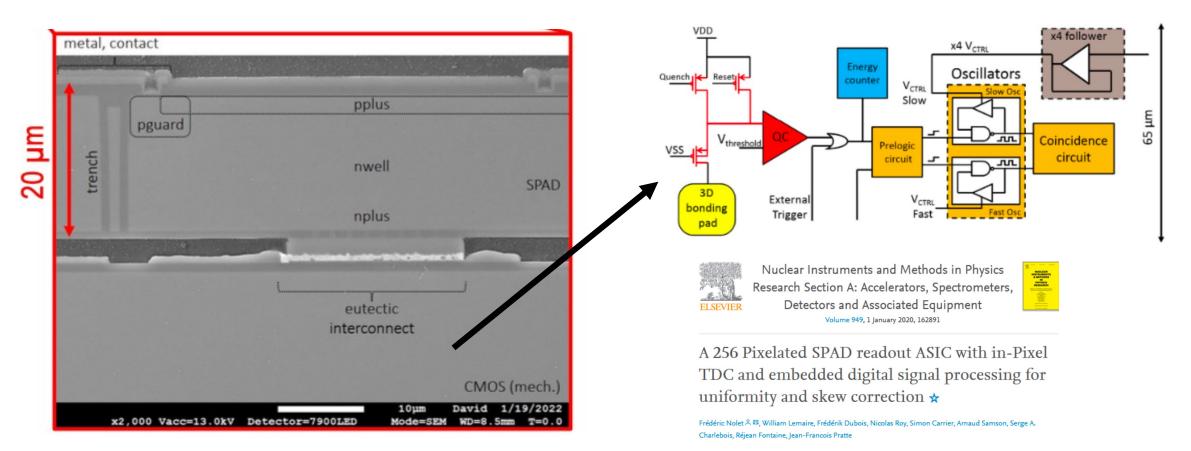


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TDC integrated in each Single Photon Avalanche Diode (SPAD)



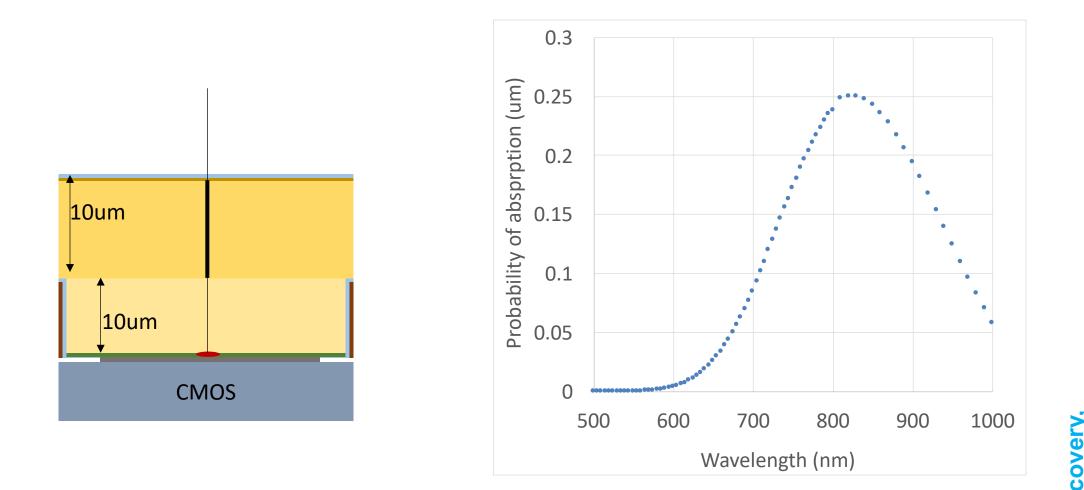
Discovery, accelerated

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Engineering, Université de Sherbrooke, Sherbrooke, QC, J1K 2R1, Canada

Wavelength that are problematic

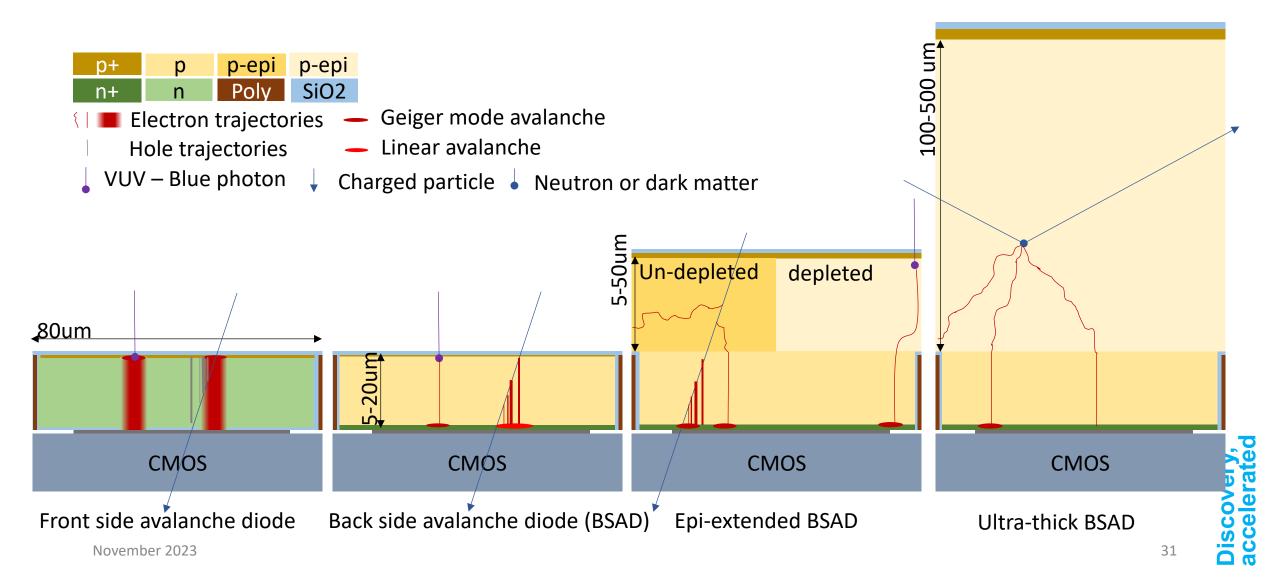


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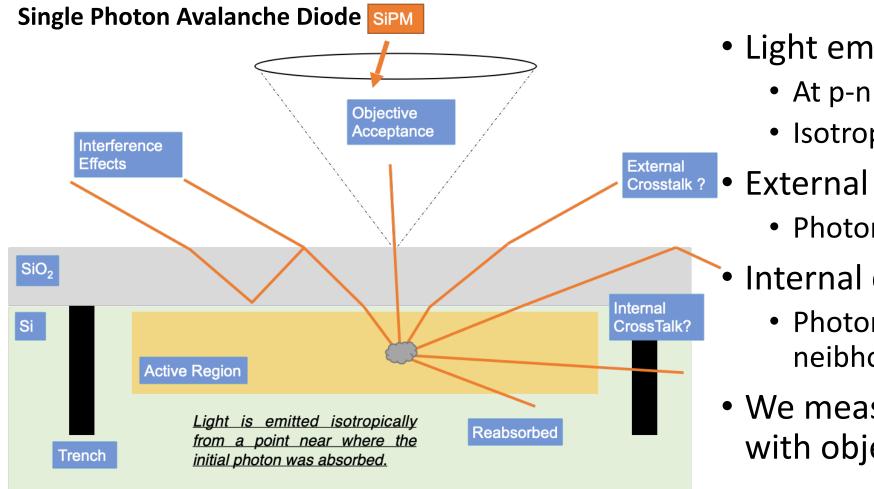
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The 3D integrated master plan





Light emission to crosstalks



- Light emission assumptions:
 - At p-n junction maximum field
 - Isotropic
- External cross-talk
 - Photon escaping the SiPM surface
 - Internal cross-talk
 - Photons being absorbed in a neibhoring SPAD
 - We measure photons escaping with objective acceptance