

Advancement and Innovation for Detectors at Accelerators

WP4 Upgrade of Irradiation and Characterization Facilities

Task 4.5: Design & Development of a New Electronics Characterization System for EMC Control (*ITAINNOVA, CNRS-IPHC*)

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- 2. WP4.T5 status and plans
- •3. Summary



- <u>Goal:</u> The main goal of this activity is to upgrade Electromagnetic Compatibility (EMC) tests in order to improve the support for detector electronics designers.
 - Noise studies were greatly demanded on previous AIDA 2020 project
- **<u>Activities</u>**: Two activities are planned
 - Design and develop an automatic EMC test bench to measure the noise transfer functions (TF) of physics detectors.
 - Design and develop a portable test bench to perform in-situ EMC conducted emission measurements of power units in irradiation facilities.

• What is the novelty of the activity ?

- There are no systems to measure the TF of any detector against electromagnetic noise.
- There is not any portable test bench to measure the noise emissions of DC-DC converters or small PS units during the radiation test campaigns.





Two institutes participates in this tasks:
 Instituto Tecnológico de Aragón (ITAINNOVA) (Zaragoza, Spain)
 Institut Pluridisciplinaire Hubert CURIEN (IPHC)/CNRS (Strasbourg, France)



- ITAINNOVA and IPHC will participate in both activities
 ITAINNOVA will provide the design /control of the EMC equipment (both test benches)
 - ➢ IPHC-CNRS will be in charge of the *interface between the EMC system and the DAQ* of the detector for the TF test bench and the *technical background about radiation aspects and regular access* to its radiation facility.
- One Milestone and deliverable are planned:
 MS17: Apply TF test bench to FEE prototypes [M23]
 D4.5 : Develop a conductive noise test bench for irradiation facilities [M44]



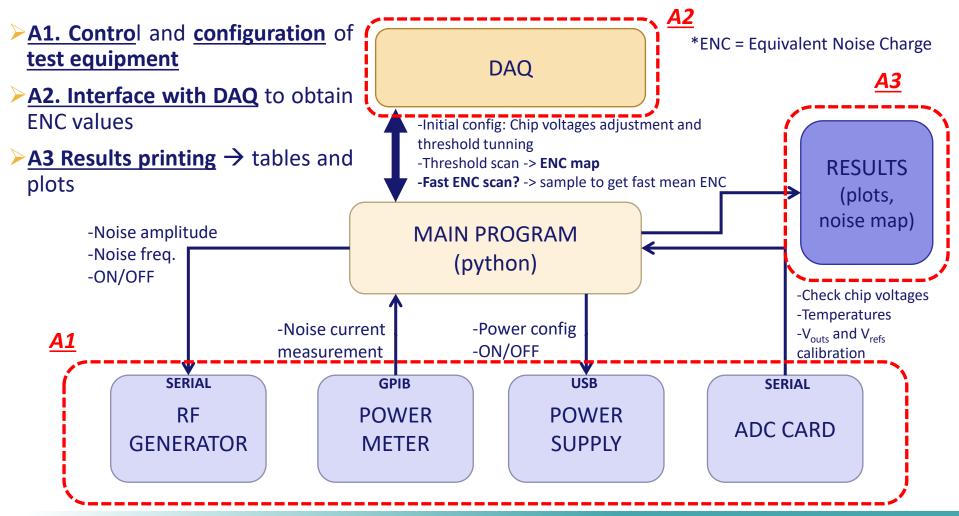
 During the first year of the project the activities has been focused the design and develop an automatic EMC test bench to measure the noise transfer functions (TF) of physics detectors.

> 1 week \rightarrow a few hours ...





• This the simplified scheme of the system.





- Routines to control test instruments are already done and tested (A1).
 - They work properly on both Windows and Linux operating systems, for <u>easy adaptation to any DAQ detector.</u>





2. WP4.T5 status and plans

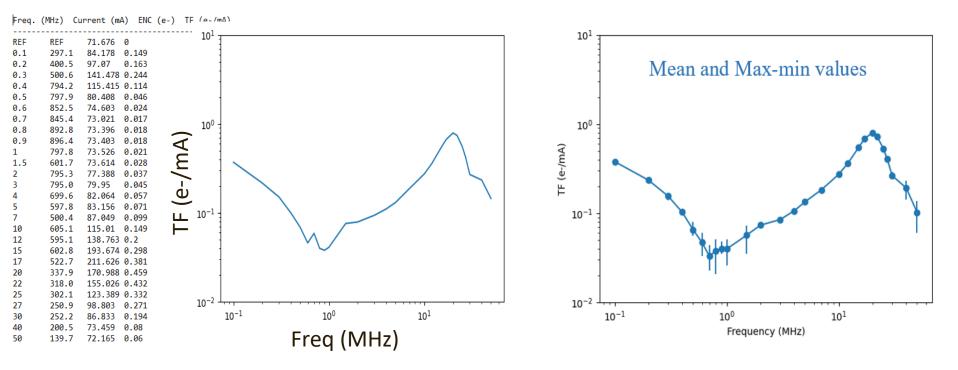
- Interface with DAQ is complete -> multiplatform compatibility with some sub-routines adjustments (A2)
 - It reads and modifies the DAQ configuration files to adjust different chip parameters when needed.
 - >It configure and launch noise scans and read the results from the DAQ output.
 - ➢ Fast noise scan has been implemented (using only a small sample of pixels) in order to quickly adjust injected noise before launching a full noise scan.
 - Valid for different DAQ Already tested for CMS DAQ and also BDAQ53 (Atlas DAQ)

The main program is fully functional.

- >It works as a high-level control capable of running complex test procedures, while controlling all the test instruments, and interfacing with the DAQ software.
- > The results are initially presented in a text file and plot images. (A3)



- First automated test was completed RD53 TF
 - The test were carried out on a RD53A Single Chip Card (SCC) and CMS Ph2 ACF
 - > The **robustness, reliability and repeatability** of the system have been tested
 - To this end, 10 tests have been carried out on an RD53A-SC.(max, min, Std,..)
 - The results are very consistent and reliable.

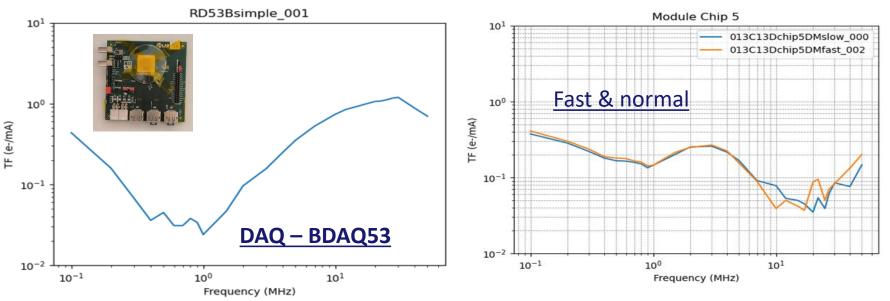




- System has been tested <u>with a diferent DAQ system</u> BDAQ53 (ATLAS)
- A <u>feature to increase the speed of tests</u> has been implemented (configurable)

Weeks - hours – <u>Now even minutes</u>

It may be used to perform a preliminary test.



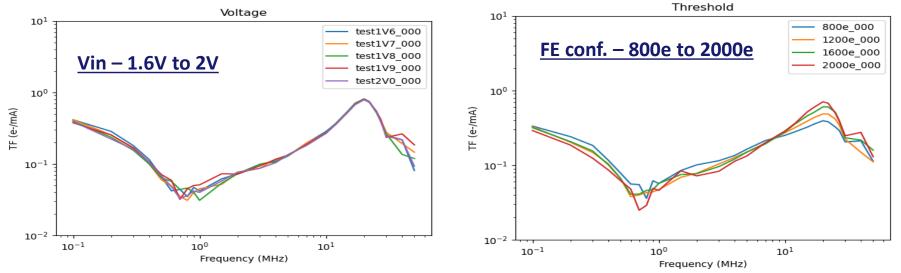


- A set of <u>complex test</u> has been performed in orde to demonstrate the potential of the system (we have used as reference RD53A EMC test)
 - SLDO regulator sentsitivity to operation point (In this case the SLDO es configured as LDO only)

Input voltage varying from 1.6V to 2.0V

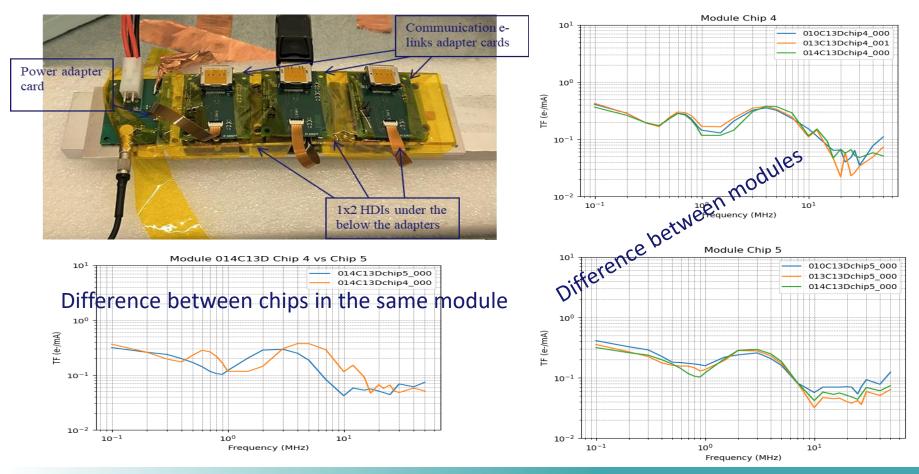
Different Front end chip threshold configuration

• Varying it from 800 to 2000 electrons.





<u>Studies at system level</u> - Small power group of CMS pixel -phase II
 >3 modules connected in series - CMS –HDI -1x2 3D sensors

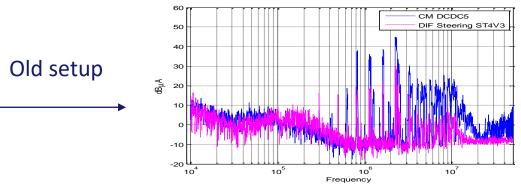


F. Arteche (ITAINNOVA) - AIDAinnova Y2 meeting - WP4 Session



- We move on to the next stage of the task.
- Design and develop a portable test bench to perform in-situ conducted EMC emission measurements of power units in irradiation facilities.







- A GaN transistor based DC-DC converter will be used as a reference power unit to design the system (current source).
 - The unit has already been designed
- Test validation will be performed at IPHC -Strasburg



3. Summary

- An overview of the status and plans for WP4.T5. has been presented
- The <u>task is progressing well</u>, with a significant focus on the development of the automatic TF measurement system.
 - >This activity has now been completed.
 - >It is a very valuable tools to perform many noise studies
 - ><u>MS17 has already been submitted</u>.
 - **EUROLABS project will benefit** form this developments. (TA facility)
- Our <u>next steps involve designing and developing a portable</u> <u>test bench to perform in-situ conducted EMC emis</u>sion measurements of power units in irradiation facilities.