

TWEPP-07 Topical Workshop on Electronics for Particle Physics

Thursday 6 September 2007

Poster session (16:45 - 18:00)

[id] title	presenter	board
[39] A complete set of firmware for the TileCal Read-Out Driver.	Mr VALERO BIOT, Alberto	
[11] A Trigger/Timing Logic Unit for ILC Test-beams	Dr CUSSANS, David	
[78] R/O Device based on USB1.0 for Spectroscopy DAQ	Mr MASEK, Petr	
[27] Digital part of SiPM Integrated Read Out Chip ASIC for ILC hadronic calorimeter	Mr DULUCQ, FREDERIC	
[37] Proposal, development and test of an analog front-end electronic board for the Nemo telescope.	Dr GIORGI, Filippo Maria	
[62] Installation and Commissioning of the CMS Timing, Trigger and Control Distribution System	Dr TROSKA, Jan	
[80] Track momentum discrimination using cluster width in silicon strip sensors for SLHC.	Prof. PARRINI, Giuliano	
[34] FPGA based Readout Logic of the Front-end Electronics of the Absolute Luminosity Monitor	IWANSKI, Wieslaw	
[21] System test for the ATLAS Pixel Detector data acquisition	TRIPLETT, Nathan	
[1] Distribution of the Timing, Trigger and Control Signals in the Endcap Cathode Strip Chamber System at CMS	Mr MATVEEV, Mikhail	
[40] Proposal of a readout technique for low-pitch pixel devices	Dr GABRIELLI, Alessandro	
[23] Results and Consequences of Magnet Test and Cosmic Challenge for the CMS Barrel Muon Alignment System	Dr SZÉKELY, Géza	
[7] Development of a Front-End Electronics for Pico-second Resolution TOF Detectors	TANG, Fukun	
[42] Calibration and performance tests of the Very-Front-End electronics for the CMS electromagnetic calorimeter	BLAHA, Jan	
[8] Status of the Optical Multiplexer Board 9U Prototype	Dr TORRES PAIS, Jose	
[93] ELMB Microcontroller Firmware and SCADA Integration for the LHCb Muon Detector Readout Control System	Dr NOBREGA, Rafael	
[18] CARLOSrx: an on line Data Acquisition system for ALICE ITS SDD	Dr FILIPPO, Costa	
[25] MAPS in 130 nm and 90 nm triple well CMOS technologies for HEP applications	Mr POZZATI, Enrico	
[3] ALIBAVA : A PORTABLE READOUT SYSTEM FOR SILICON MICROSTRIP SENSORS	Mr MARCO, Ricardo	
[72] The CMS Pixel FED	PERNICKA, Manfred	
[43] Optimization of amplifiers for MAPS	DOROKHOV, Andrei	
[83] Design of an integrated particle detector-cell based on latchup effect		
[59] Software environment for controlling and re-configuration of Xilinx Virtex FPGAs	Mr FEHLKER, Dominik	

[46] Final Test at the Surface of the ATLAS Endcap Muon Trigger Chamber Electronics	KUBOTA, Takashi	
[57] SKIROC : A FRONT-END CHIP TO READ OUT THE IMAGING SILICON-TUNGSTEN CALORIMETER FOR ILC	Mr FLEURY, Julien	
[29] SPIROC (SiPM Integrated Read Out Chip): Dedicated very front-end electronics for an ILC prototype hadronic calorimeter with SiPM readout.	Mr RAUX, Ludovic	
[41] Design of on-chip data sparsification for a mixed-mode MAPS device	Dr GABRIELLI, Alessandro	
[70] The ATLAS Level-1 Muon to Central Trigger Processor Interface	Mr HAAS, Stefan	