

TWEPP-07 Topical Workshop on Electronics for Particle Physics

Wednesday, 5 September 2007

Parallel session B4 - ASICs 1 FE chips (10:55 - 13:00)

time	[id] title	presenter
10:55	[24] Development of a small-scale prototype of the GOSSIP chip in the 0.13um CMOS technology.	Mr KLUIT, Ruud
11:20	[28] Steering and Readout Chips for DEPFET Sensor Matrices	Mr KREIDL, Christian
11:45	[79] Development of a selftriggered high counting rate ASIC for readout of 2D gas microstrip neutron detectors.	FIUTOWSKI, Tomasz
12:10	[84] Test results on the n-XYTER ASIC, a self triggered, sparcifying readout ASIC	Dr SCHMIDT, Christian
12:35	[63] VFAT2 : A front-end system on chip providing fast trigger information, digitized data storage and formatting for the charge sensitive readout of multi-channel silicon and gas particle detectors.	Dr ASPELL, Paul