

TWEPP-07 Topical Workshop on Electronics for Particle Physics

Wednesday, 5 September 2007

Parallel session B6 - ASICs 3 future (16:20 - 18:00)

time	[id] title	presenter
16:20	[15] Development of SEU-robust, radiation-tolerant and industry-compatible programmable logic components	Dr KLOUKINAS, Kostas Mr BONACINI, Sandro
16:45	[56] a low power and low signal 4 bit 50MS/s double sampling pipelined ADC for Monolithic Active Pixels Sensor	Mr DAHOUMANE, Mokrane
17:10	[71] The GBT, a Proposed Architecture for Multi-Gbps Data Transmission in High Energy Physics	MOREIRA, Paulo
17:35	[47] A circuit topology suitable for the readout of ultra thin pixel detectors at SLHC and elsewhere	Dr CAMPBELL, Michael