



# A READOUT SYSTEM-ON-CHIP FOR A CUBIC KILOMETER SUBMARINE NEUTRINO TELESCOPE

S.Anvar, V.Gautard, H.Le Provost, F.Louis, K.Menager, Y.Moudden, B.Vallage, E.Zonca,  
on behalf of the KM3NeT consortium<sup>1</sup>

IRFU/SEDI-CEA Saclay  
F-91191 Gif-sur-Yvette, France

<sup>1</sup> Supported by the European Commission through FP6 and FP7

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26-30 September 2011**



# The KM3NeT Detector

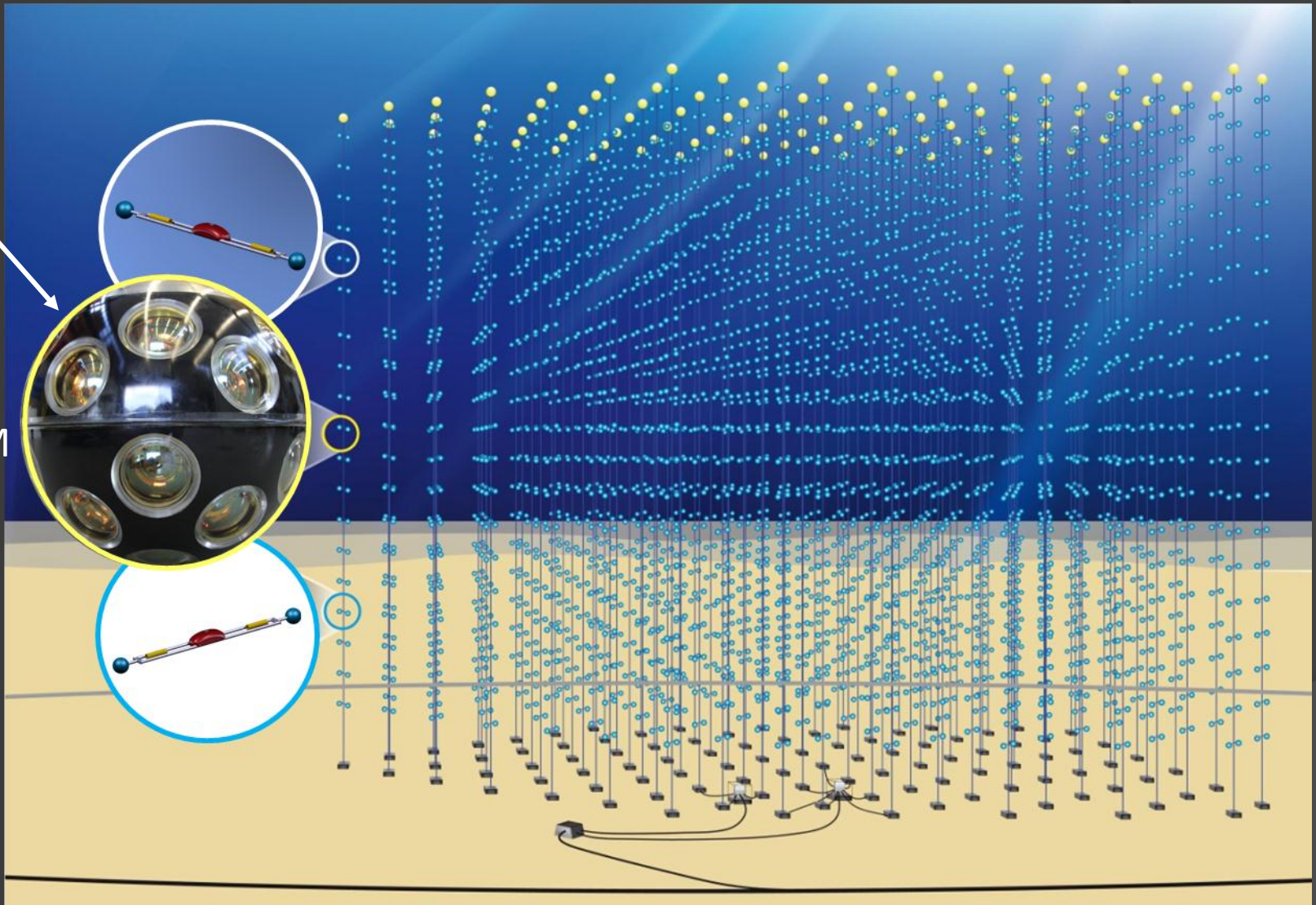


Digital  
Optical  
Module  
(DOM)

31  
PMTs/DOM

12800  
DOMs

320 lines



<http://www.km3net.org>

Technical Design Report (ISBN 978-90-6488-033-9) Conceptual Design Report (ISBN 978-90-6488-031-5)

# The DOM processor board

Power conversion board

Processor board &  
Software  
designed at CEA-  
IRFU

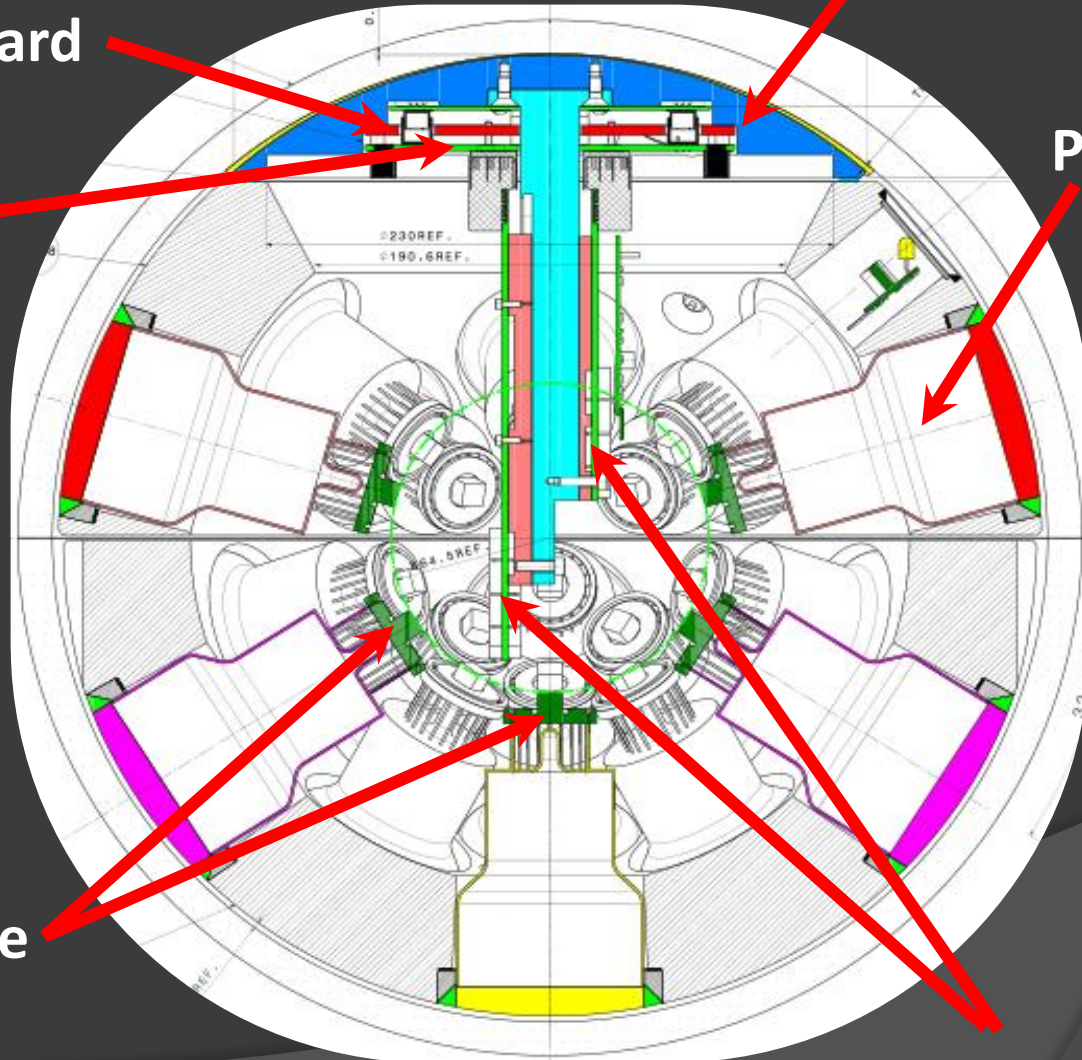
DOM designed at  
NIKHEF institute  
(Amsterdam,  
Netherlands)

HV PMT base

Heat conductor

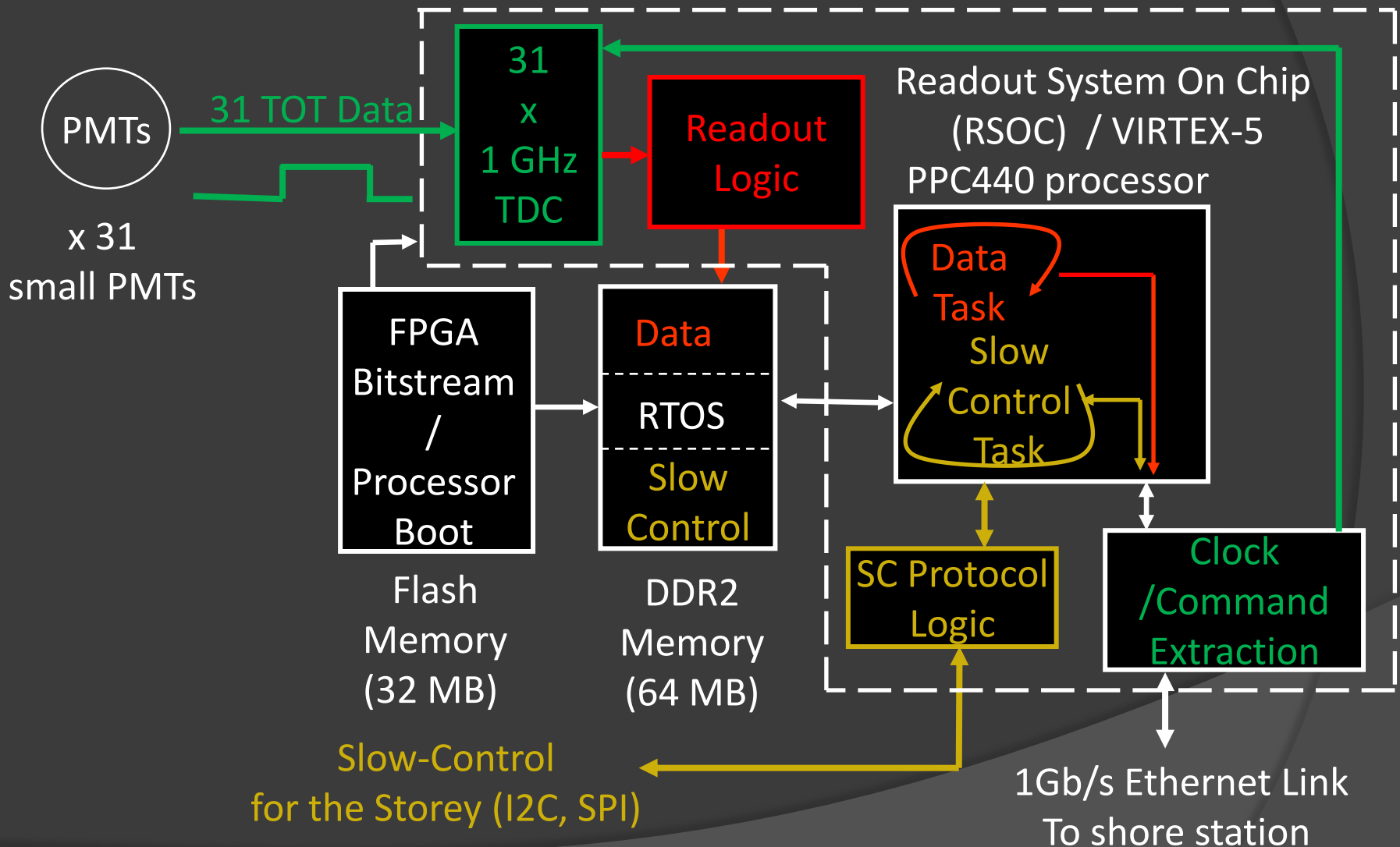
PMT

Signal collection board



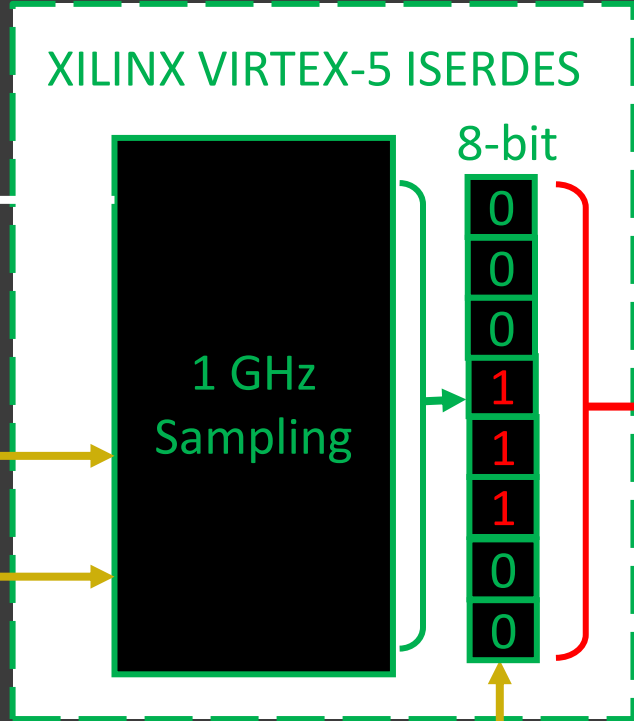
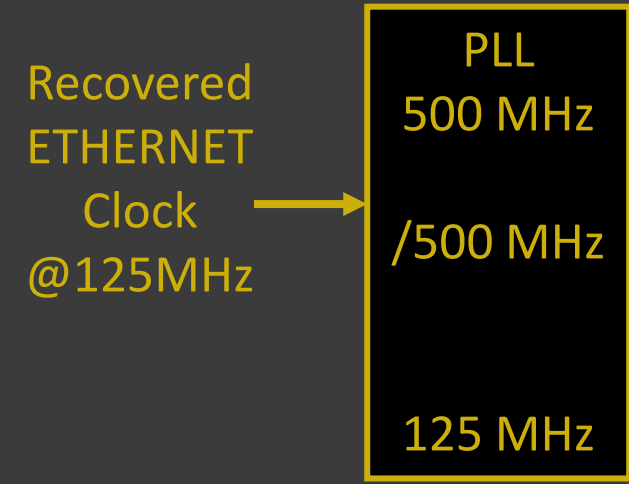


# The KM3Net prototype Offshore Processor Board



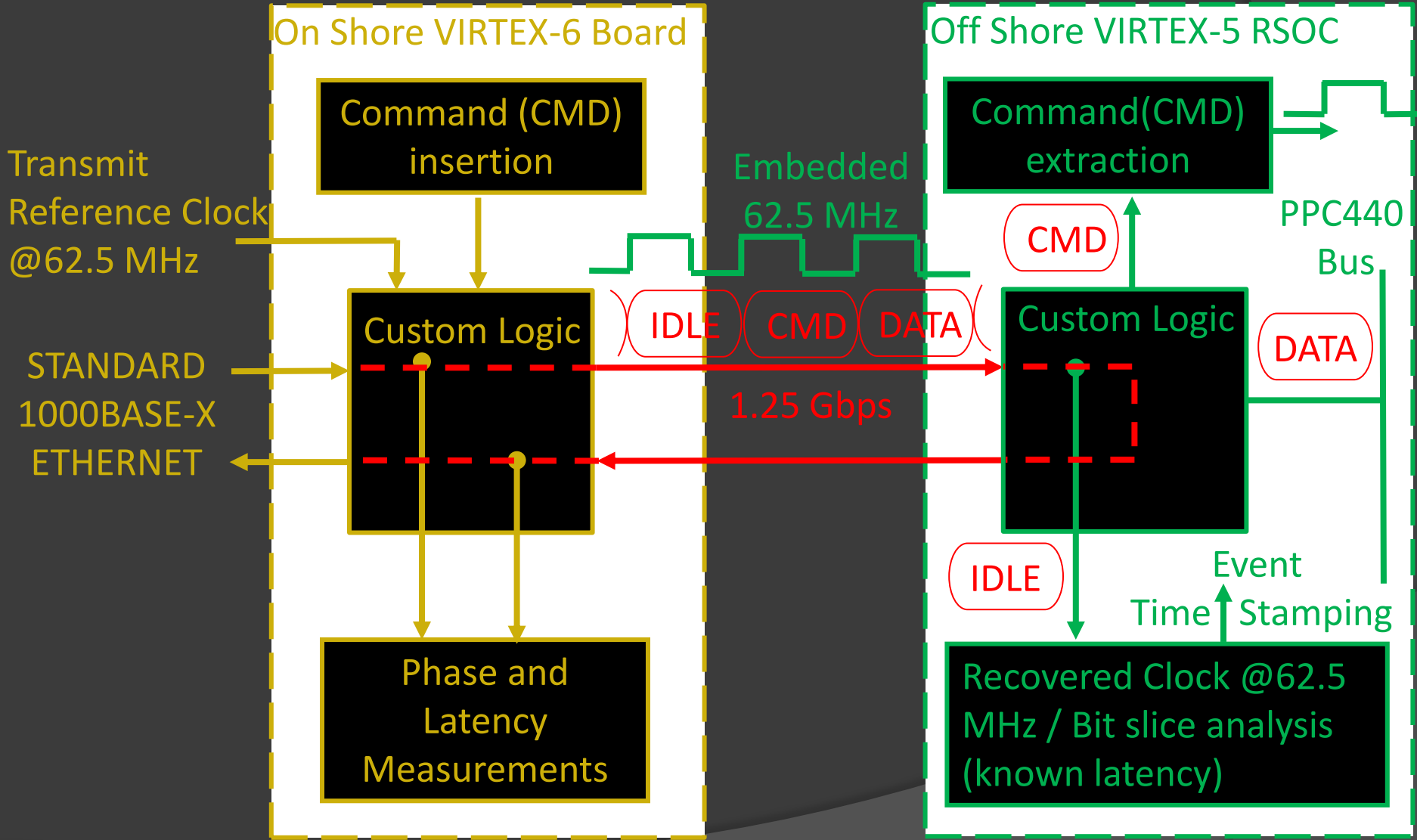


0 0 0 1 1 1 0 0

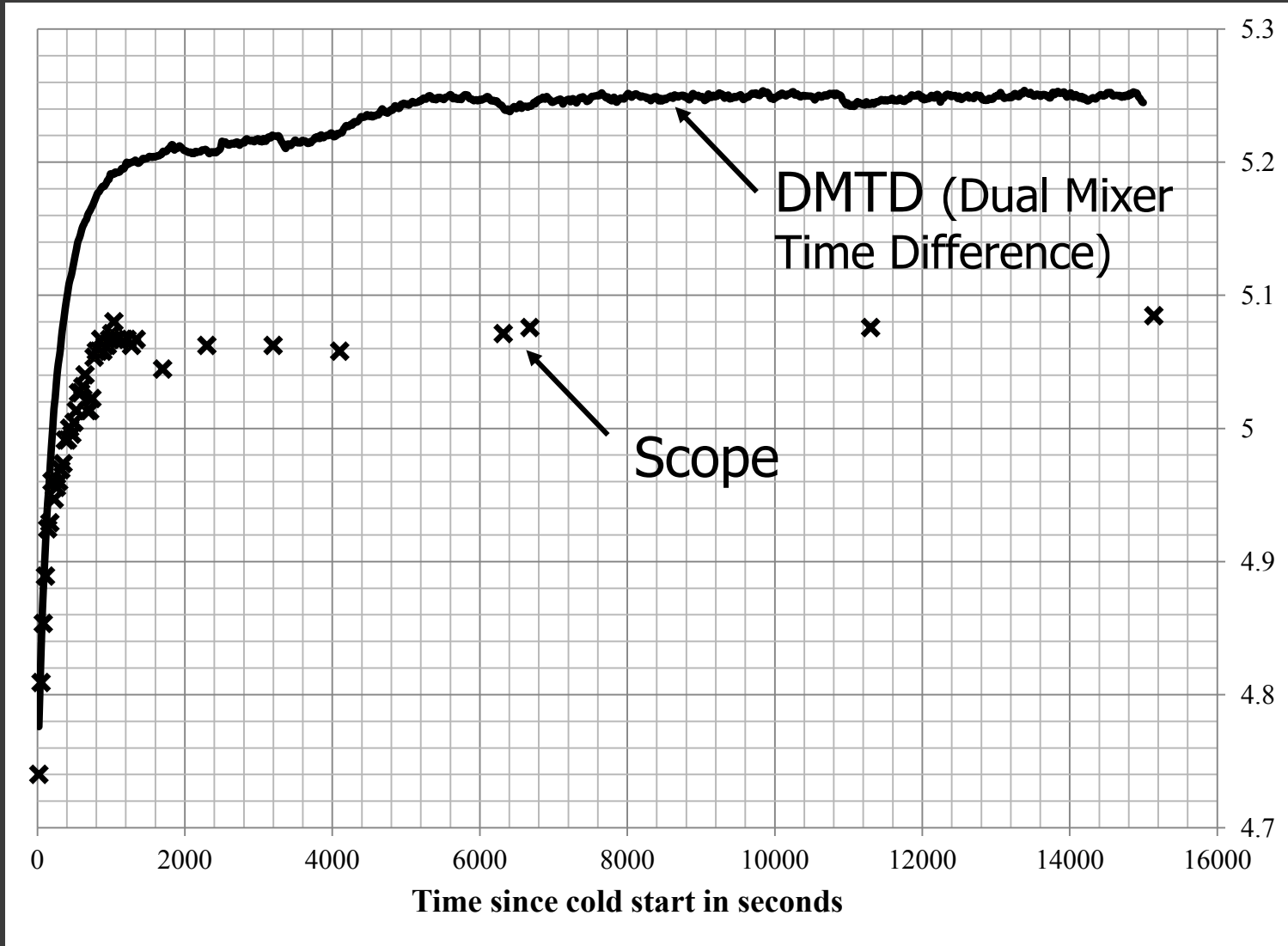


[Originally designed by A.Zwart (NIKHEF) / small PMTs test bench for ALTERA]

# Clock distribution

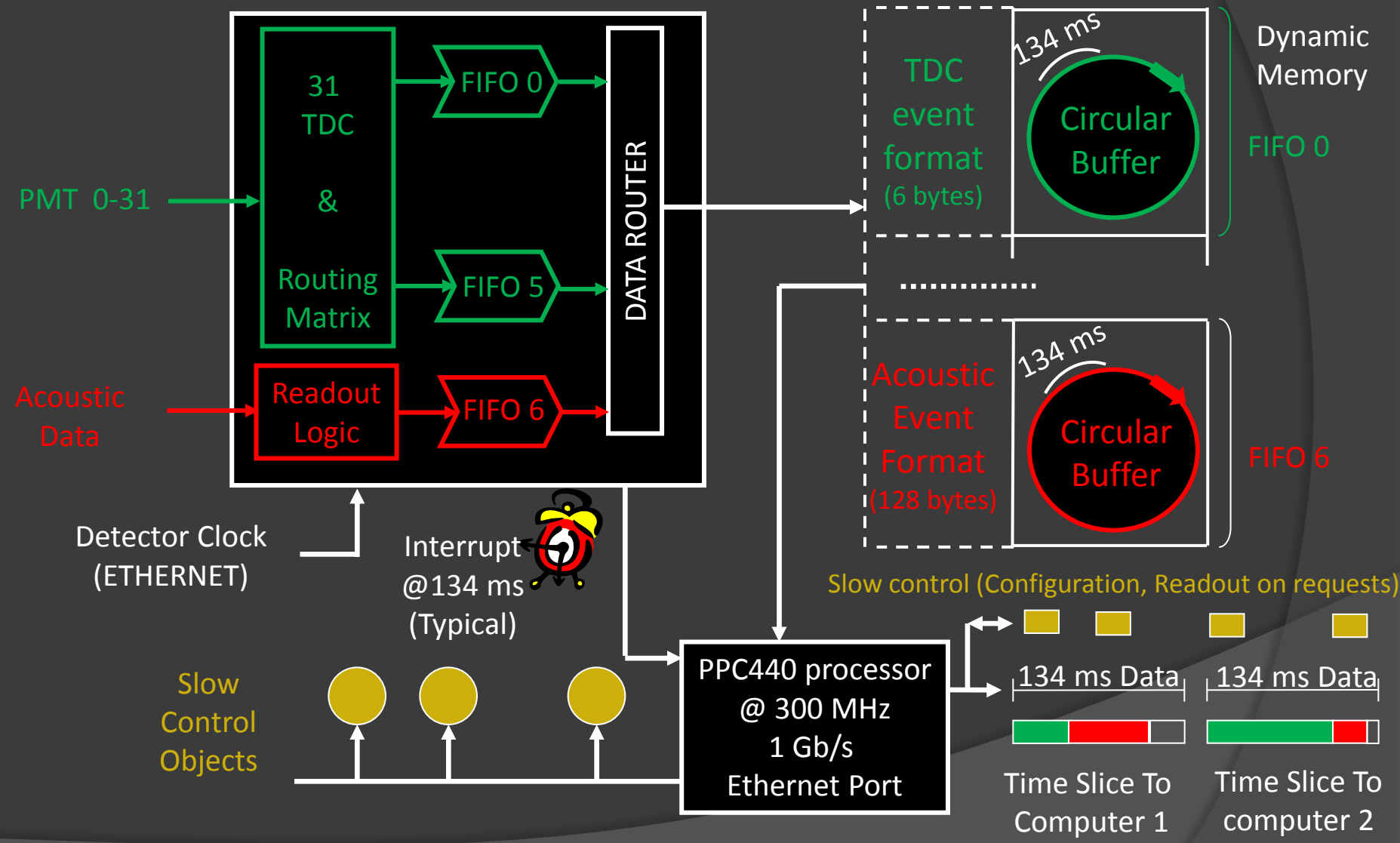


# Transmit/Receive clock skew



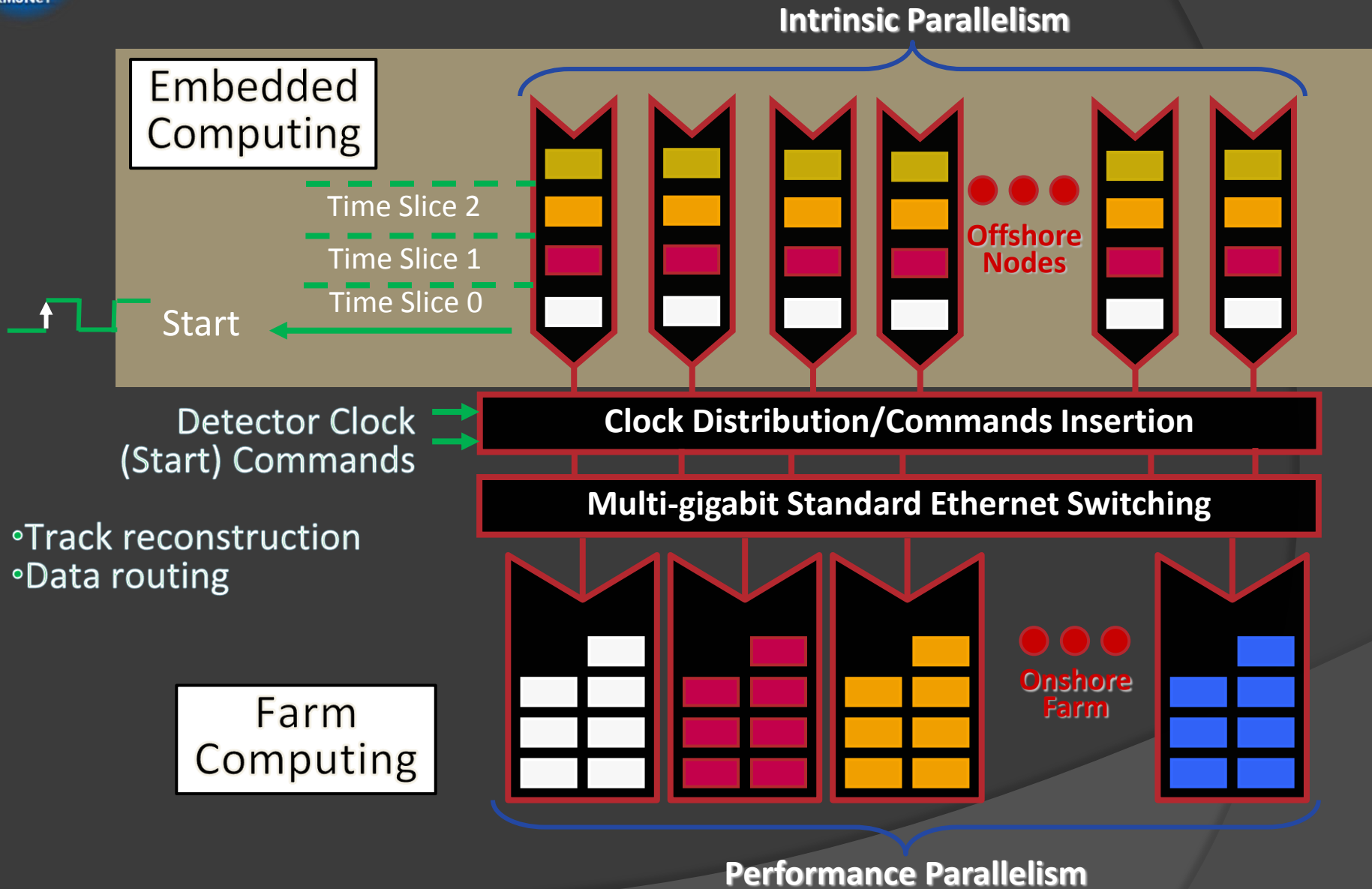
Tx/Rx clock skew measured on shore with DMTD and oscilloscope (ns)

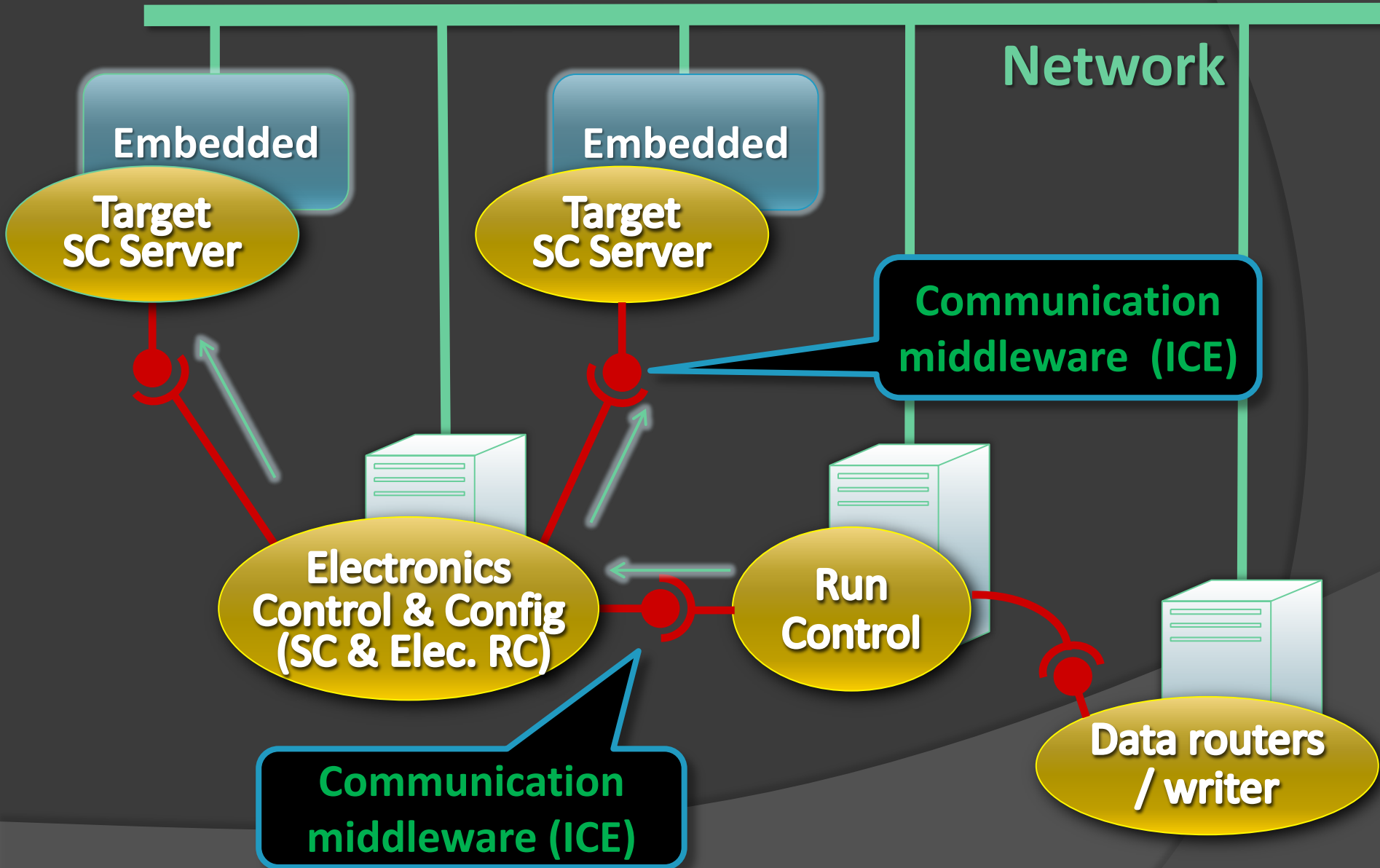
# Store & Forward Acquisition model





# Time Slice Building





# Data acquisition setup

Pulse generator

Synchronous 1000BASE-X ETHERNET

On Shore VIRTEX-6 Board

STANDARD 1000BASE-X ETHERNET

Host PC

- Run Control
- Target Configuration
- Data acquisition
- vxWorks RTOS boot server

RSOC VIRTEX5FX-70

- vxWorks RTOS
- 1 TDC Channel
- 1 dedicated TDC ASIC channel
- PPC440@300 MHz Bus@75 MHz

Start Command  
Reference Clock @62.5 MHz

KDaq Run Control

Configuration

Site Config... SiteCConfig/KoalaBench.xcfg

Data Path... DataNode/AcqData

Data Storage Enabled

Acquisition Control

Disconnect

STANDBY

Reset Init

Configure Start

Stop

Automatic Runs

Run Monitor

Run time 00:01:23

Data (MB) 89.8

Events 3171

Run ID

Run Site KoalaBench

Run Number 00037

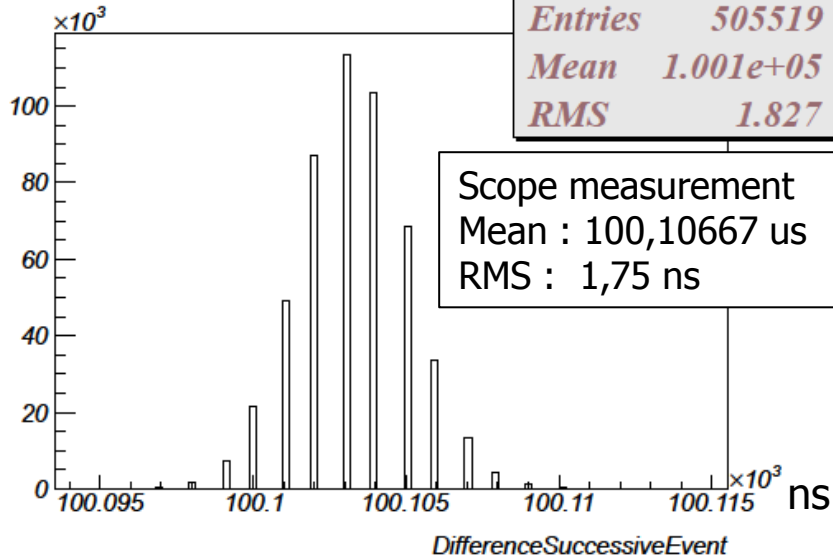
Date & Time 11.03.25@18:06:35

Console

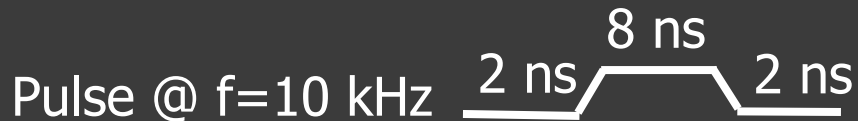
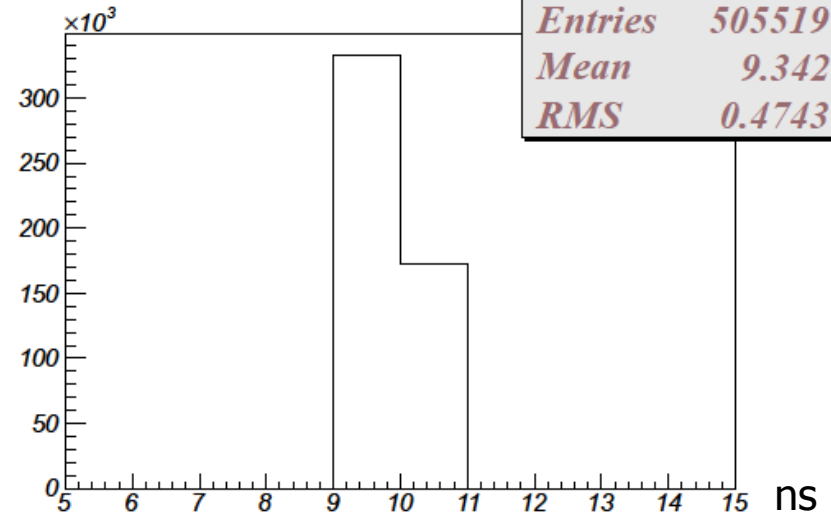
```
>>> Init event from state IDLE: Done
>>> Reset event from state STANDBY: Done
>>> Start event from state READY: Done
>>> Stop event from state RUNNING: Done
>>> Reset event from state STANDBY: Done
>>> All active nodes connected
>>> Init event from state IDLE: Done
>>> Reset event from state STANDBY: Done
>>> Start event from state READY: Done
>>> Stop event from state RUNNING: Done
>>> Reset event from state STANDBY: Done
>>> Init event from state IDLE: Done
>>> Reset event from state STANDBY: Done
>>> Start event from state READY: Done
>>> Stop event from state RUNNING: Done
>>> Reset event from state STANDBY: Done
>>> Init event from state IDLE: Done
>>> Reset event from state STANDBY: Done
>>> Start event from state READY: Done
>>> Stop event from state RUNNING: Done
>>> Reset event from state STANDBY: Done
>>> Start event from state READY: Done
>>> Stop event from state RUNNING: Done
```

# Acquisition results

DifferenceSuccessiveEvent



WidthPatternPulse



Power : 7 W

Current acquisition setup (Source limitation): 60 Mb/s

Standalone measured TCP/IP throughput

PPC440@400 MHz / Bus@100 MHz / WindRiver Zero Copy buffer  
/Jumbo frames : 988 Mb/s

- ◎ Common Readout system functions integrated in a single component (RSOC):
  - Event Time stamping @ 1 GHz
  - Clock and command distribution
  - Slow-control and data acquisition performed in a RTOS multi-tasking embedded system
- ◎ RSOC is a node designed to be plugged in a complete Data acquisition System
  - Server/Client topology (ICE)
  - Scalable system