

TWEPP 2011 Topical Workshop on Electronics for Particle Physics

Tuesday, September 27, 2011

A2 - ASICs - Room EI 7 (2:50 PM - 4:05 PM)

-Conveners: Christophe de La Taille

time	[id] title	presenter
2:50 PM	[91] Development of low power Phase-Locked Loop (PLL) and PLL-based serial transceiver	Mr MORON, Jakub
3:15 PM	[93] SKIROC2, a Front-end Chip designed to readout the E-Cal of ILC	Ms SEGUIN-MOREAU, Nathalie
3:40 PM	[100] Wideband (500 MHz) 16 bit Dynamic Range Current Mode Preamplifier for the CTA cameras	Dr GASCON, David