

## **Session Program**

**26-30 Sept 2011**

# **TWEPP 2011 Topical Workshop on Electronics for Particle Physics**

## ***B3a - Programmable Logic, design tools and methods***

Vienna, Austria

**Vienna University of Technology** Department of Electrical  
Engineering Gusshausstraße 27-29 1040 Vienna, Austria

# Wednesday 28 September

09:50

## B3a - Programmable Logic, design tools and methods

**Session** |

**Location:** Vienna, Austria, Room EI 8, **Vienna University of Technology**  
Department of Electrical Engineering Gusshausstraße 27-29 1040 Vienna, Austria |

**Convener:** Magnus Hansen

09:50-10:15

### A Low-Power Wave Union TDC Implemented in FPGA

**Speaker**

Dr Jinyuan Wu

10:15-10:40

### A 32-Channel High Resolution (<15 ps RMS) Time-to-Digital Converter (TDC) in a Field Programmable Gate Array (FPGA)

**Speaker**

Mr Cahit Ugur

10:40