IFAST IIF PITCH

“HIGHEST”
High-Temperature High-Gradient Superconductors

CERN
CSIC-ICMAB (public research center, ES)
Ceraco (private company, DE)
SLAC as supporting partner

Submitted by:
Sergio Calatroni
Principal applied physicist

Background and aim

- High-temperature superconductors (HTS) were discovered in 1986 and until now have never been used in a high-power microwave device, like an accelerating structure.
- We believe that with this proposal we can push HTS fabrication technology over the last hurdle and demonstrate operation in such a device
- HTS-based accelerating structures would open many opportunities of new, higher-performance, more efficient and more sustainable accelerator rf system
- We have already demonstrated:
  - Successfully applying 2D HTS tapes in 3D geometries
  - Measuring a higher quality factor of these HTS coated RF cavities at low power

➢ We want to develop and optimize a 3D coating technology and demonstrate its scalability to make practical RF high power devices

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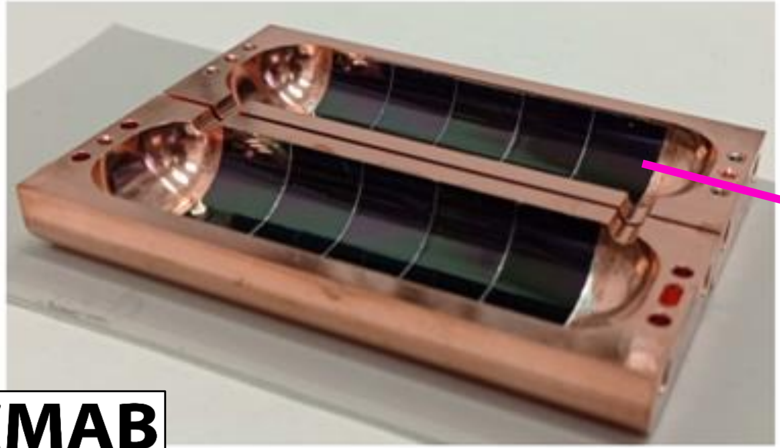
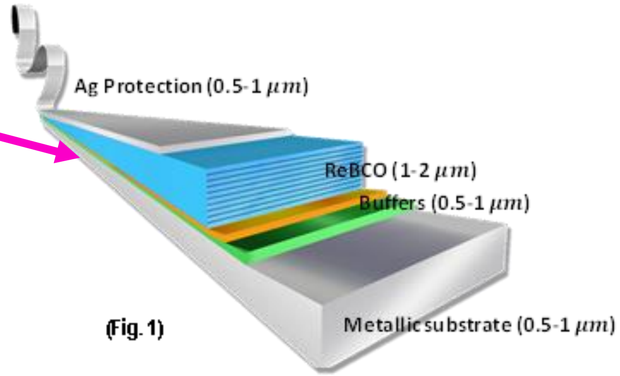
Technical overview - 1

- We have developed a technology for applying 2D HTS tapes to 3D RF “RADES” cavities demonstrating the potential of HTS for RF applications J. Golm et al., IEEE TAS, Vol. 32, No. 4, (2022) 1500605



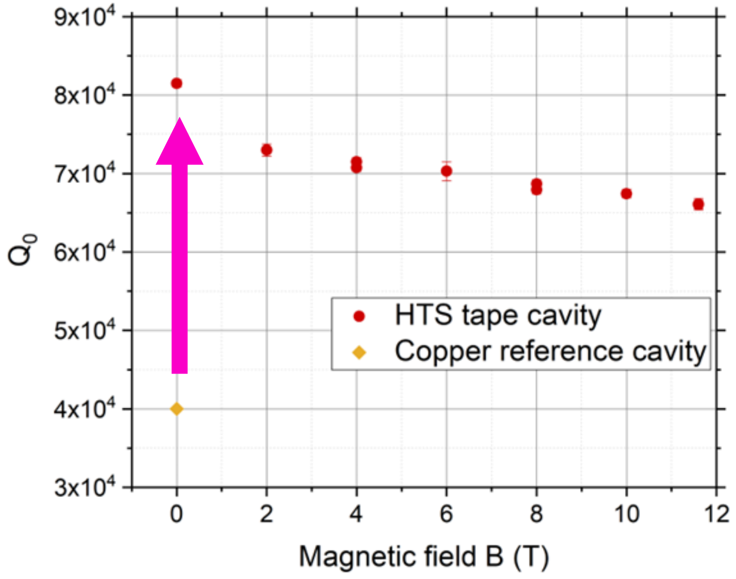
RADES cavity

HTS tapes



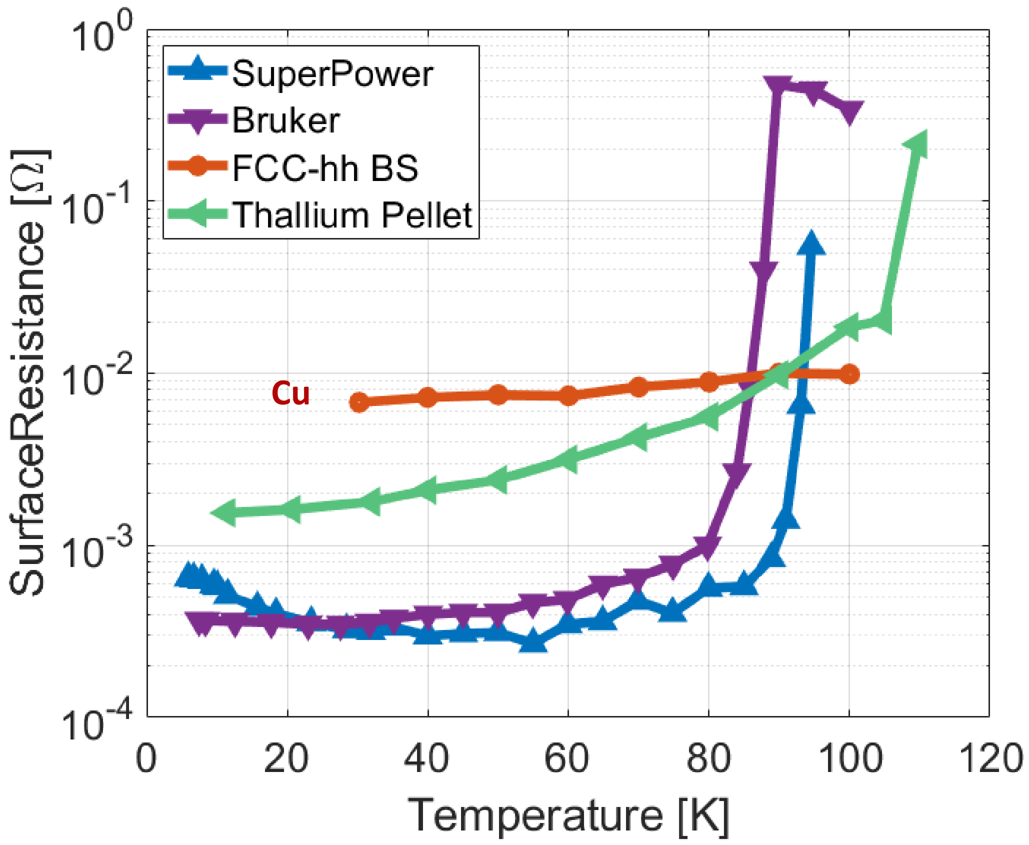
(Fig. 1)

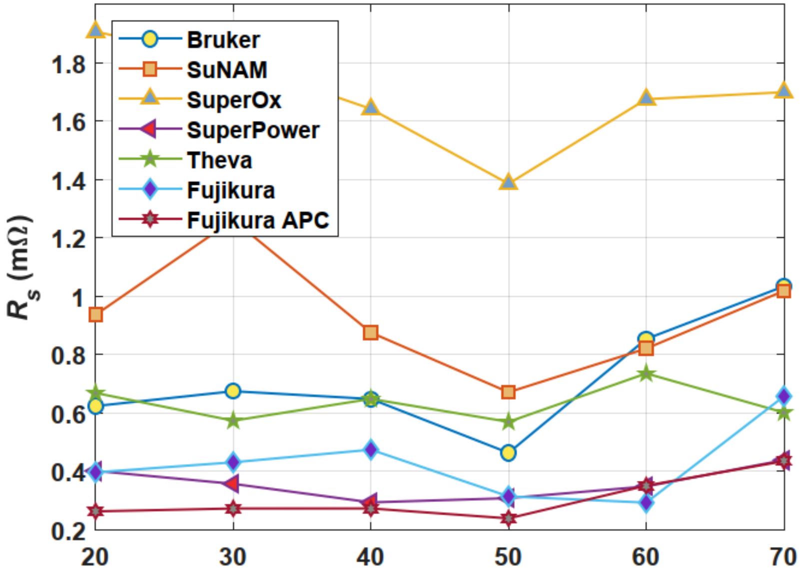
2x improvement of RF quality factor compared to copper
(new prototype 5x improvement)



Technical overview - 2

- A potential improvement at least x10 compared to copper has been measured on samples of tapes (8 GHz) at low RF power

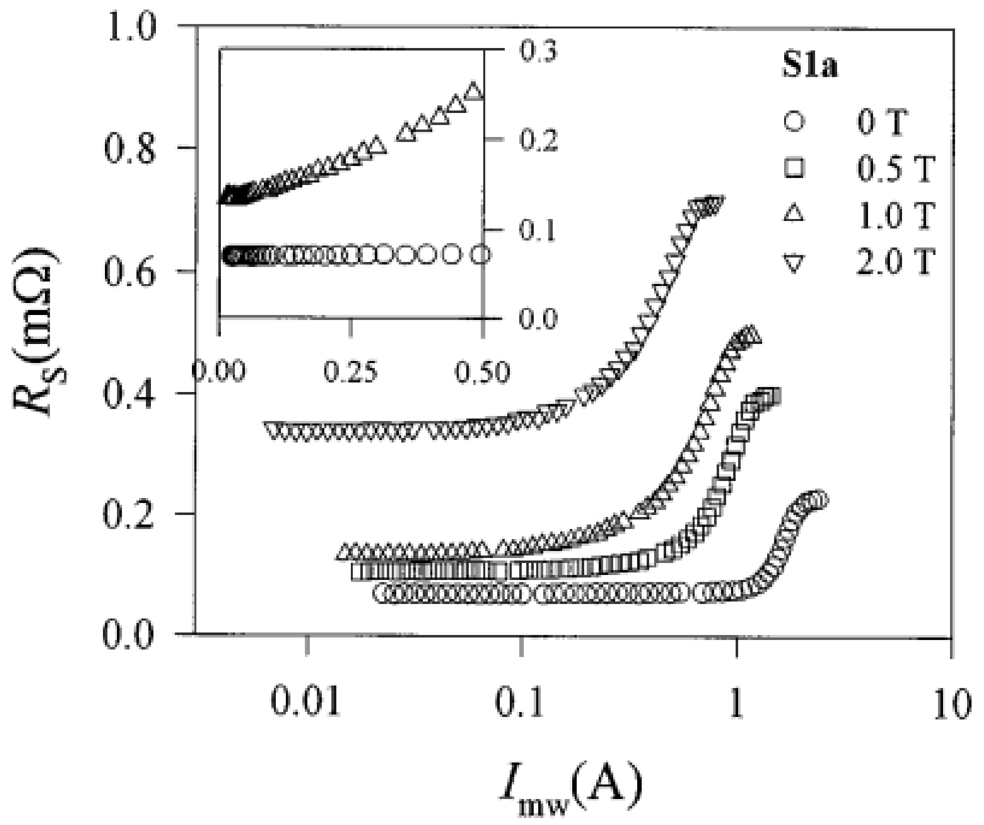




(adapted from Romanov et al, Sci. Rep. (2020) 10:12325)

Technical overview - 3

- There are very few measurements on HTS at high RF currents (mostly microstrip resonators). But physics is proven.



1 A RF equivalent to 2.8x10^6 A/m (microstrip 200 µm, 350 nm thick, 8 GHz, 30 K)
Equivalent to 780 MV/m in an elliptical cavity

Powell et al. Journal of Applied Physics 86, 2137 (1999)

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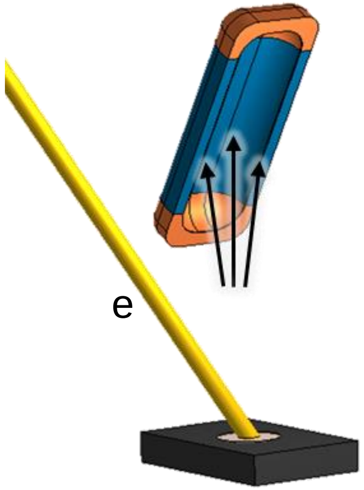
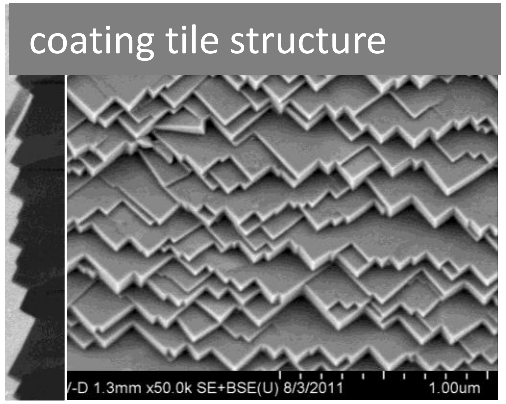
Technical overview - 4

- We have developed first prototypes of 3D direct HTS coating

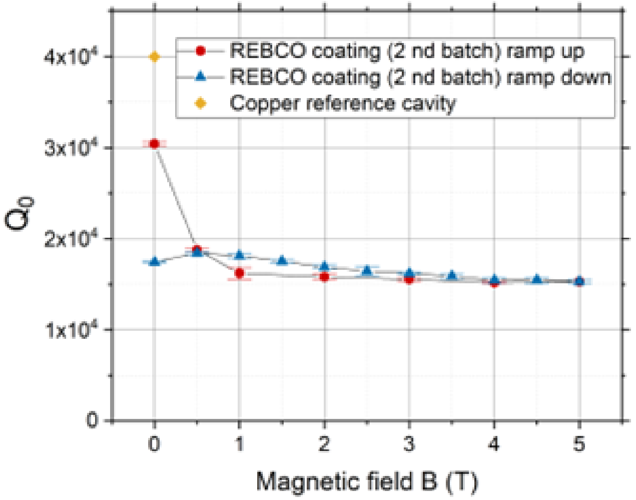
ceraco
ceramic coating

Inclined deposition
technology
(fixed substrate)





(unpublished results)



- ➢ We want to improve direct-3D coating by implementing a substrate rotation and control mechanism, to allow uniform coating.
- ➢ We think that for accelerator, industrial applications this is the road to pursue

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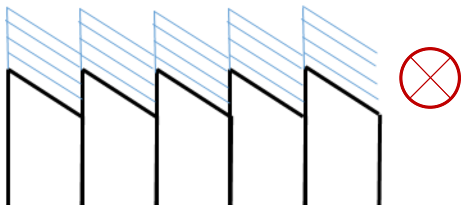
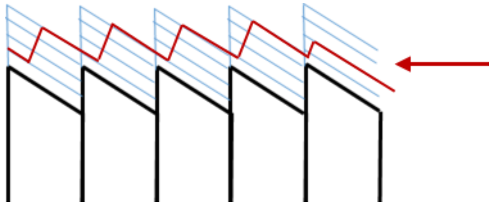
Technical proposal - 1

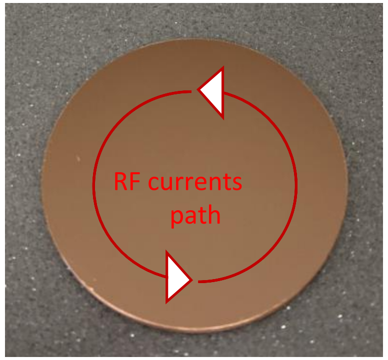
- The coating must reach the cavity surface at ideal angle everywhere
  Proposal for 3D coating technique (Ceraco)



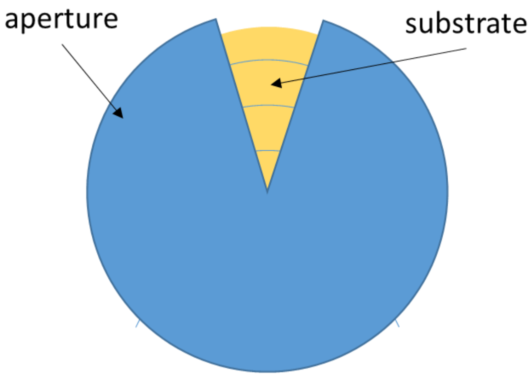
Current flows along tile structure
High critical current density



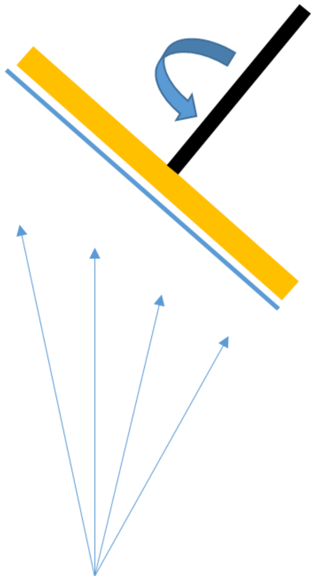
Current flows across tile structure
Low critical current density

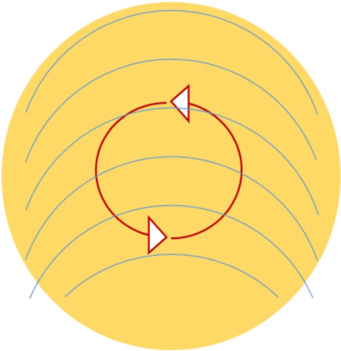


Cu disc, to be coated with HTS and tested at SLAC with “mushroom cavity”

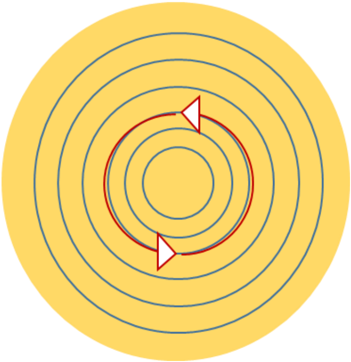


Development of substrate rotation for inclined deposition





No rotation during coating:
RF currents cross the tile structure



With rotation:
RF currents will flow along the tiles

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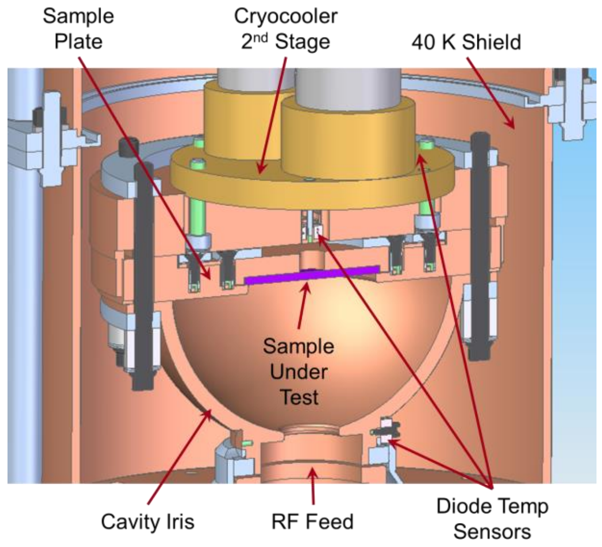
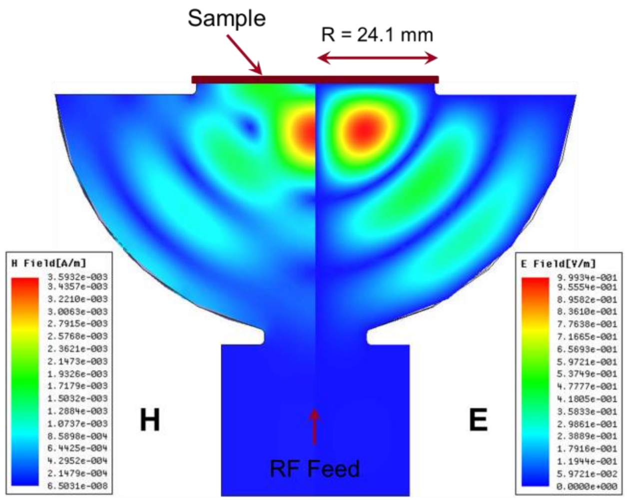
Technical proposal - 2



- Demountable high-power RF cavity
- Cavity O.D. = 142 mm
- Can achieve H_{peak} of about 360 mT using our 50 MW XL-4 Klystron.

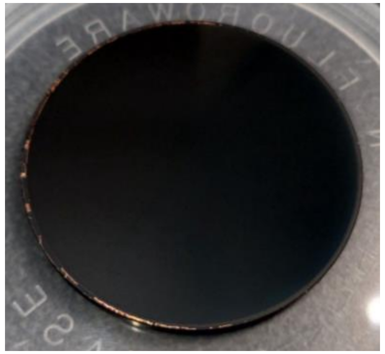
- High-Q X-band hemispheric cavity with a TE_{032}-like mode at 11.4 GHz.
- Zero E-field on the sample
- Maximum H-field on the sample
- Sample accounts for ⅓ of total cavity loss



HTS-coated with tapes
By CSIC-ICMAB

Image here

HTS-coated sample
By CERACO



Technical proposal - 3

- The C[3] study aims at cryogenically cooled copper
- Goal is to combine the advantage of higher gradients at lower temperatures, with the higher Q factor of HTS coatings -> **energy efficiency**

TABLE I. Summary of the accelerating parameters of the distributed-coupling accelerating structure at 300 and 77 K. The peak fields are calculated for an average accelerating gradient of 100 MV/m.

Parameter	300 K	77 K
Frequency (GHz)	11.402	11.438
Q_0	10000	22500
Q_{ext}	10000	10000
Shunt impedance (MΩ/m)	155	349
Peak surface E (MV/m)	250	250
Peak surface H (MA/m)	0.575	0.575
Steady state rf power (MW)	17	9
Iris diameter (mm)	2.6	2.6
Length (cm)	26	26

E. Nanni et al., PRAB 24, 093201 (2021)

Cryoplant efficiency (Carnot + engineering)

SRF temperature	Ratio W_{300K}/W_{cryo}
77 K	13
50 K	20
4.2 K	230
1.9 K	920

Thanks to T. Koettig, CERN

A factor x10 , x20 improvement in Q factor could be the ultimate aim for having relevant energy savings

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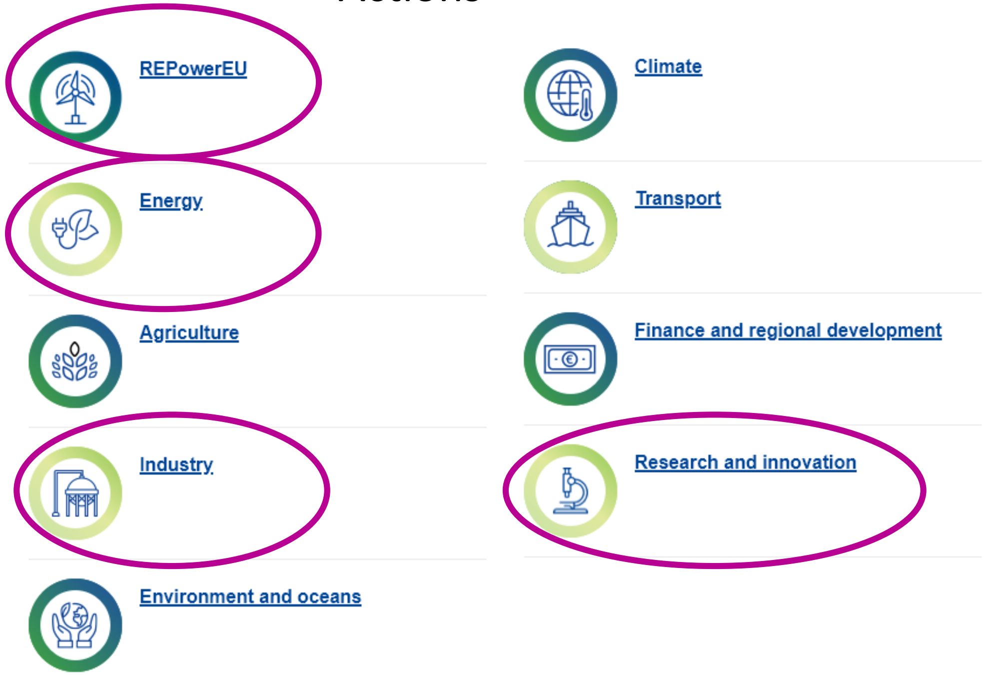
Addressing the European Green Deal

Benefits

Actions



- New-generation collider linacs are expected to use hundreds of GW of electricity

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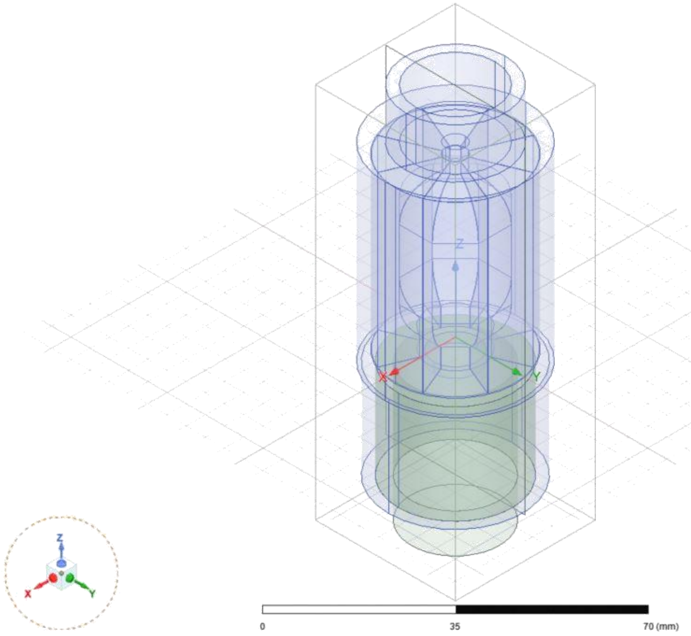
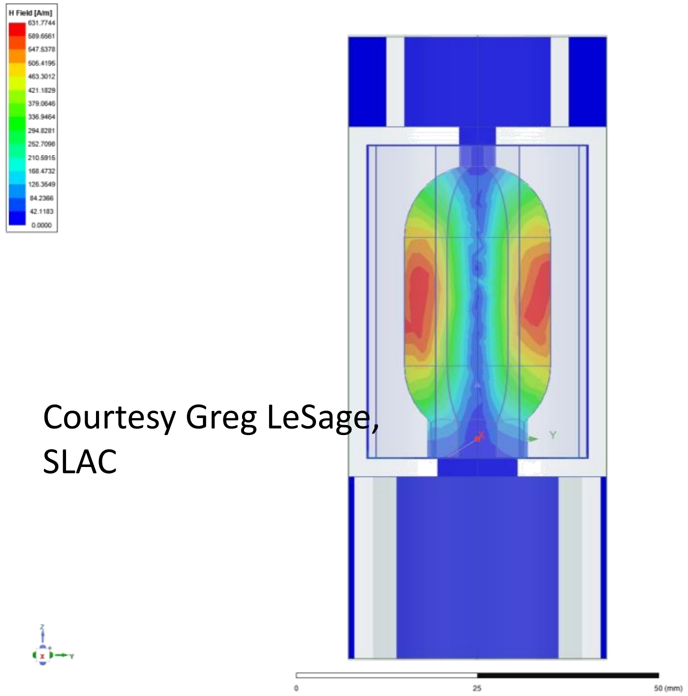
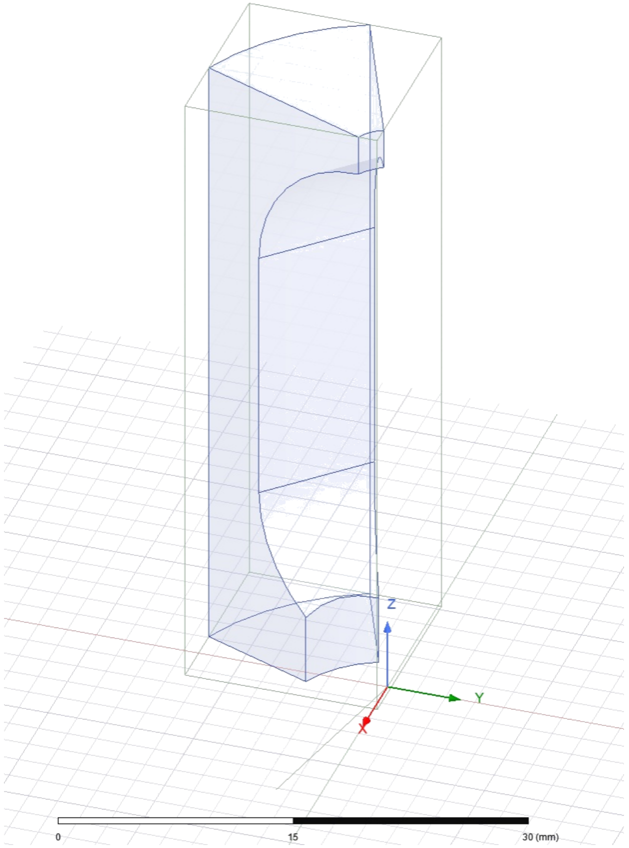
Future view - 1

- Device approach: X-band pulse compressor (SLAC) as first “real” device
- May be coated with tapes by CSIC-ICMAB for device validation
- Direct 3-D coating best option for future large-scale production





Courtesy Greg LeSage, SLAC



Max field surface 3.126 MA/m

Future view - 2

- How could it be done in practice in a future cavity? Bimetallic cavities

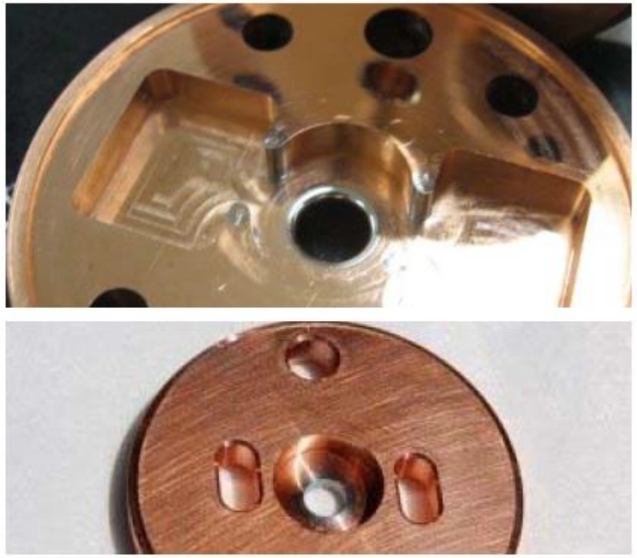
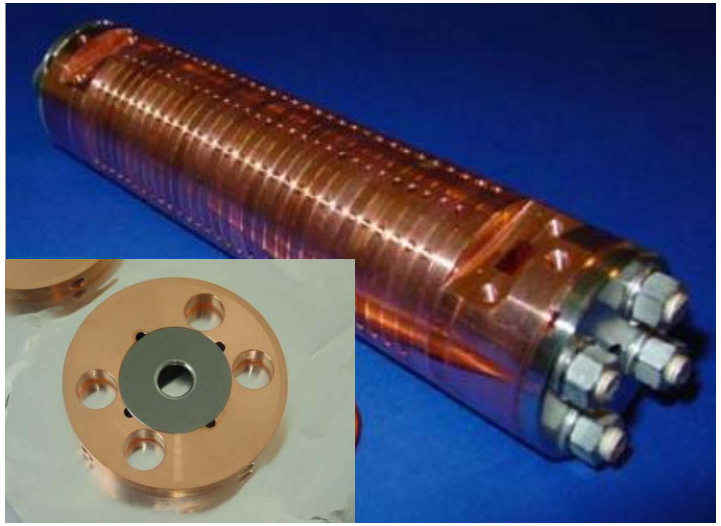
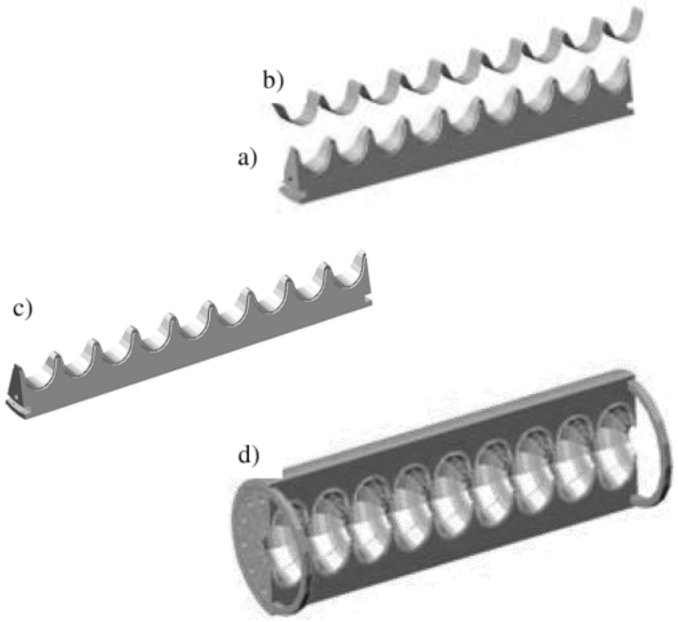


Figure 7: High temperature brazed stainless steel inserts and copper heat sinks shown in the racetrack input coupler, and a typical iris insert cavity, prior to final machining.

J. Haimson, WEPMS085
Proceedings of PAC07



CLIC Mo-iris prototype



P. McIntyre et al., IEEE TAS 19 (2009) 1380

Work plan and risk mitigation

	Q1	Q2	Q3	Q4	Q5
WP 1 (CERN)					
Coordination activities					
Samples and substrates procurement		M1			
RF low power characterization of 2D coated segmented cavities				D1	
Final report					D2
WP 2 (Ceraco)					
Design and fabrication of sample holder rotation system		M1			
HTS 3D coating of disc samples				D1	
WP3 (CSIC-ICMAB)					
2D coating on discs and segmented cavities for benchmarking			D1		
Measurement of superconducting properties of 3D HTS coatings					D2
SLAC supporting partner					
RF high power characterization of 3D coated HTS discs in their mushroom cavity					

Risk	**Mitigation**
Segmented test cavity delayed	HTS optimization studies to be performed with older “RADES” cavities
Quality of 3D coatings on discs	Measurement of superconducting (non-RF) properties at ICMAB will help in optimization work
High power characterization at SLAC delayed	Preliminary low-power characterization is possible with standard laboratory devices

Applications, impact, business and commercialization

- The accelerator sector will be the primary target of our work.
  - Future high-energy or high-power linacs
  - Compact, low-power consumption linacs for medical devices, light sources
- The HTS technology will be applied at industrial scale
  - IP ownership is within CERN and CSIC-ICMAB for 2D coatings with commercial tapes.
  - IP ownership will be within Ceraco and CERN for 3D coating technology. This technology can be patented (HTS 3D coating technology and methods), and IP rights commercialized
- Our next step will be to launch a new round of fund raising for manufacturing a prototype of a real-size high power RF device
  - This will include the design, fabrication and coating, and its validation in a high-power RF bench test bench.
  - We have the interest and support of a major player in RF technology as SLAC, who will design and test the prototype
- A proper business and commercial plan will be finalized upon successful completion of the present study

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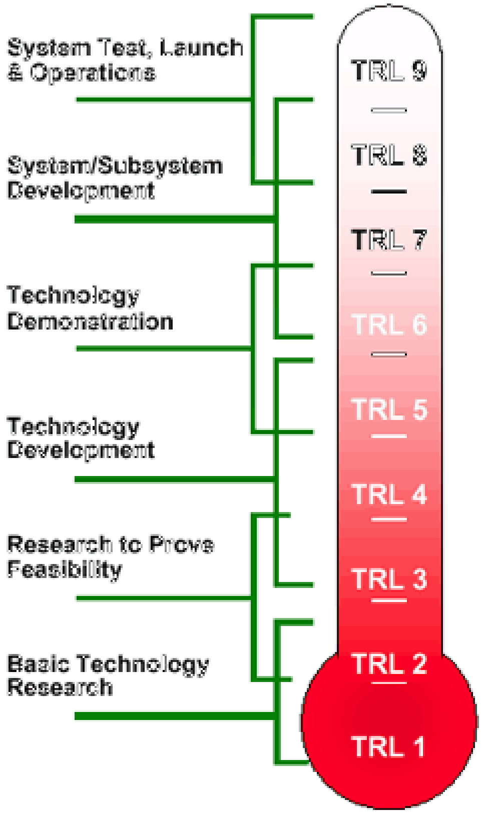
Industrial application prospect

- At the end of this study, we aim at consolidating TRL4.
- Prototype pulse compressor with SLAC will demonstrate TRL6.
  - Timescale: 2-3 years after completion of this study
  - Need a further round of funding
  - This will include the design, fabrication and coating, and its validation in a high-power RF bench test bench.
  - Industry will be involved for 3D coating
- Future accelerator projects will drive achieving further TRLs and drive commercialization.
  - Industry will be involved for construction of devices
  - Other companies may be involved for hardware manufacturing



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Resources and budget

- CERN:
  - Provided resources: two senior physicist (scientific coordination, 0.2 FTE) and one senior Fellow (follow up, measurements, 0.5 FTE)
  - Requested resources: 10 kEUR (sample manufacturing)
- Ceraco:
  - Provided resources: one senior scientist (design, procurement, coating, 1 FTE)
  - Requested resources: 100 kEUR (80 kEUR manpower for coating operations, 20 kEUR sample holder manufacturing)
- CSIC-ICMAB:
  - Provided resources: one senior scientist (0.2 FTE), and one PhD student (0.5 FTE)
  - Requested resources: 50 kEUR (40 kEUR PhD student and manpower for coating and characterization work, 10 kEUR consumable)

Ratio for the requested IIF funds: 120 kEUR personnel and labour / 40 kEUR material

- Final deliverable is a report on the demonstrated achieved performance, and on the prospects for scalability to accelerator-scale RF devices.

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Budget table

	Manpower	Materials	Total
CERN		10 kEUR	10 kEUR
CERACO	80 kEUR	20 kEUR	100 kEUR
CSIC-ICMAB	40 kEUR	10 kEUR	50 kEUR
			160 kEUR

Contact information

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14 September 2022

Dear Sergio,

It is my pleasure to write this letter in support for your iFAST proposal "High-Temperature High-Gradient Superconductors."

At SLAC we are interested in the development of high temperature superconductors for a variety of applications including dark matter searches, quantum devices, and accelerator applications. Extending the reach of superconducting technology to high-gradient and high-temperature is particularly exciting as it may open new avenues for developing rf pulse compressors and superconducting rf accelerator cavities, even up to liquid nitrogen temperatures (~80 K). Development of accelerator technology operating at cryogenic temperature of ~80 K is a significant part of our ongoing R&D efforts at SLAC.

In your proposal, the approach you are developing to apply 2D high temperature superconducting (HTS) tapes to 3D geometries has produced exciting preliminary results and further development is critical for developing HTS based accelerator technology. We would gladly collaborate on the testing of HTS samples produced as part of your program. Our recent collaborative effort to test a first HTS sample at SLAC has shown that the infrastructure exists and is available at SLAC to participate in this work. If this work were to proceed to a second phase we would also be excited to jointly develop a full-scale prototype high-power device and test it at SLAC with an X-band high power rf source.

Again, I wish you success on your proposal and look forward to working with you and your team in the future on the development of high-temperature high-gradient superconductors.

Sincerely,

Emilio A. Nanni, Ph.D.

SLAC NATIONAL ACCELERATOR LABORATORY • 2575 SAND HILL ROAD • MENLO PARK • CALIFORNIA • 94025 • USA
Operated by Stanford University for the U.S. Department of Energy

Questions from IIF panel

- ✓ Could you express in quantitative and verifiable terms the improvements with respect to the state-of-the-art that your project is pursuing.
- ✓ Can the impact of HTS-based accel. structure on accelerator RF systems be better illustrated in quantitative terms ?
- ✓ The most important parameters to be measured in the characterization, both at low power and at high power, should be described and target values aimed at should be defined and compared with state-of-the-art figures.
- ✓ Can the sentence "The HTS technology will be applied at industrial scale" be explained in terms of the time scale and the possible level of industrial engagement ?
- ✓ Is it already clear what results of the R&D work can be patented ?

- The budget should be presented in Euro
- The schedule should extend not later than March 2025 (which is 2 months before end of IFAST)
- In your presentation we request that at least 1 slide is reserved to expected impact of your project also in term of addressable and obtainable market, and to make a definite statement on how your project addresses the environmental challenges.
- The precise TRL: where it stands, where you think it will be at the end of the project and the estimated time in the future to reach TRL6.
- EvB will ask details about the technical aspects of the proposal.