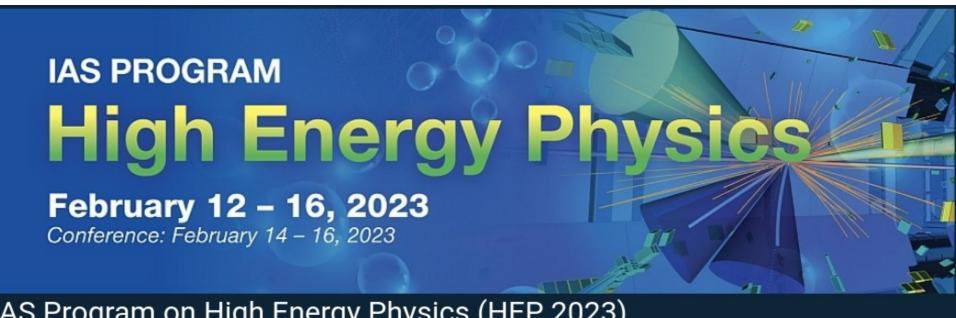


Status and Progress of the IDEA Detector Concept

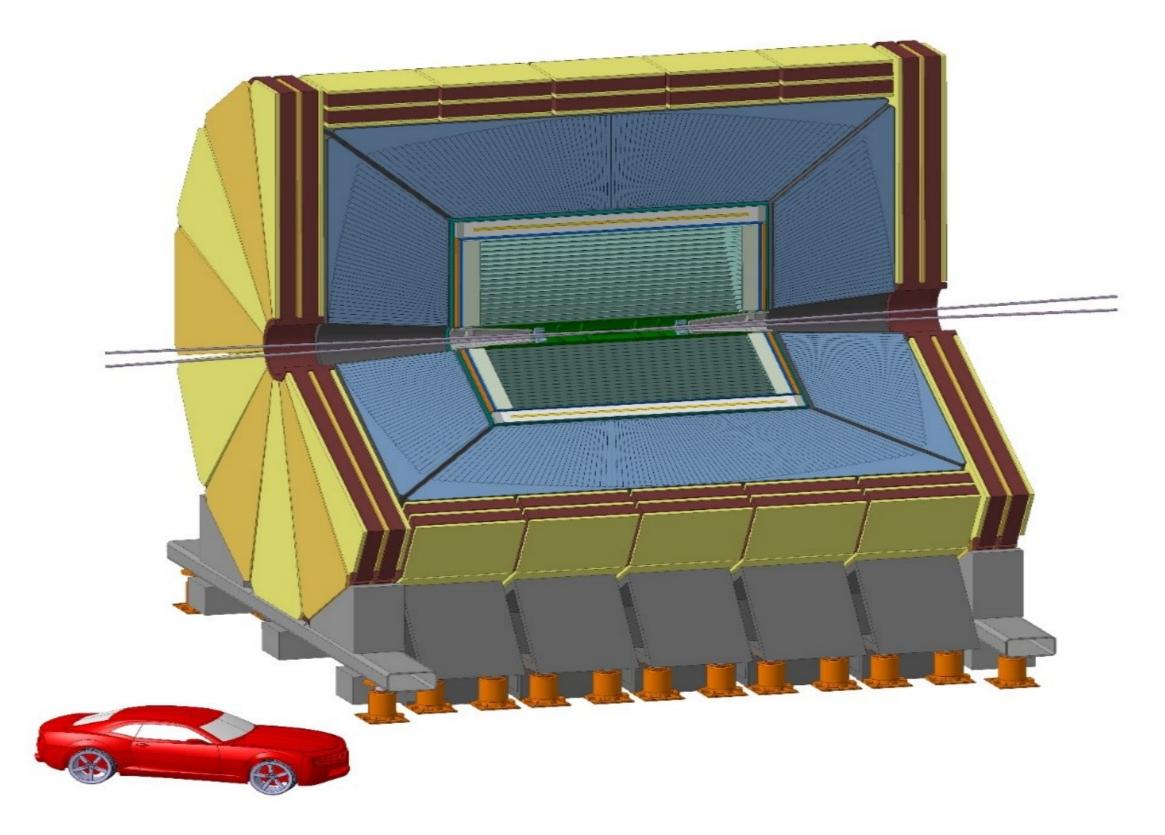
Roberto Ferrari INFN Pavia on behalf of the IDEA proto-collaboration

Mini Workshop: Experiment and Detector Hong Kong 13.02.2023



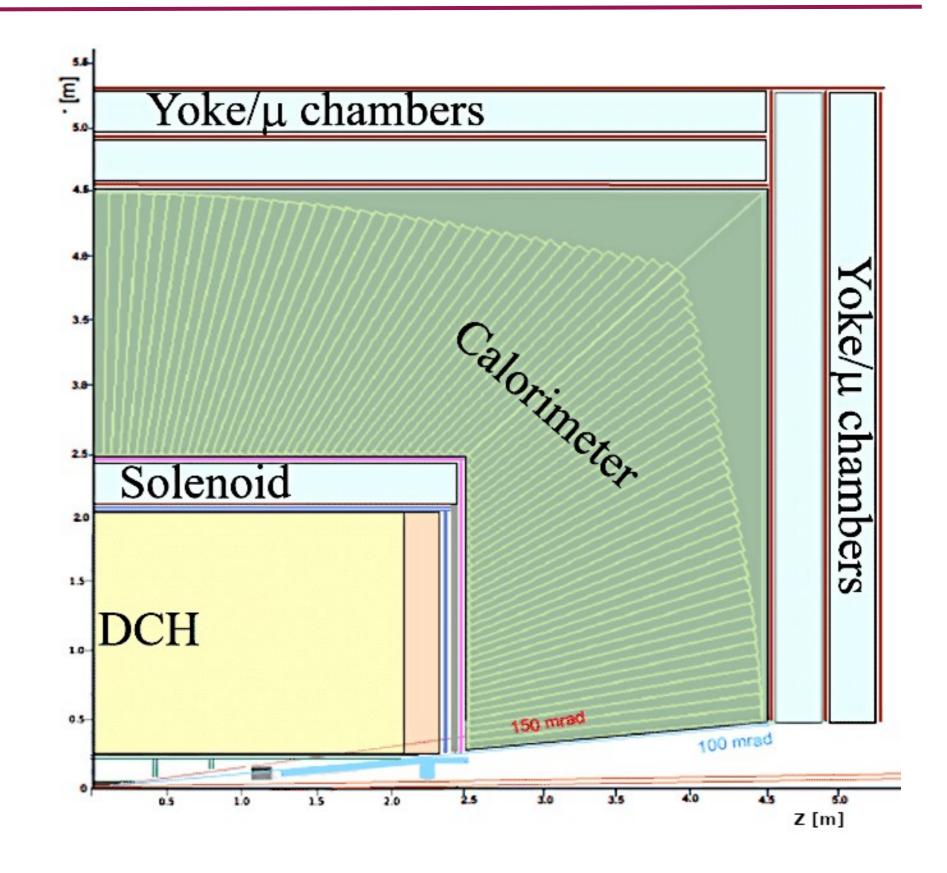
IAS Program on High Energy Physics (HEP 2023)

IDEA: Innovative Detector for e+e- Accelerator



IDEA concept

- Muon chambers
- Dual-readout calorimetry 2 m / 7 λ_{int}
- Thin superconducting solenoid
 - + 2 T, 30 cm, ~ 0.7 X_0 , 0.16 λ_{int} @ 90°
- Transparency for tracking
 - Si pixel vertex detector
 - Drift Chamber
 - Si wrappers (strips)
- ✦ Beam pipe: r ~ 1.5 cm

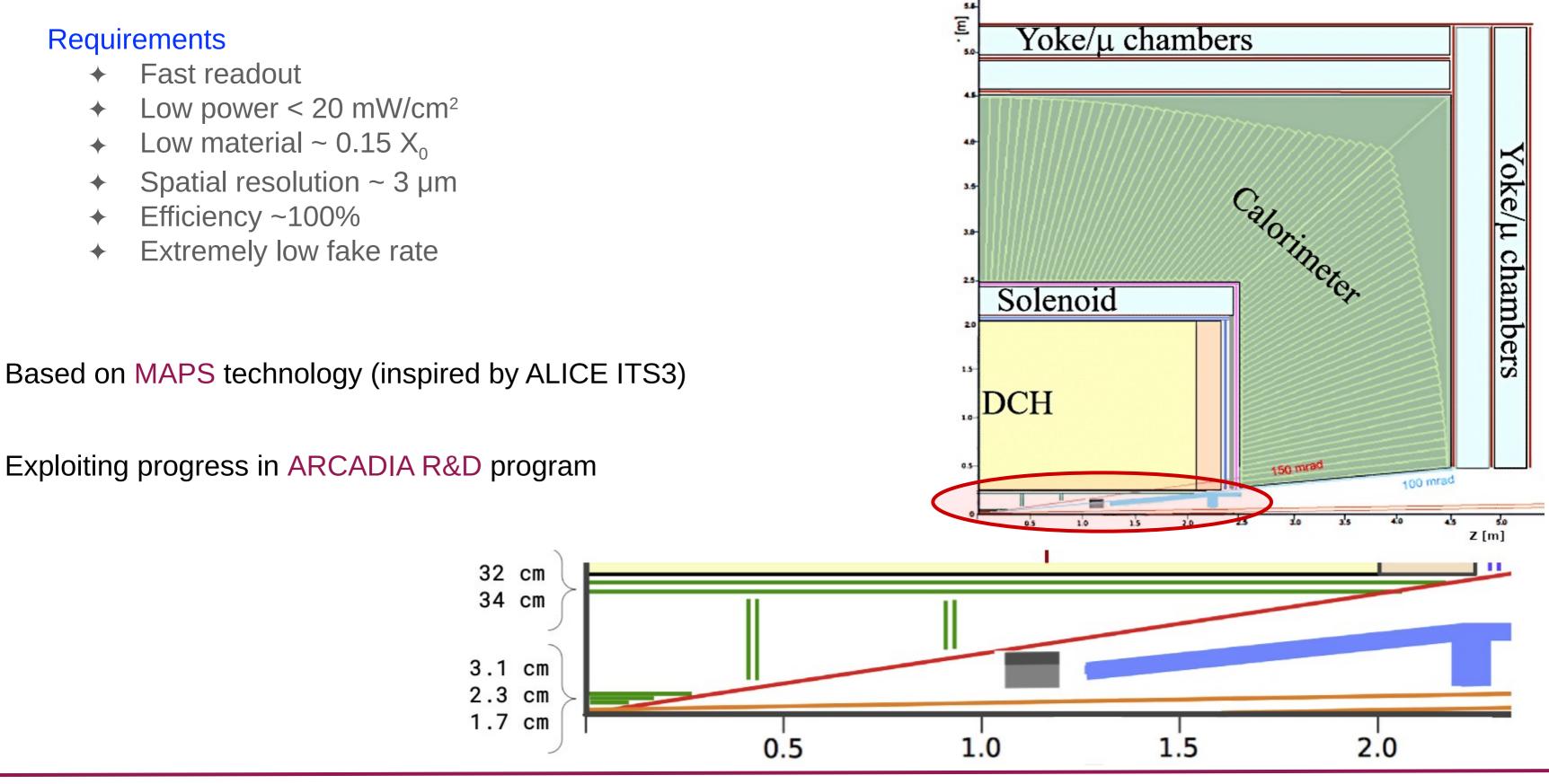


+ $\Delta(1/p_T)$

- high precision measurement at end of tracker
- σ_{rΦ}
 - finely segmented vertex detector
- Challenging requirements for detector materials

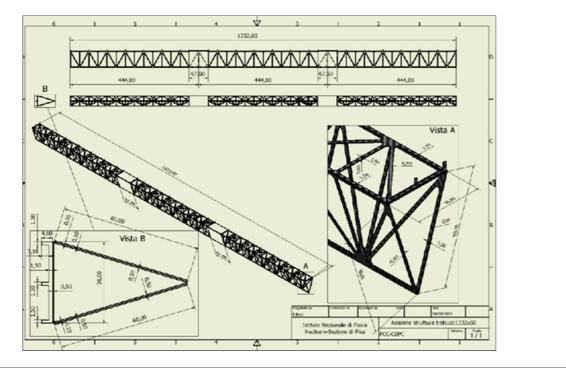
Physics process	Measurands	Detector subsystem	Performance requirement
$ZH, Z \rightarrow e^+ e^-, \mu^+ \mu^-$ $H \rightarrow \mu^+ \mu^-$	$m_H, \sigma(ZH)$ $\mathbf{BR}(H \to \mu^+ \mu^-)$	Tracker	$\Delta(1/p_T) = 2 \times 10^{-5} \oplus \frac{0.001}{p(\text{GeV}) \sin^{3/2} \theta}$
$H \rightarrow b\bar{b}/c\bar{c}/gg$	$BR(H \rightarrow b\overline{b}/c\overline{c}/gg)$	Vertex	$\sigma_{r\phi} = 5 \oplus \frac{10}{p(\text{GeV}) \times \sin^{3/2} \theta} (\mu \text{m})$
$H \rightarrow q\bar{q}, WW^*, ZZ^*$	$\begin{array}{c} BR(H \to q\bar{q}, \\ WW^*, ZZ^*) \end{array}$	ECAL HCAL	$\sigma_E^{\text{jet}}/E = 3 \sim 4\%$ at 100 GeV
$H \to \gamma \gamma$	$\mathrm{BR}(H \to \gamma \gamma)$	ECAL	$\frac{\Delta E/E}{\frac{0.20}{\sqrt{E(\text{GeV})}} \oplus 0.01}$

Vertex detector

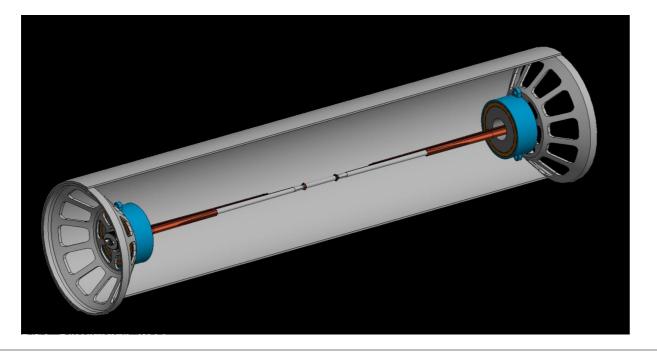


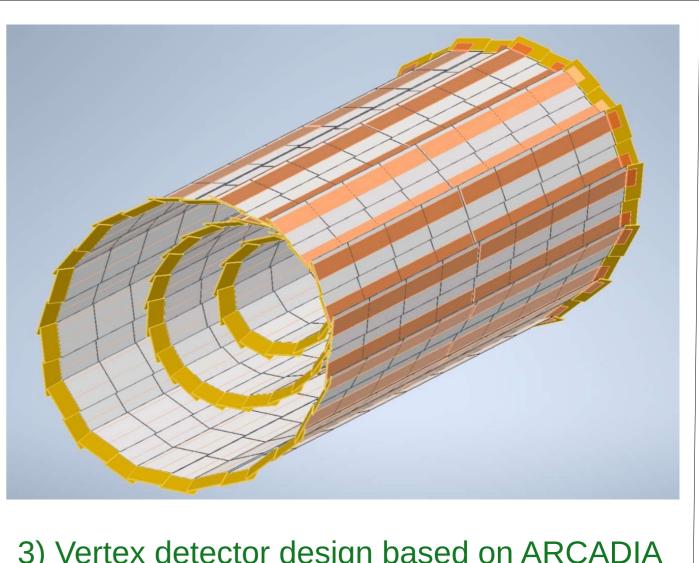
Vertex detector





2) MDI integration: engineered design for pipe and vertex support



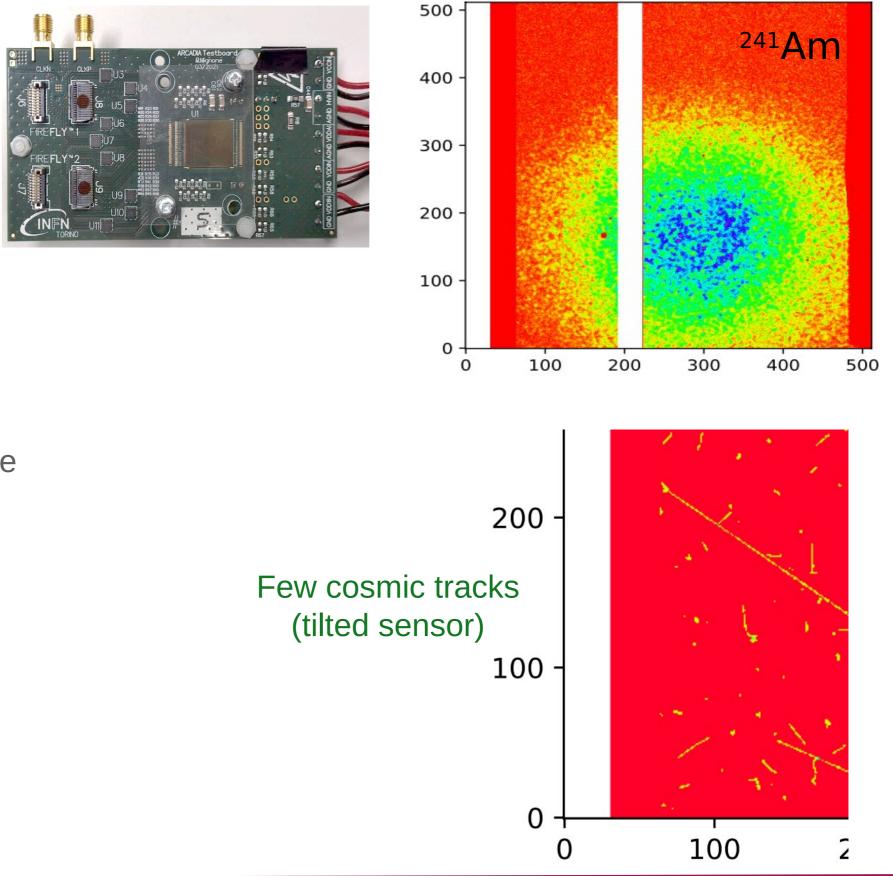


3) Vertex detector design based on ARCADIA

Arcadia project

CMOS DMAPS Platform

- INFN project, w/ CH and China
- Part of EU AIDAInnova project \blacklozenge

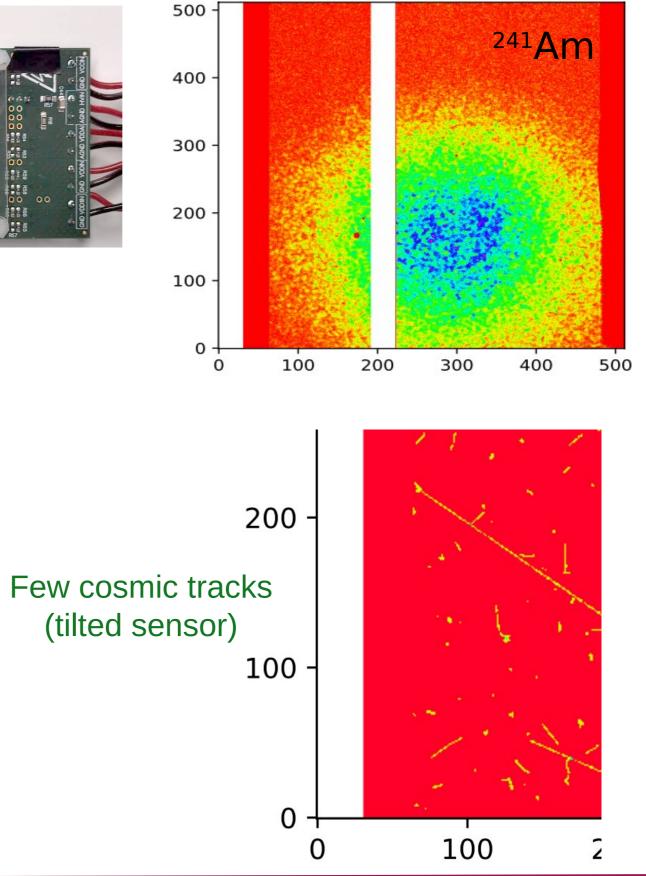


ARCADIA-MD1: 25×25 µm² pixels

- Tested sensor and back-side processing ✦
- Readout architecture charact. ongoing \blacklozenge
- 110 nm CMOS CIS technology \blacklozenge
- High-resistivity bulk, operated in full depletion mode +
- Matrix = 512×512 pixels \blacklozenge
- Thickness = 200 μ m \blacklozenge

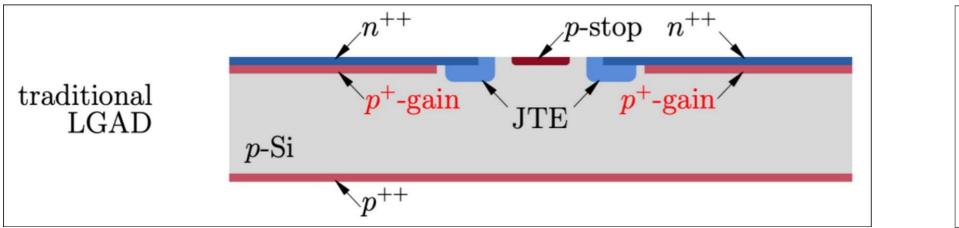
ARCADIA-MD2 submitted in summer 2021

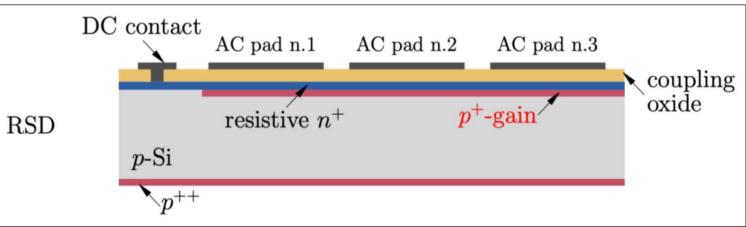
design and architecture improvements targeting \blacklozenge power reduction and scalability



Vertex detector – LGAD RSD

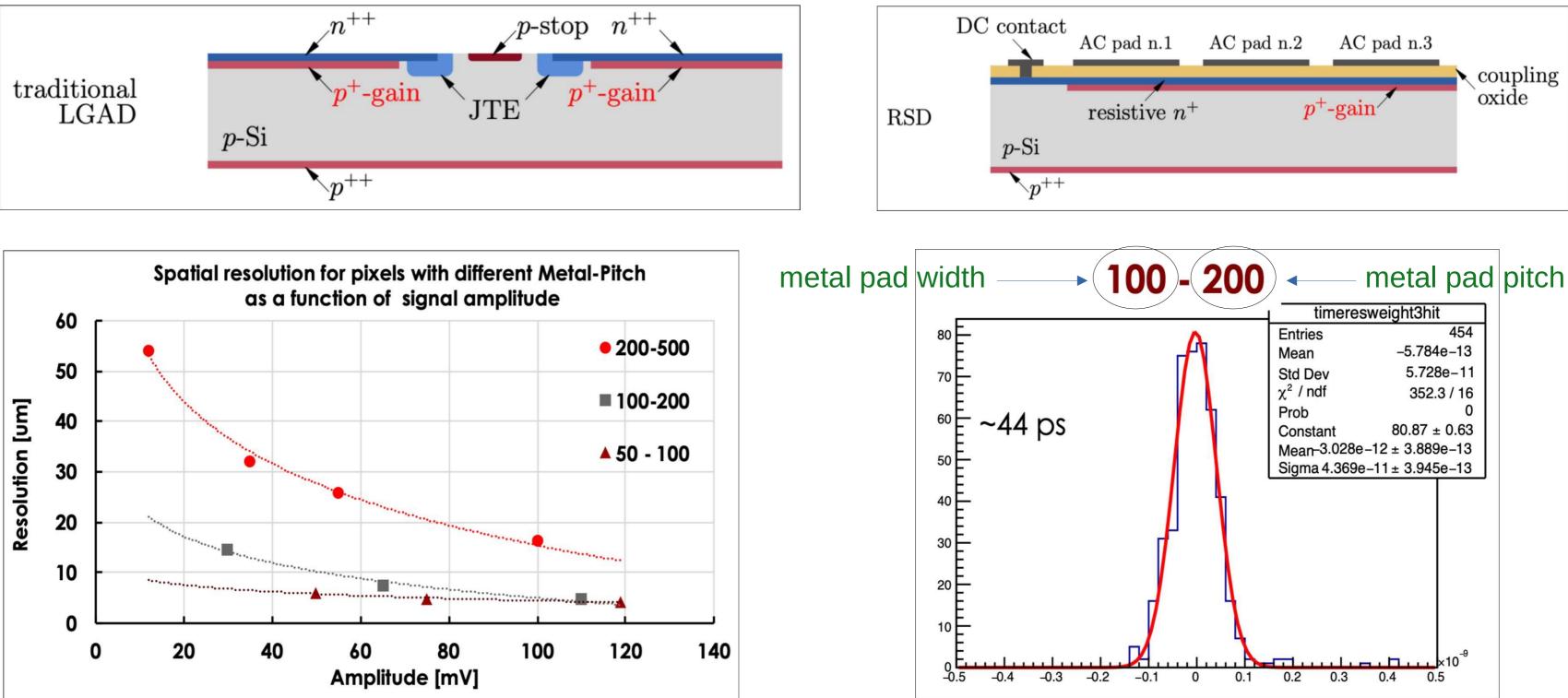
New activity on LGAD Resistive Silicon Detector: high segmentation, excellent position and time resolution

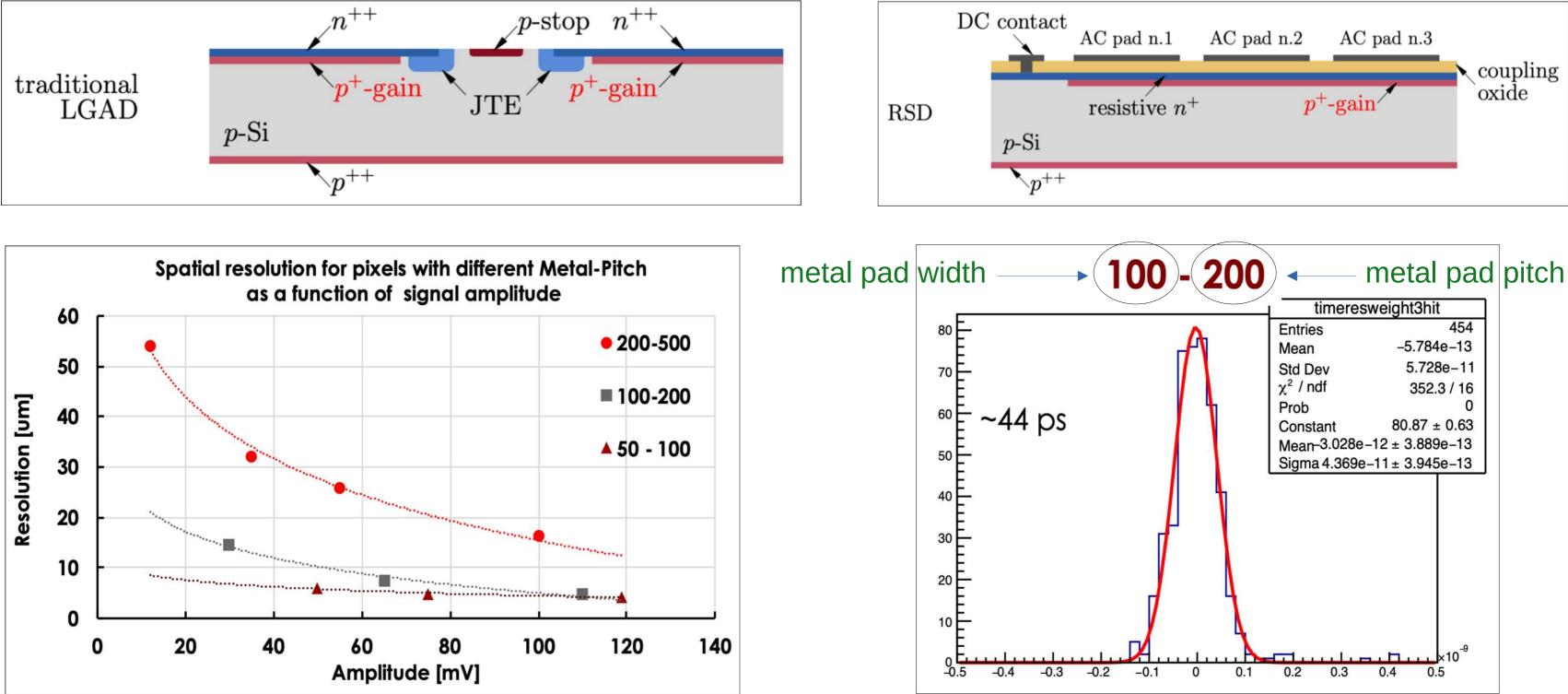




Vertex detector – LGAD RSD

New activity on LGAD Resistive Silicon Detector: high segmentation, excellent position and time resolution





Drift chamber

Compromise between granularity and transparency

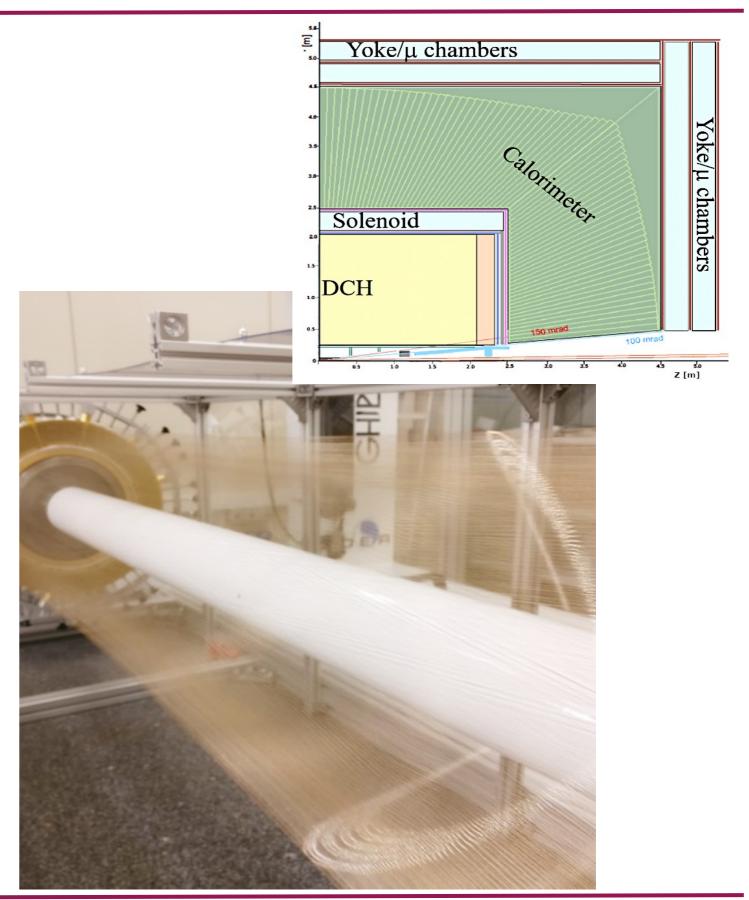
- High momentum resolution
- Ultra light detector
- Assisted by Si wrappers

Dimensions

- ★ L = 400 mm
- ♦ R = 35 ÷ 200 cm
- ✤ Total thickness: 1.6% X₀ at 90°
 - Tungsten wires dominant contribution
- ✤ 112 layers for each 15° azimuthal sector

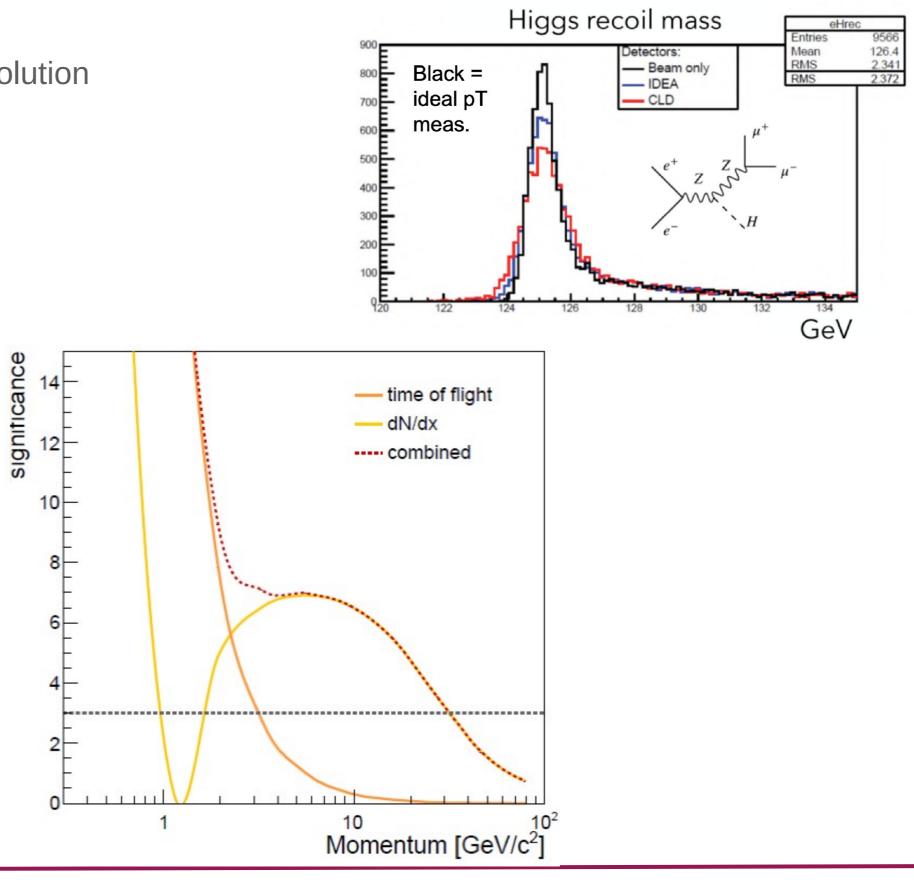
Inherits from previous DCHs

KLOE and MEG II



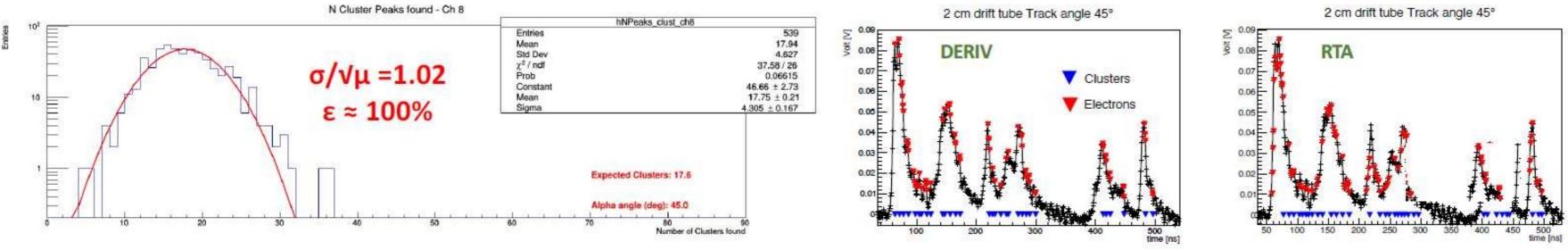
Drift chamber: Particle ID with cluster counting

- Tracks w/ rather low momenta ($p_T ≤ 50 \text{ GeV}$)
 - Transparency more relevant than asymptotic resolution
- + He based gas mixtures (He / $iC_4H_{10} = 90 / 10$)
 - ionisation signals last few ns
 - max drift time: 350 ns
- Fast readout (~GHz sampling)
- PID by counting dN_{cl}/dx
 - # of ionisation acts per unit length
 - better PID resolution than dE/dx
- 0.75 < p < 1.05 gap recoverable with timing layer
 - 100 ps sufficient for 3σ K/p



Drift chamber: results on cluster counting

- Successful beam test in July 2022 \blacklozenge
 - cluster counting works \rightarrow analysis ongoing \blacklozenge
 - collaboration with Chinese groups on \blacklozenge algorithm development

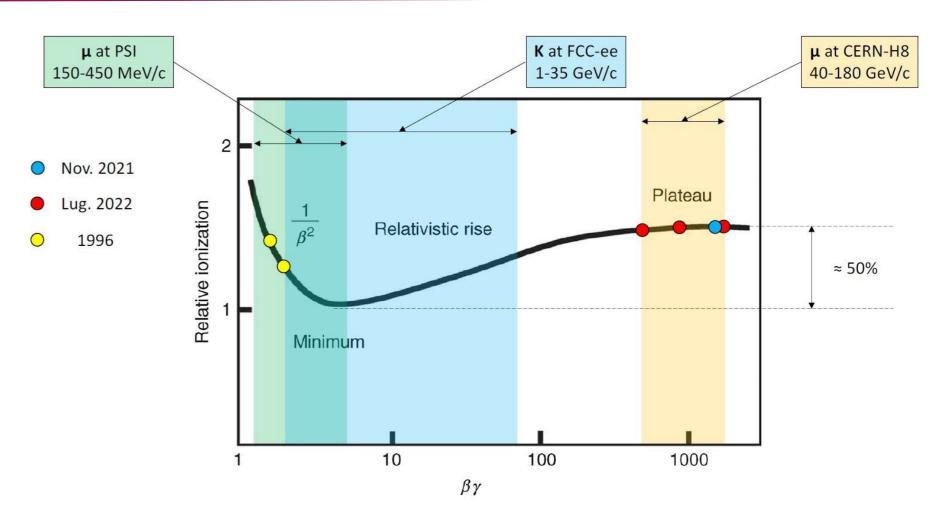


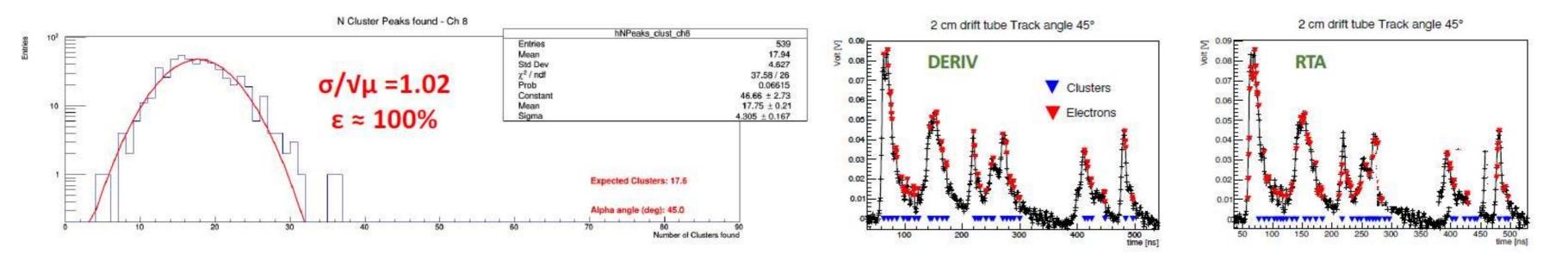
Hong Kong, 13.02.2023



Drift chamber: results on cluster counting

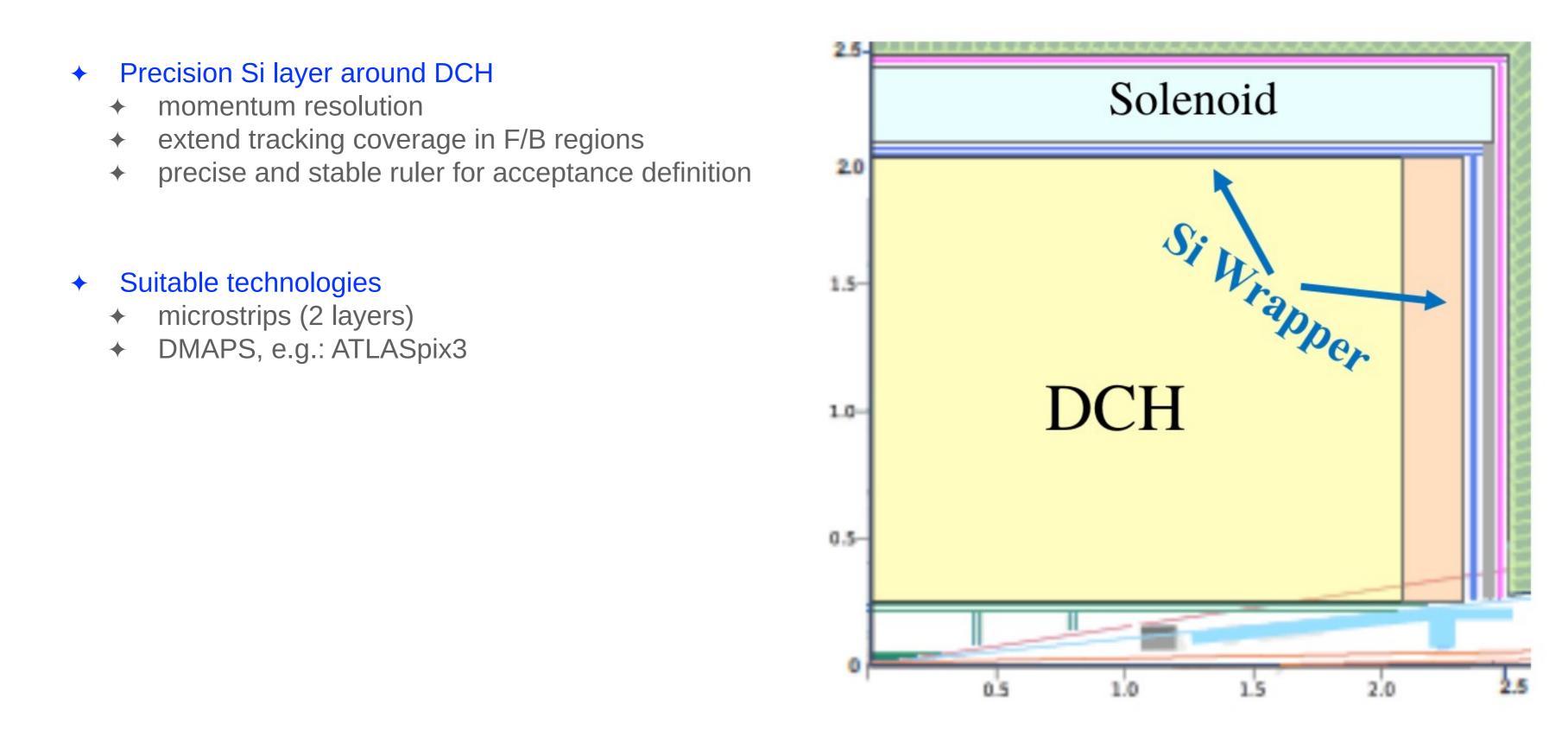
- Successful beam test in July 2022
 - cluster counting works \rightarrow analysis ongoing
 - collaboration with Chinese groups on algorithm development
- Searching best place for addressing in 2023 relativistic rise region





13

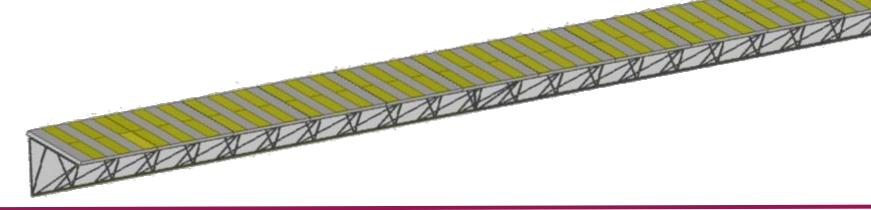
Si wrappers

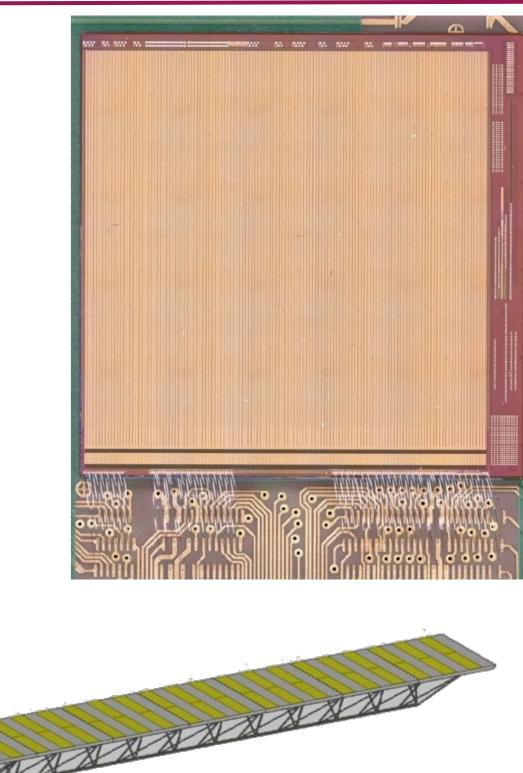


Si wrappers: ATLASpix3

Pixel size: 50×150 µm² $\mathbf{+}$

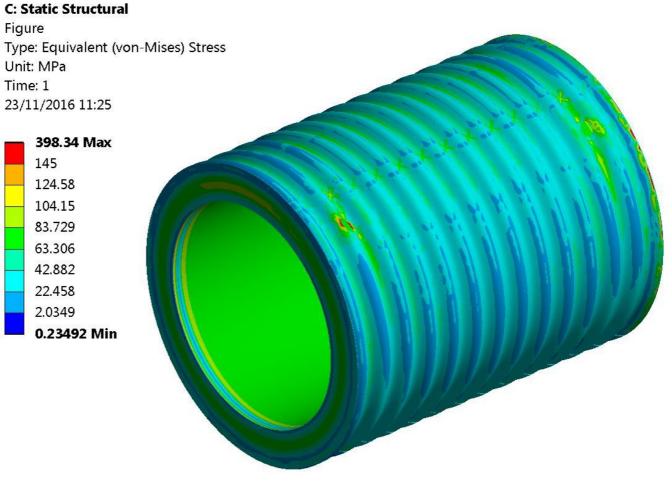
- 150 mW/cm²
- both triggerless and triggered readout \blacklozenge
- first 2 multi-chip modules successfully operated \blacklozenge
- Target: build few mini-staves of outer tracker for FCC-ee/CepC
 - 2022: realisation of prototypes and thermo-mechanical characterisation \blacklozenge

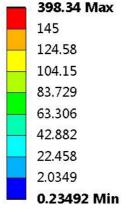


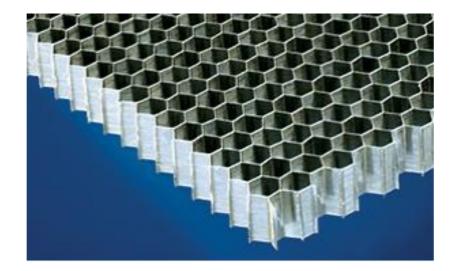


Superconducting solenoid

- Ultra light 2 T solenoid: ✦
 - 30 cm radial envelope \blacklozenge
 - single-layer self supporting winding (20 kA) \blacklozenge
 - cold mass: 0.46 X₀, 0.09 λ_{int}
 - Vacuum vessel (25 mm Al): 0.28 X₀ \blacklozenge
 - can improve with new technology
 - corrugated plates: 0.11 X₀
 - honeycomb: 0.04 X₀









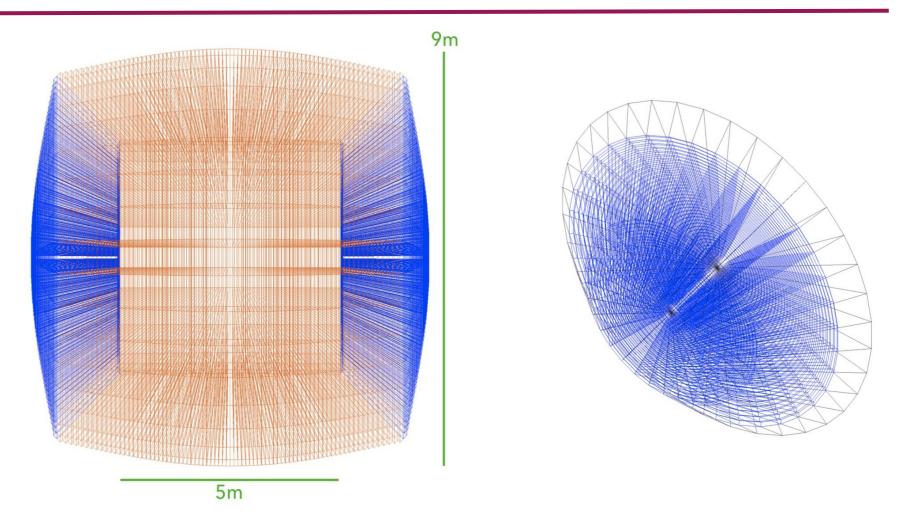
Courtesy of H. TenKate

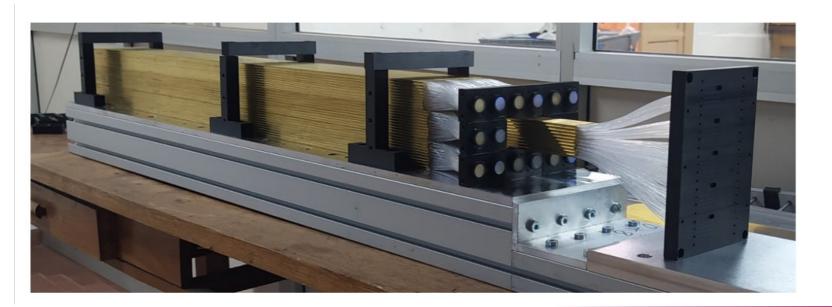
- fair $\sigma_{\rm EM} \sim 10-20\%$ / $\sqrt{\rm E}$ sufficient for \blacklozenge Higgs physics
- $\sigma_{\text{jets}} \sim 30-40\% / \sqrt{E}$ to clearly identify W, Z, H in 2 jets decays
- transverse granularity < 1 cm for π_0 \blacklozenge from τ and HF
- could be satisfied by: +
 - high-granularity dual-readout fibre \blacklozenge calorimeter → SiPM readout
 - combined dual-readout crystal/fibre calorimeter

Physics process	Measurands	Detector subsystem	Performance requirement
$\begin{array}{l} ZH,Z\rightarrow e^+e^-,\mu^+\mu^-\\ H\rightarrow \mu^+\mu^- \end{array}$	$m_H, \sigma(ZH)$ BR $(H \to \mu^+ \mu^-)$	Tracker	$\Delta(1/p_T) = 2 \times 10^{-5} \oplus \frac{0.001}{p(\text{GeV}) \sin^{3/2} \theta}$
$H \rightarrow b\bar{b}/c\bar{c}/gg$	$BR(H \to b\bar{b}/c\bar{c}/gg)$	Vertex	$\sigma_{r\phi} = 5 \oplus \frac{10}{p(\text{GeV}) \times \sin^{3/2} \theta} (\mu \text{m})$
$H \rightarrow q\bar{q}, WW^*, ZZ^*$	$\begin{array}{c} BR(H \to q\bar{q}, \\ WW^*, ZZ^*) \end{array}$	ECAL HCAL	$\sigma_E^{\text{jet}}/E = 3 \sim 4\%$ at 100 GeV
$H \to \gamma \gamma$	$\mathrm{BR}(H \to \gamma \gamma)$	ECAL	$\frac{\Delta E/E}{\frac{0.20}{\sqrt{E(\text{GeV})}} \oplus 0.01}$

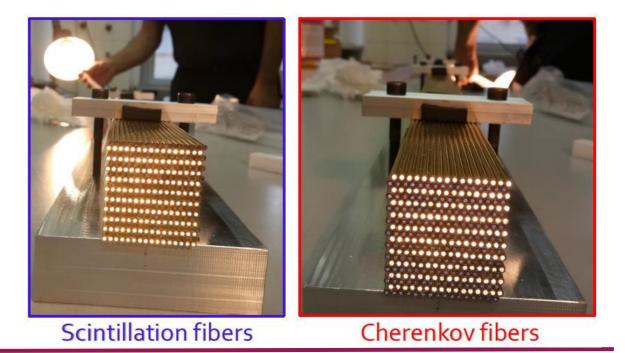
All-fibre DR calorimeter option

- DR fibre calorimeter
 - ~ 130 M fibres \blacklozenge
 - 1 mm ø, 1.5 mm pitch
 - copper absorber \blacklozenge
 - 75 projective towers × 36 slices +
 - $\Delta \vartheta = 1.125^\circ, \Delta \phi = 10.0^\circ$
 - ϑ coverage: down to ~100 mrad \blacklozenge
- G4 simulation available \blacklozenge
 - tuned to RD52 TB data \blacklozenge

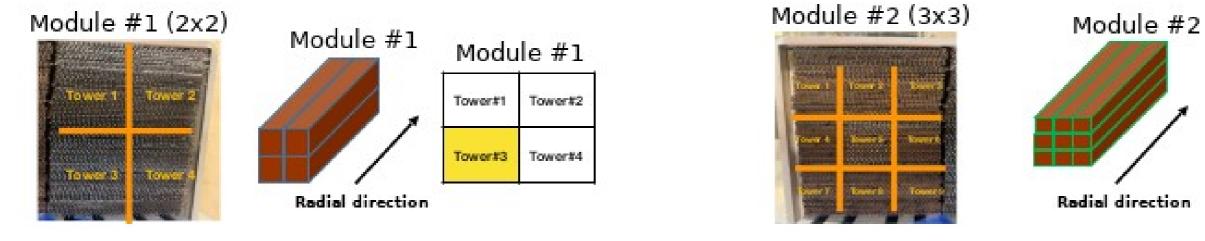




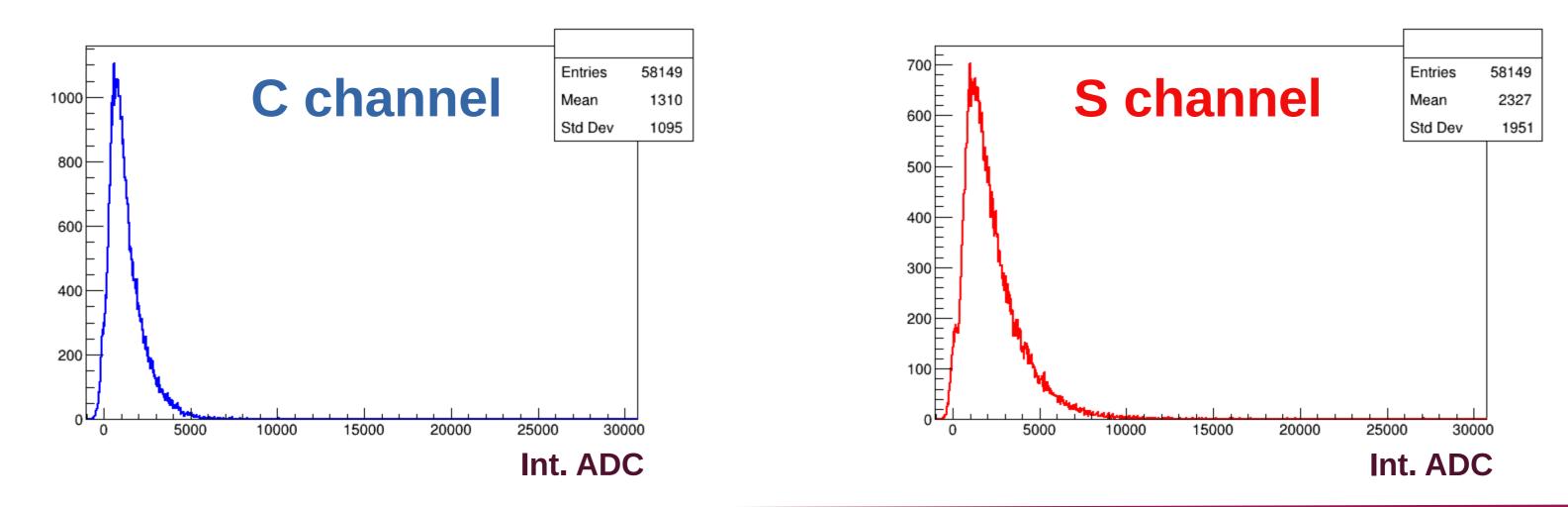
Hong Kong, 13.02.2023



2022 Korean-prototype beam test







Hong Kong, 13.02.2023

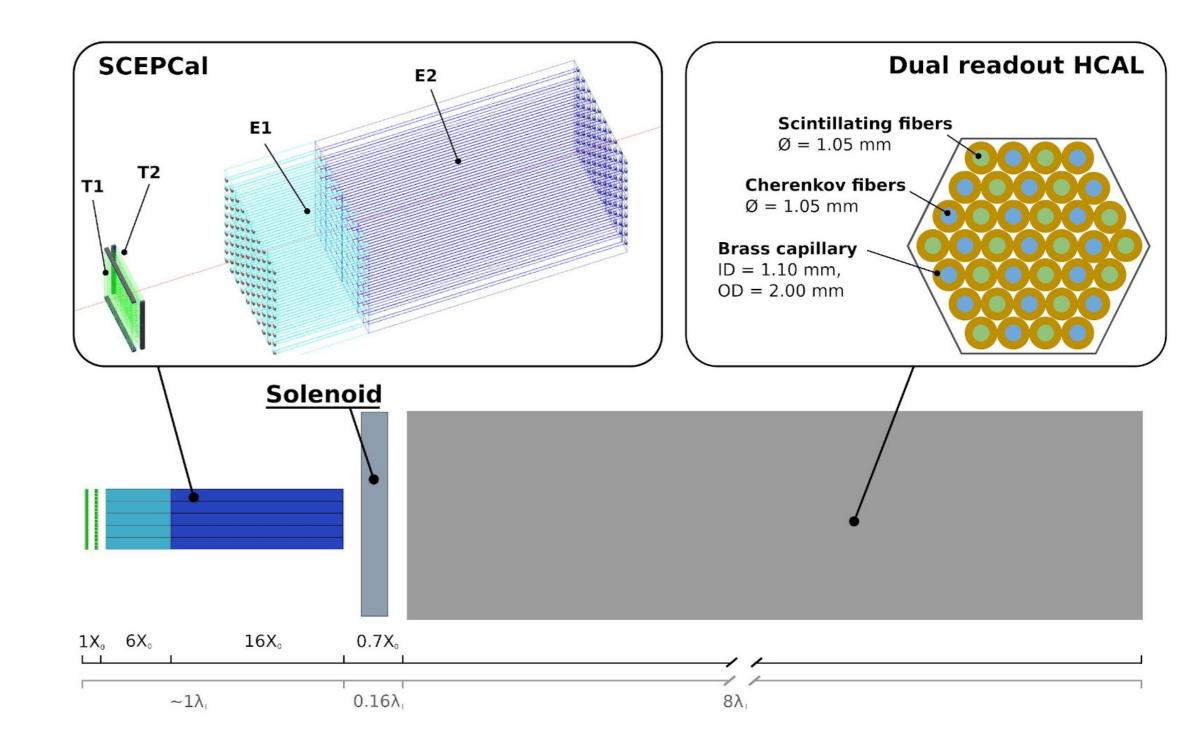
Module #2

Tower#1	Tower#2	Tower#3
Tower#4	Tower#6	Tower#6
Tower#7	Tower#8	Tower#9

Crystal option (IDEA++)

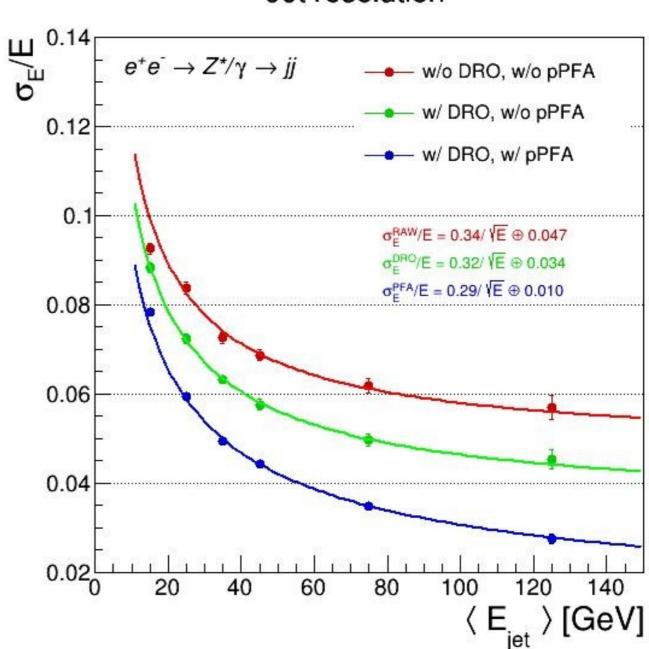
✦ ECAL ~20 cm PbWO₄

- ✤ 2 layers: 6+16 X₀
- DR with filters
- *o*_{EM} ≈ 3% /√E
- timing layer
 - LYSO:Ce crystals
 - $\sigma_t \sim 20 \text{ ps}$
- HCAL layer
 - $\sigma_{HAD}/E \sim 26\%/\sqrt{E}$



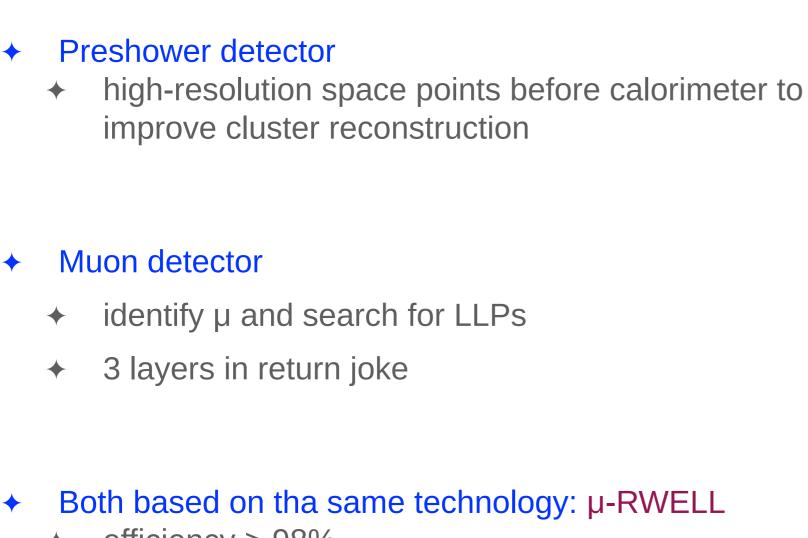
• ECAL ~20 cm PbWO₄

- DR with filters
- σ_{EM} ≈ 3% /√E
- timing layer
 - LYSO:Ce crystals
 - $\sigma_t \sim 20 \text{ ps}$
- HCAL layer
 - $\sigma_{HAD}/E \sim 26\%/\sqrt{E}$

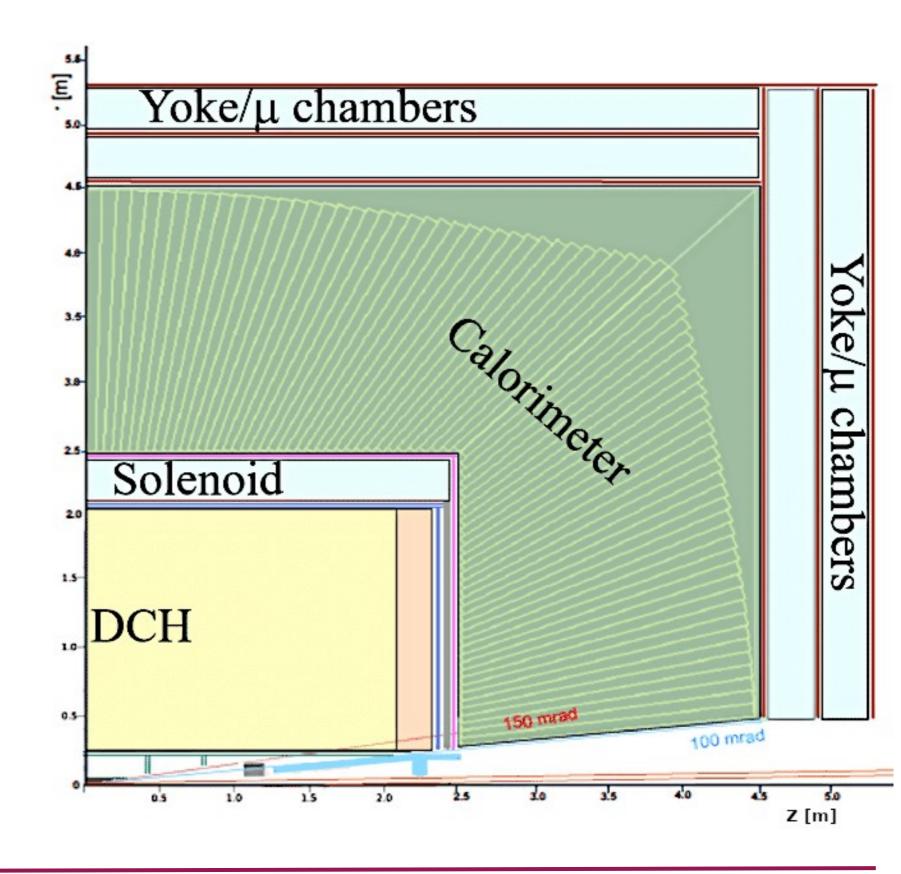




Preshower and muon detector



- efficiency > 98% \blacklozenge
- mass production
- optimisation of FEE channels/cost



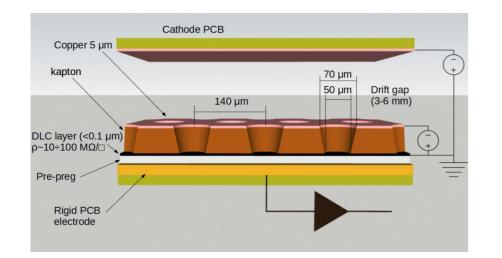
Preshower and muon detector

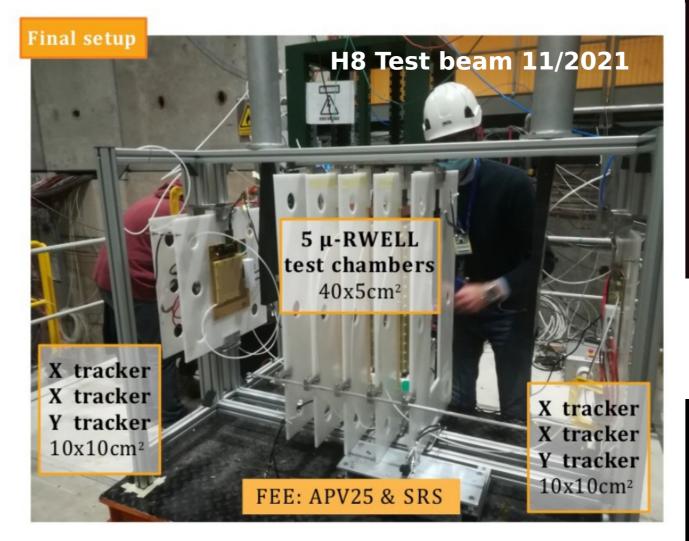
Preshower detector

- pitch 0.4 mm \blacklozenge
- resolution < 100 μ m
- 1.5 M channels

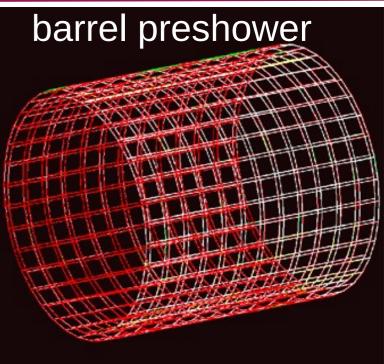
Muon detector +

- pitch 1.5 mm \blacklozenge
- resolution < 400 μ m
- 5 M channels
- 50×50 cm² 2D tiles
 - to cover > 4330 m^2 \blacklozenge

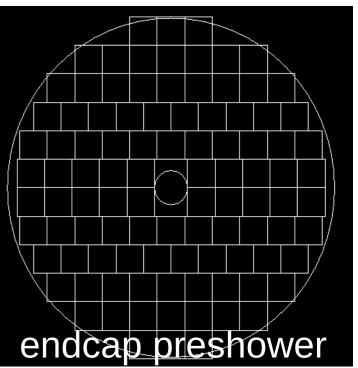




7 μ-RWELL prototypes, with resistivity from 10 to 80 M Ω / \Box , will allow to define final (50×50 cm²) detector resistivity



similar design for muon detector



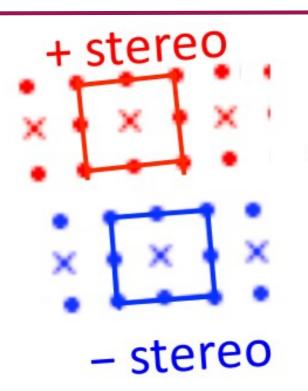
- Vertex detector w/ DMAPs for best momentum resolution
 - work in progress, i.e. Arcadia
- Silicon wrapper R&D starting from ATLASpix3 chips
 - also for outer layers of vertex detector
- Wire chamber R&D ongoing on many aspects
 - Lot of work to demonstrate cluster-counting performances
- Dual readout: project for full containment prototype(s) +
 - EM crystal option w/ DR +
- Preshower and muon system based on µ-RWELL technology
 - ✤ R&D on resistive, long, DLC strips, 2D sensors, custom ASIC

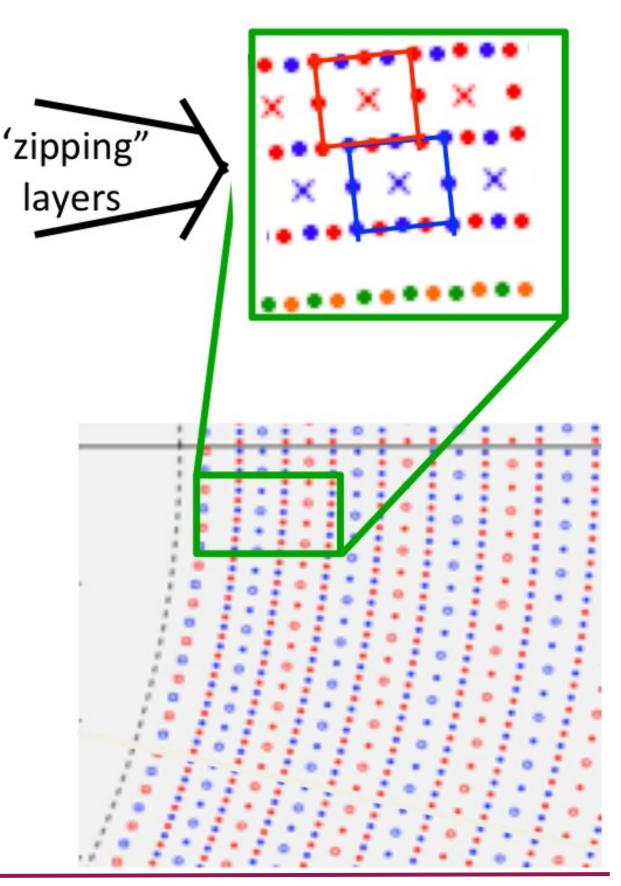
Backup

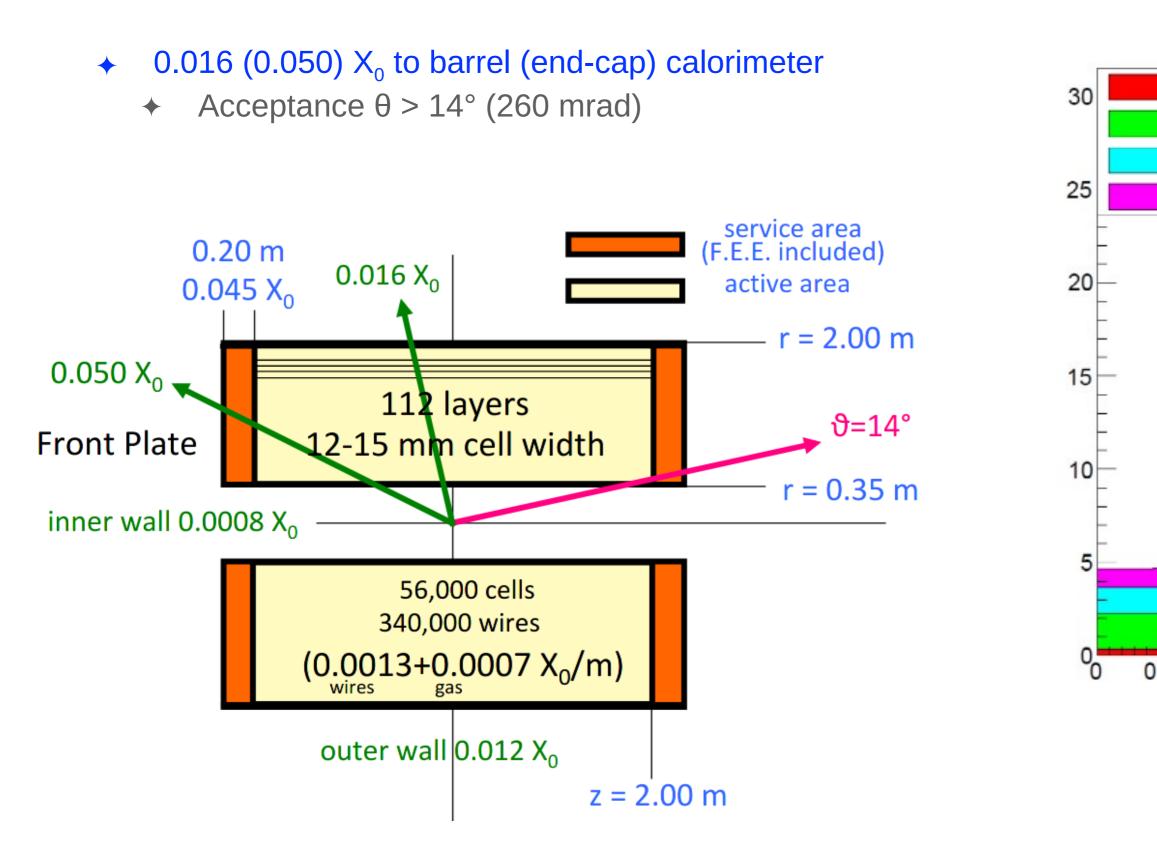
Drift chamber

Thin wires

- ◆ 20 µm sense W(Au)
- ◆ 40 µm field Al(Ag)
- ✤ 56448 square cells
 - 12 to 15.5 mm wide
 - ✤ 360 ns drift time
 - ★ zipped layers → more uniforme field
- ✤ 14 coaxial super-layers
 - ♦ 8 layers alternating-sign stereo angles
 - 24 azimuthal 15° sectors

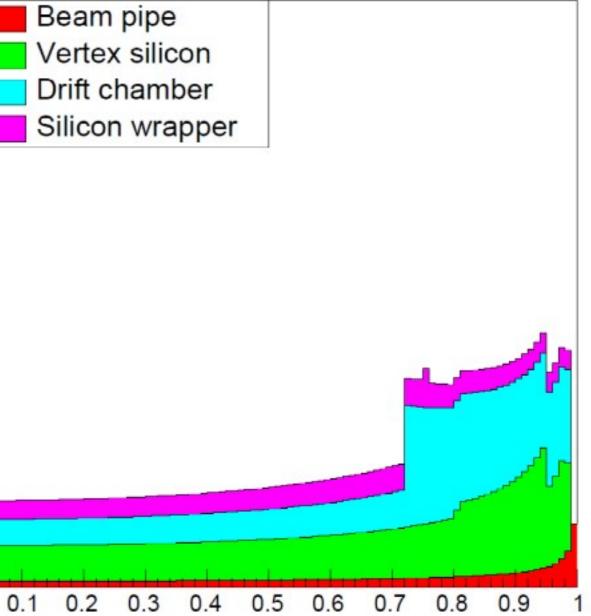






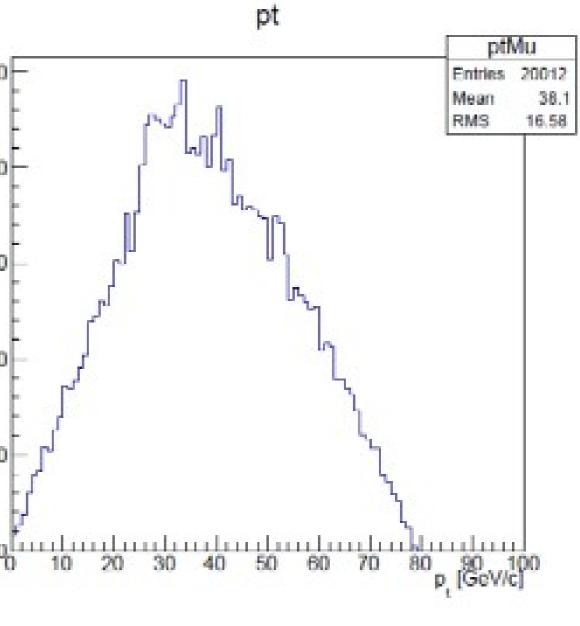


IDEA: Material vs. $cos(\theta)$



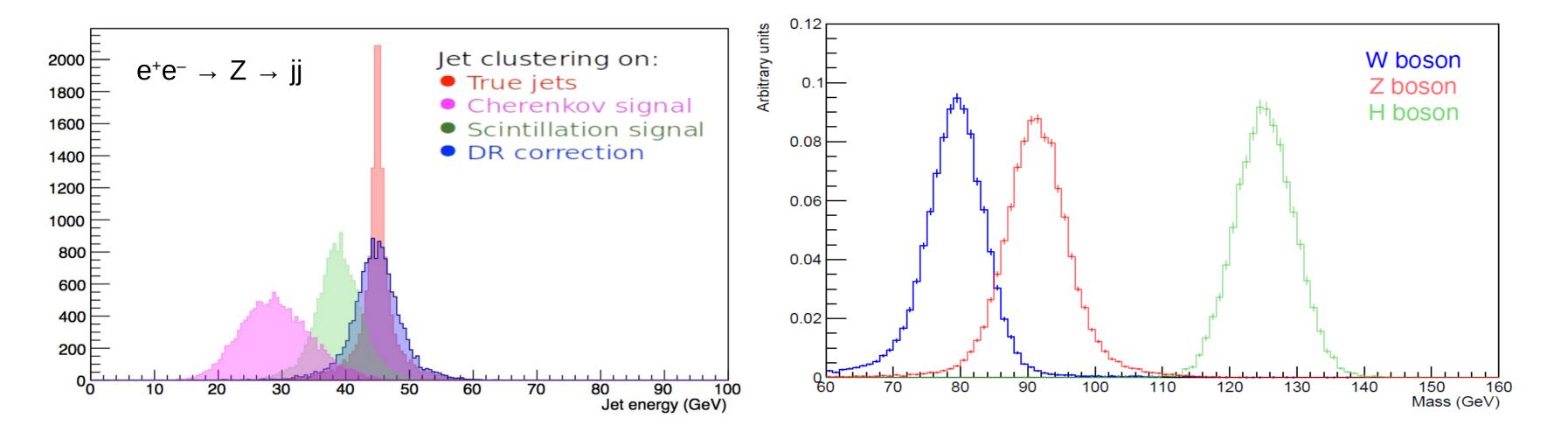
Additional requirements

*	 Excellent acceptance and luminosity control e.g. ECAL inner radius known at 15 µm 	² 600
+	 B ~ 2T for beam emittance preservation Maximise tracking volume 	dhudp ² [GeV/
*	 Bunch spacing at Z pole ~ 25 ns Limited drift time 	300
*	PID & π_0 ID for HF/ τ physics + dE/dx or TOF	200
*	 Muons in ZH events have rather low p_⊤ Transparency more relevant than asymptotic resolution 	100

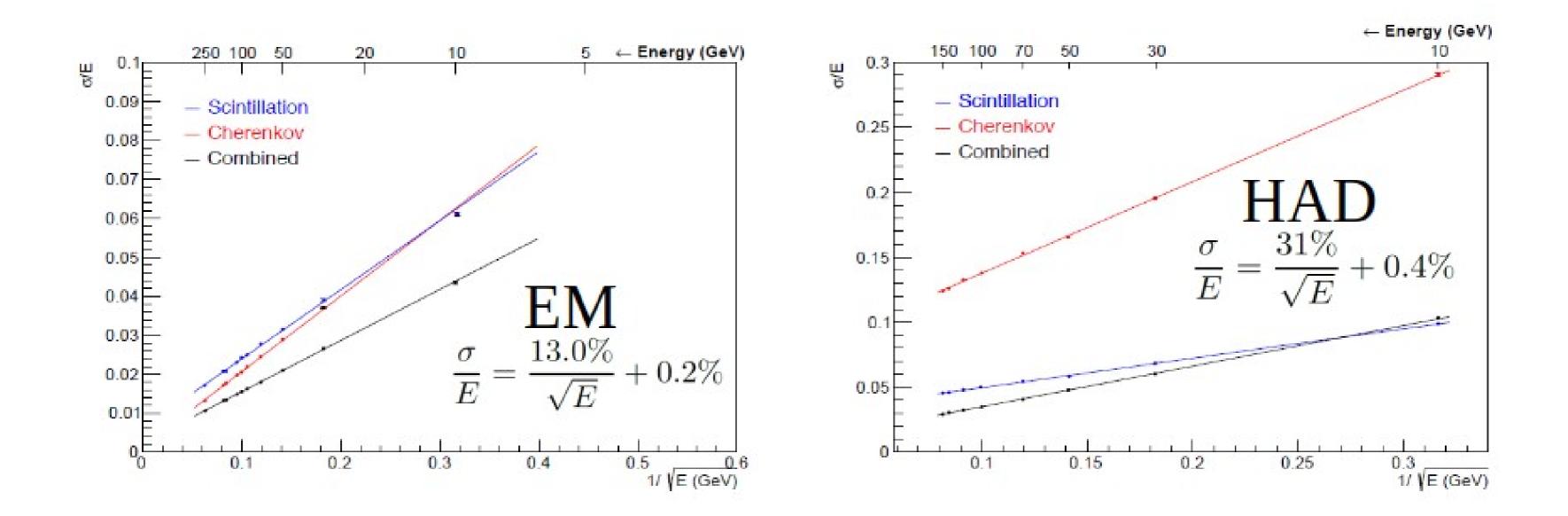


ZH (Z $\rightarrow \mu\mu$)

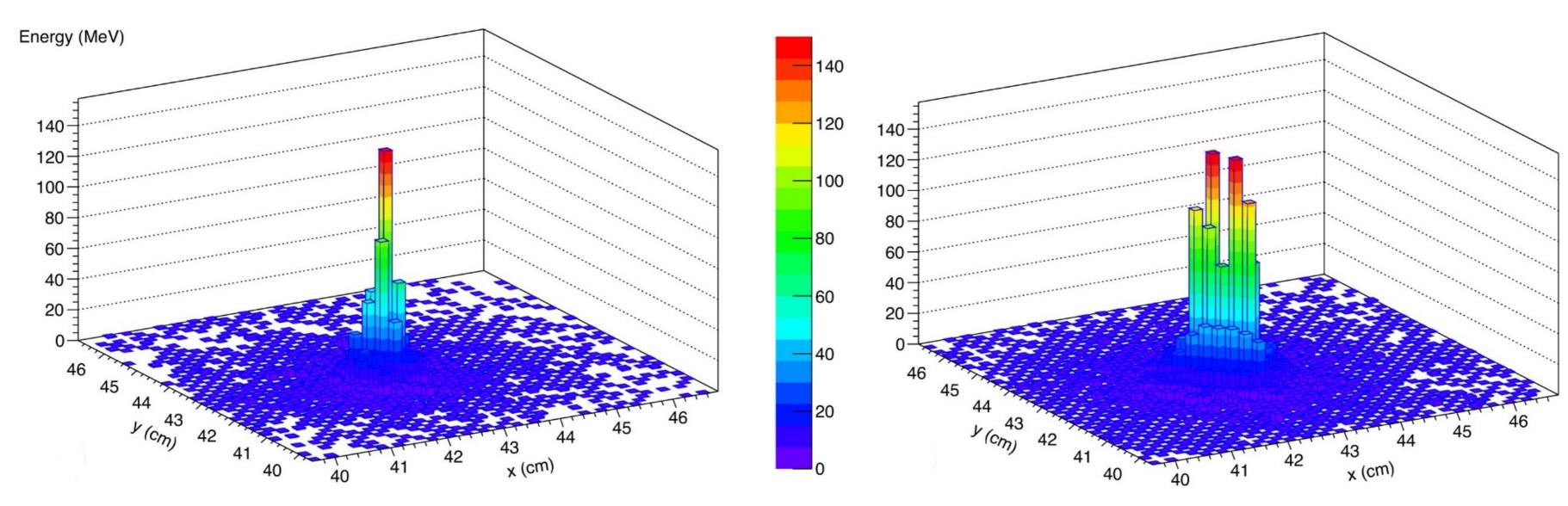
- Gaussian resolution
- Adequate separation of W / Z / H



✦ Good resolutions averaged over eta and phi



Event displays



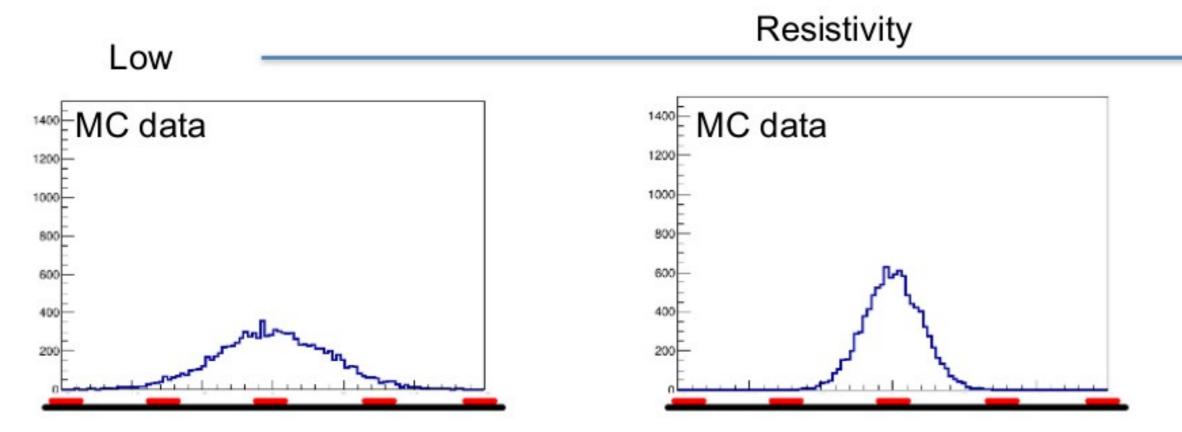
50 GeV e-

Hong Kong, 13.02.2023

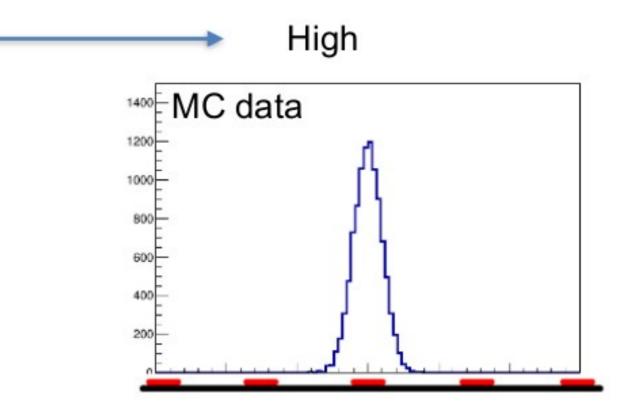
100 GeV π^0

R&D: optimal DLC resistivity by studying spatial performance +

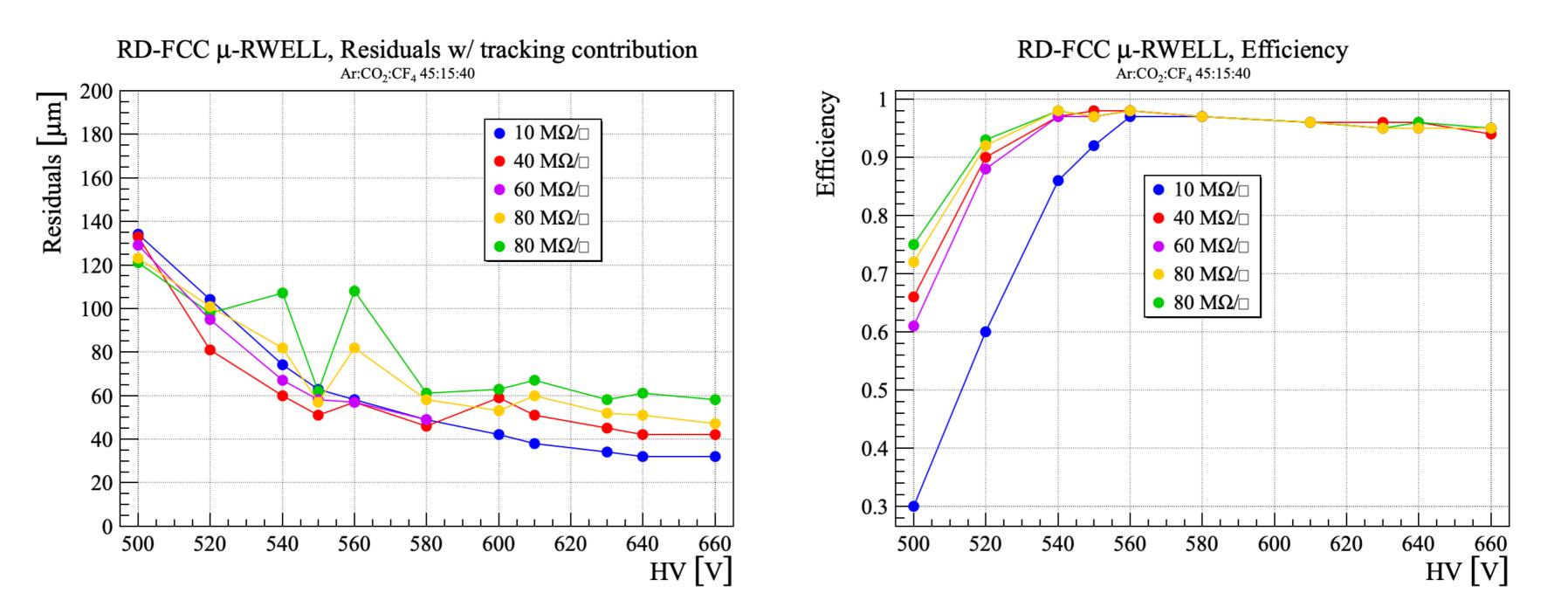
- Preshower: 10, 30, 50, 70, > 100-200 MΩ/□ \blacklozenge
- Muon: 15, 35 MΩ/□ \blacklozenge
- e.g.: effect of resistivity on charge spread \blacklozenge



Hong Kong, 13.02.2023



✦ Residuals and efficiency for different resistivities



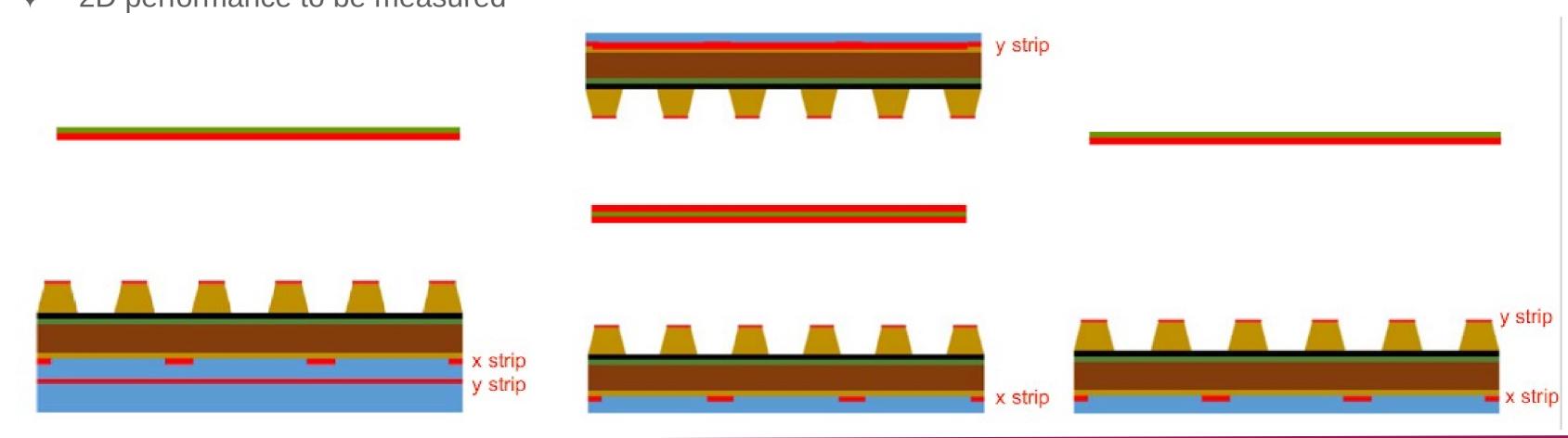
2D $\mu\text{-}RWELL$ ideas

$\mu\text{-RWELL}$ w/ 2D anode readout

- ✦ Good performance but need higher gain wrt. 1D
- More complex PCB construction

2 stacked 1D μ -RWELL

- ✤ 1 view per µ-RWELL
- easy PCB construction
- 2D performance to be measured



$\mu\text{-}RWELL$ with strips on top and anode

- HV on DLC
- TOP to ground
- 2D performance to be measured

- Test with TIGER ASIC
 - developed for BESIII CGEM-IT
- Prepare new readout card based on System On Modules
 - aim: develop dedicated ASIC for μ-RWELL



Measured performance TIGER ASIC

Parameters	Values
Input charge	5-55 fC
TDC resolution	30 ps RMS
Time-walk (5-55 fC range)	12 ns
Average gain	10.75 mV/fC
Nonlinearity (5-55 fC range)	0.5%
RMS gain dispersion	3.5%
Noise floor (ENC)	$1500 \ e^{-}$
Noise slope	$10 \ e^{-}/pF$
Maximum power consumption	12 mW/ch