

Rui De Oliveira December/11/2022



#### -20 persons

-13 persons from a contracting company  $\rightarrow$  only productions using well established technologies.

-7 CERN staff  $\rightarrow$  3 administration and 4 dedicated to R&D prototypes.

-1400 square meters (new building finished in 2018).

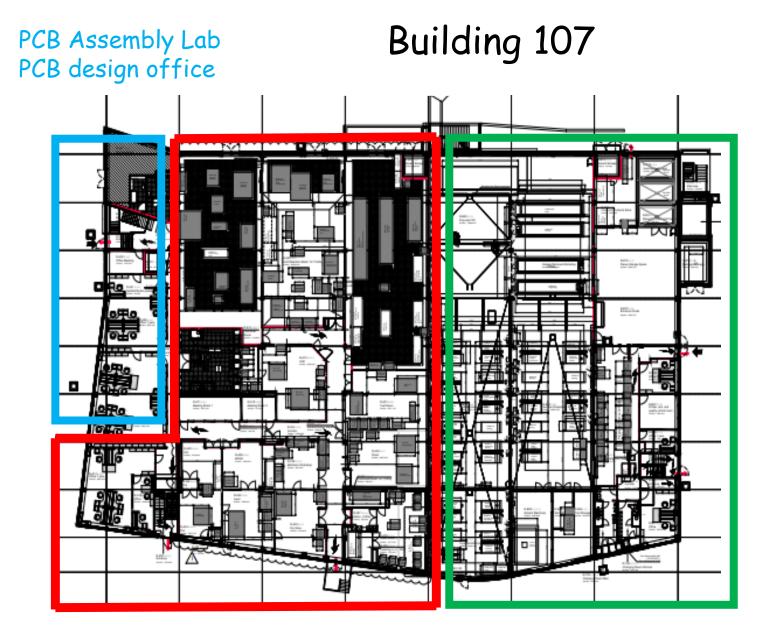
-Making PCBs since 1965

-Mandate:

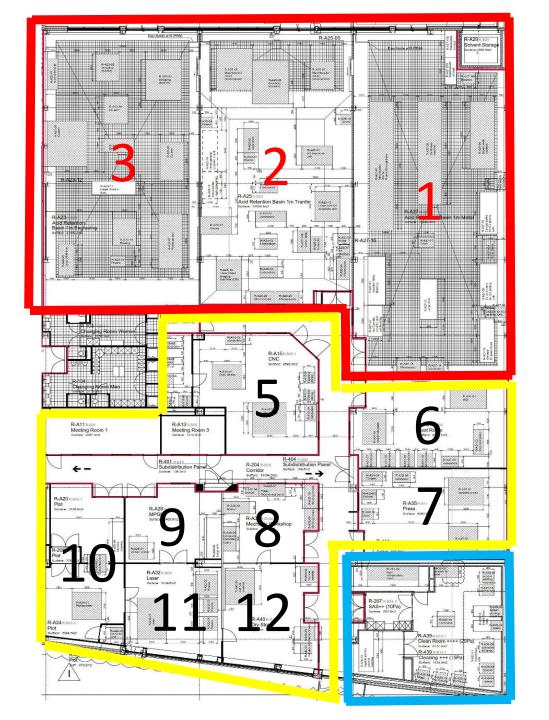
-Produce parts difficult to find in industry.

-Provide to CERN users expertise in interconnections technologies.

-Make sometime mass production.



MPT workshop (EP Dept. -20persons) Surface treatment workshop



Wet processing Area 1/Plating facility 2/Photo processes 3/Chemical Etching

Dry processing Area 5/Drilling/milling 6/Test 7/Pressing 8/MPGD 9/Chemical analysis 10/Photo mask lab 11/Vacuum deposition 12/Dry store







### Plating facility

- -Cu : bath -Ni : bath -Au Bath -Carbon Horizontal -desmear Horizontal
- -Cu oxide Horizontal

### Chemical etching facility

-Cu,SS,Al,Ni Horizontal -Ti ,W, in hood -Stripping horizontal line -Cu/Ni horizontal plating line

#### Photo processes

UV exposure : std and large size Laser Direct imaging Lamination : std and large Soldermask spray deposition Horizontal development:

-Solid resists

Wet area

- -Coverlays
- -Solder masks
- -Liquid resists



## Dry area

### CNC processes

-Drilling -Milling



## Press facility

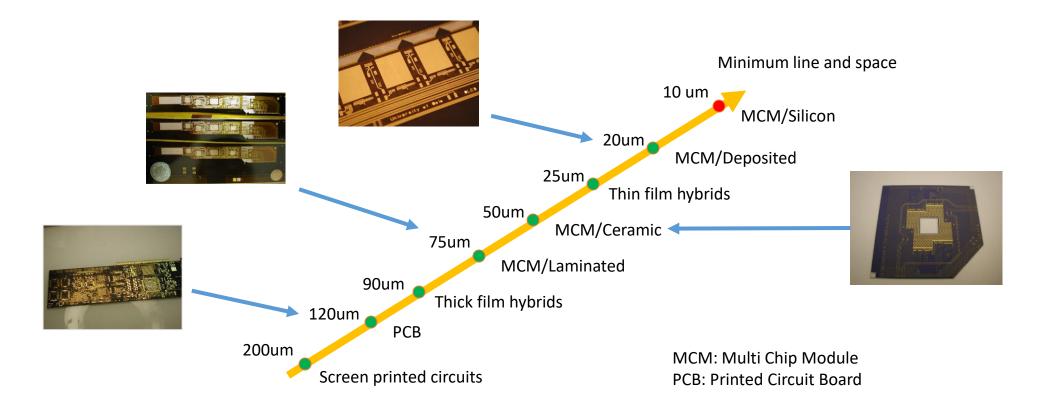
-Multilayer gluing -2 isostatic presses

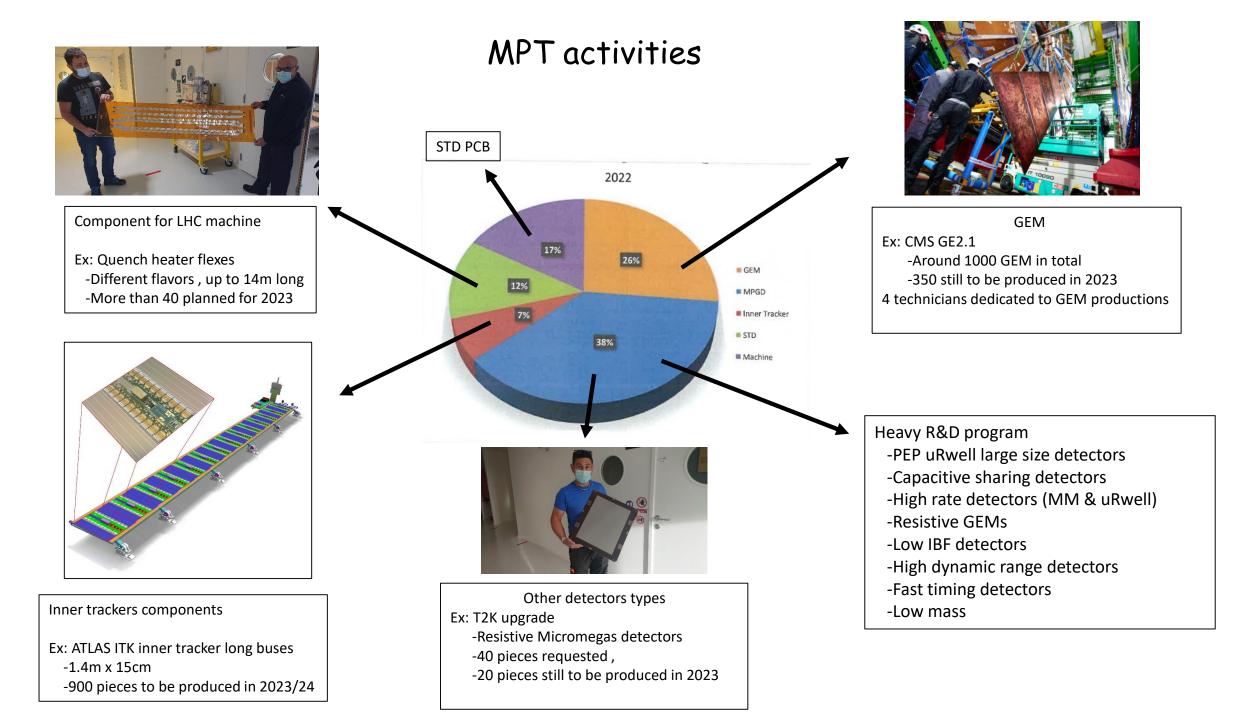


## Test facility

-Automatic Optical Inspection -Electrical , flying probe tester -microscopes

# Core interconnections technologies available at MPT





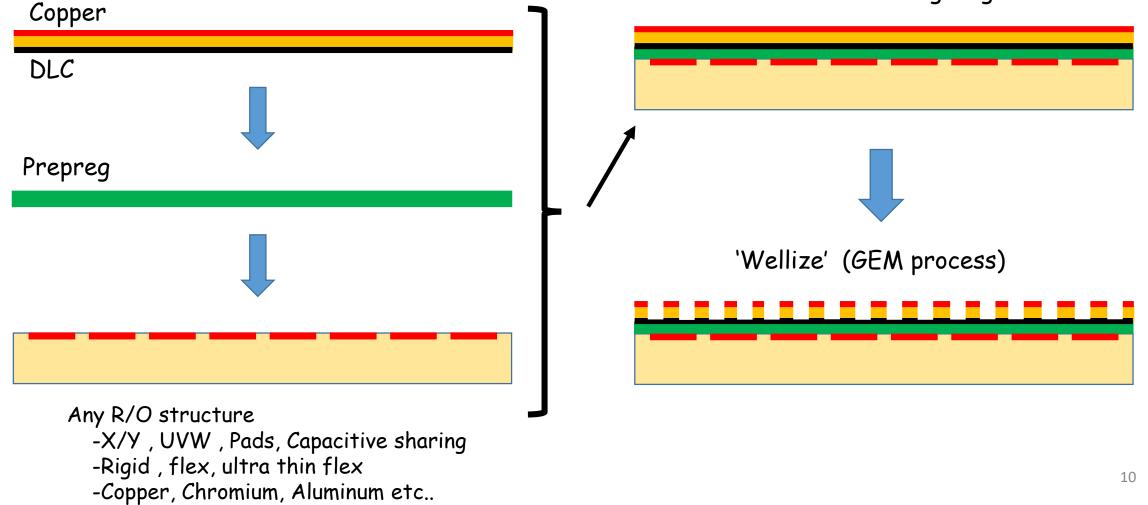
We are working on many projects, but I want to highlight today 2 of them:

# 1/News on uRwell detectors production

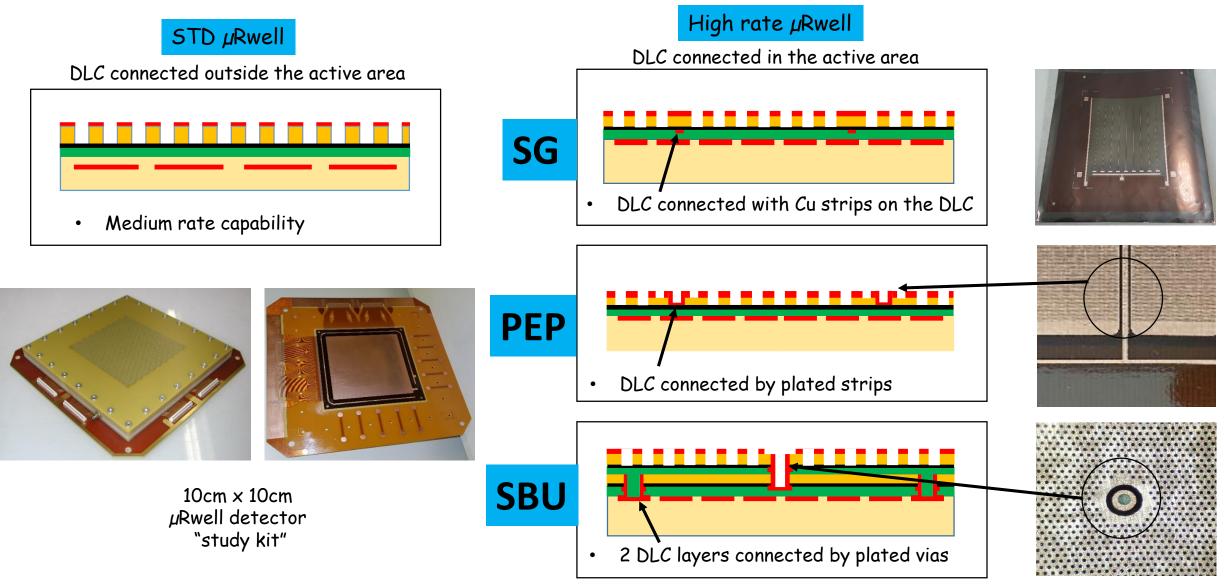
2/New vacuum depositing machine

# uRwell structure (reminder)

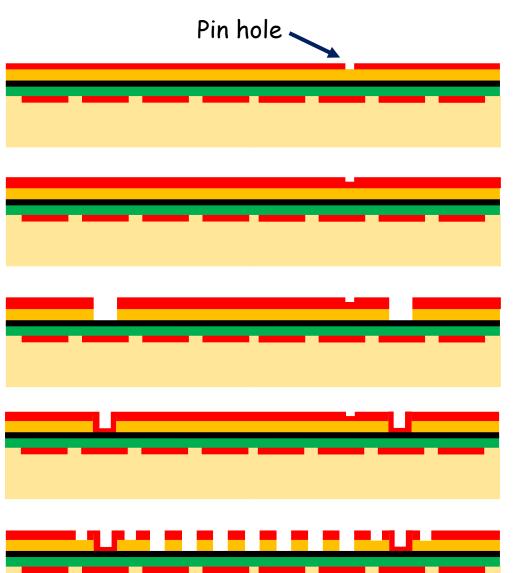
After Vacuum gluing



# Many different ways to improve the rate capability



# PEP uRwell is for the time being our target



Basic structure

 $P \rightarrow copper Plating$ To hide the pin holes

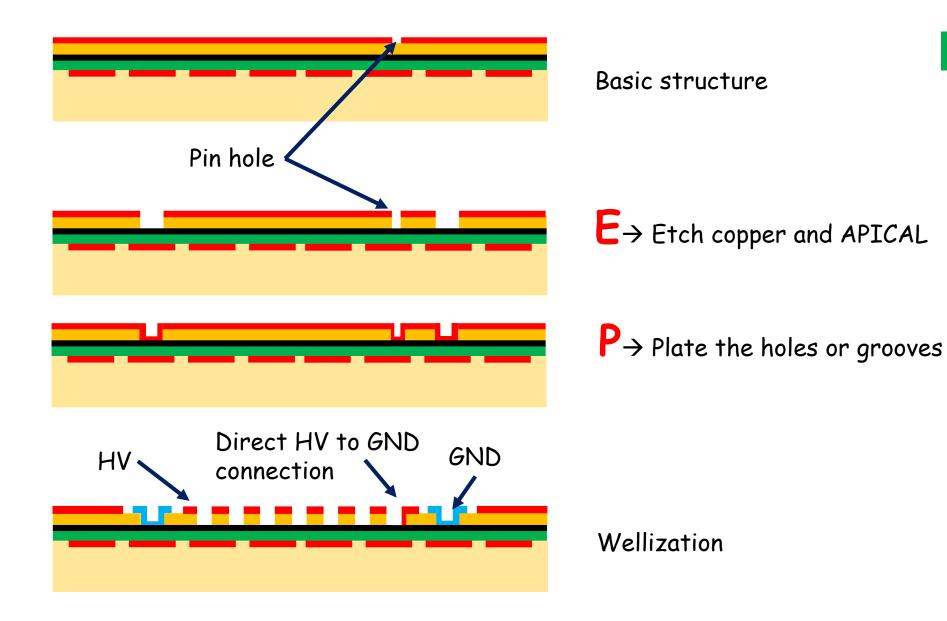
 $E \rightarrow E$ tch copper and APICAL No need for precise alignment

 $P \rightarrow$  Plate the holes or grooves For DLC charge evacuation

Wellization, easy alignment on visible patterns

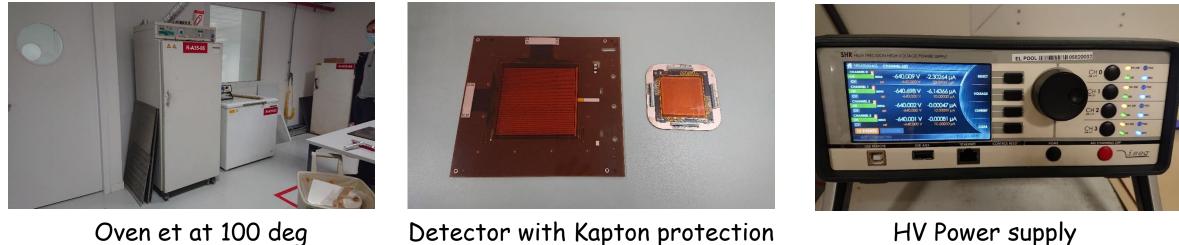
Around 95% yield

# PEP uRwell without pin hole protection



Around 60% yield

# **E-cleaning**



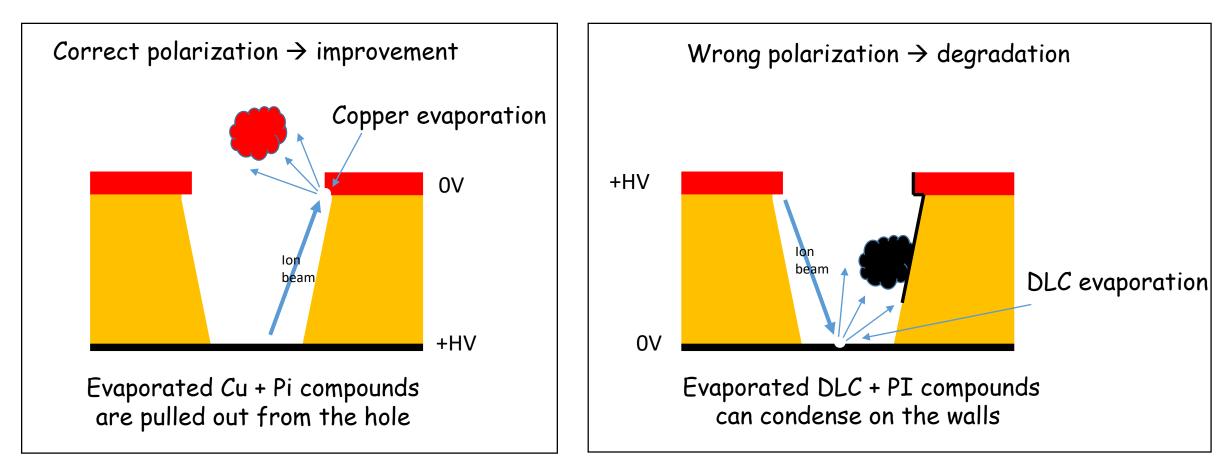
HV connections

HV Power supply 10 to 20uA limitation

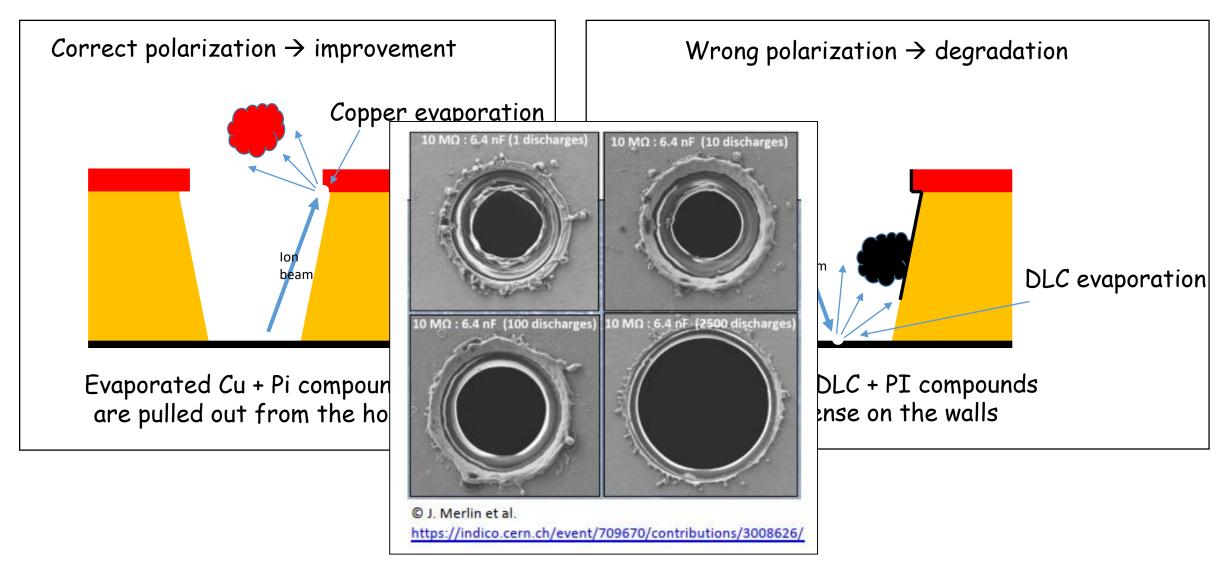
1/Minimum requirement → the detector should hold 250V in open air, leakage current <5nA.</li>
2/We put a kapton protection on the detector.
3/We then put the detector in oven @100 deg for 8 hours.
4/We apply 250V, 10 to 20uA limitation
5/We wait to reach a leakage current <1 to 2nA and stay 2 hours with this current.</li>
6/We then raise the voltage by 50V steps below 550V and then 20V steps.
7/We repeat step 3 and 4 till we reach 640V.

Depending on the detector size and the type of defect the E-cleaning can take a full week 14

### Explanation tentative presented at RD51 February/09/2022

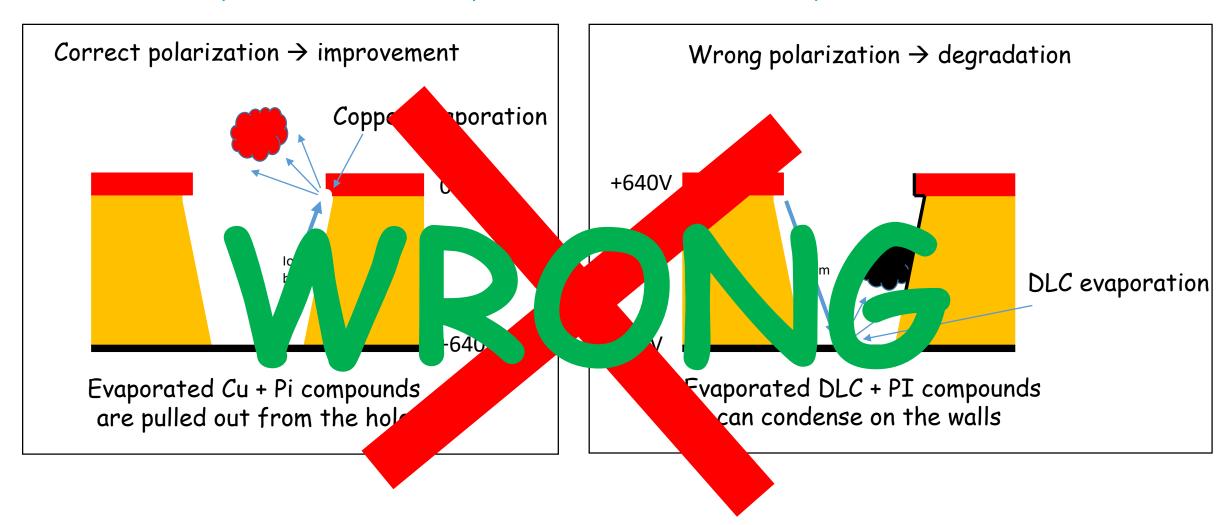


### Explanation tentative presented at RD51 February/09/2022

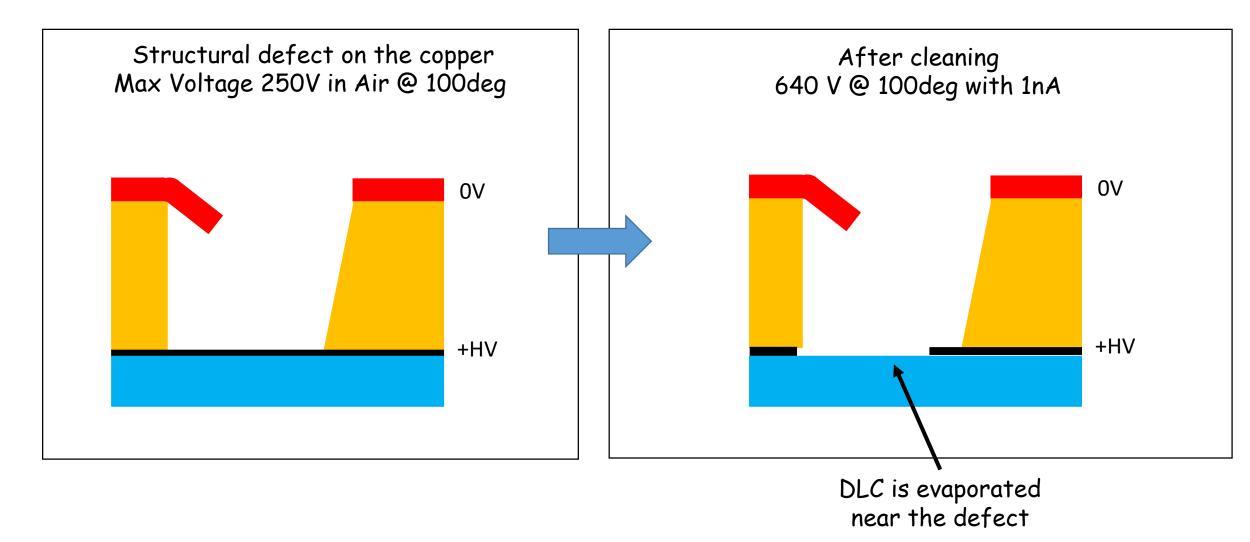


In the GEM case we can clearly see material pulled out from holes after repetitive sparks We do not see this with uRwell, but I do not see why the principle should be different

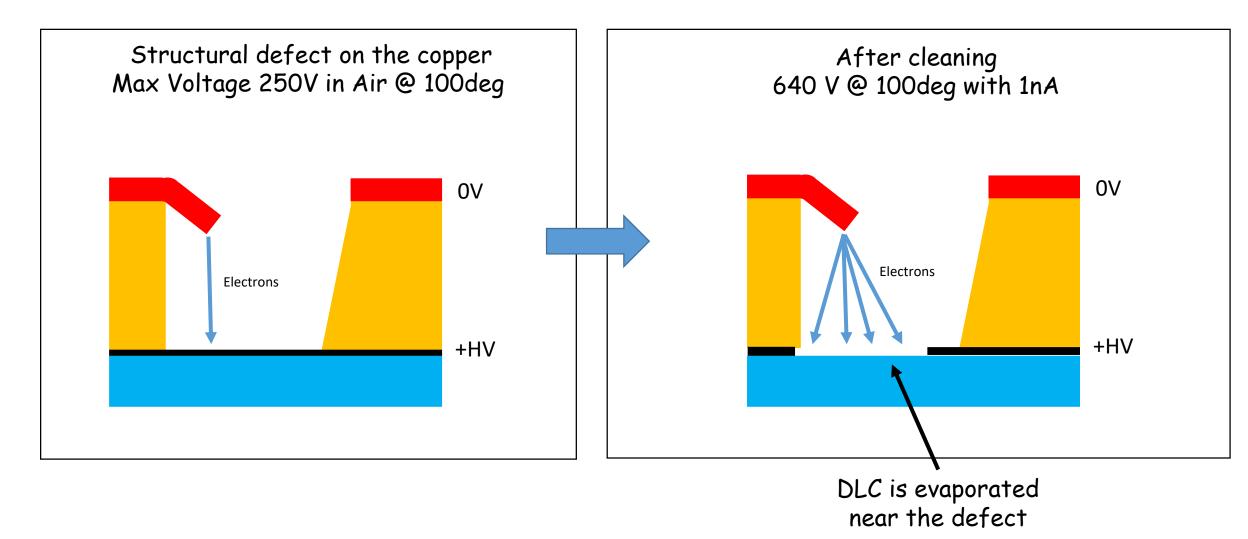
### Explanation tentative presented at RD51 February/09/2022



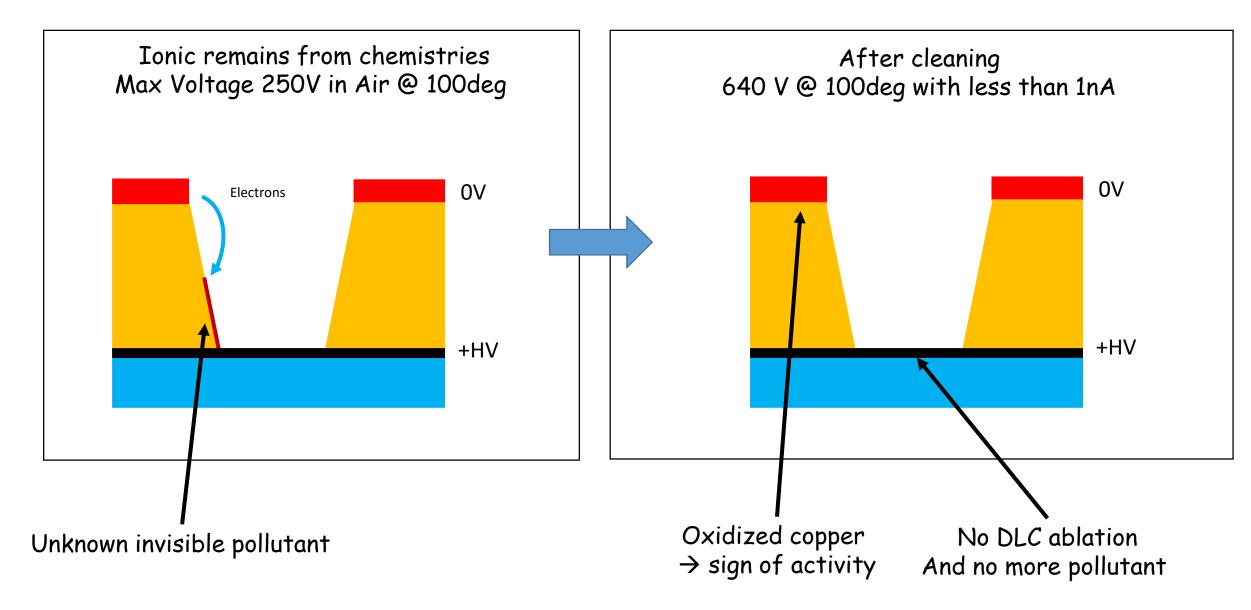
# Second tentative, first type of defect



# Second tentative, first type of defect



# second type of defect (invisible)



### After a full week cleaning and billions discharges

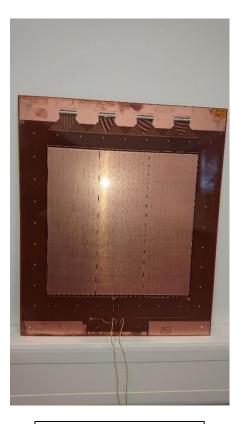
© J. Merlin et al. https://indico.cern.ch/event/709670/contributions/3008626/

10 MΩ : 6.4 nF (100 discharges) 10 MΩ : 6.4 nF (2500 discharges

10 MO : 6.4 nF (1 discharges)

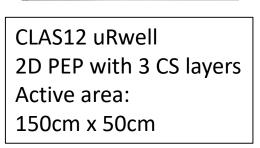
10 MΩ : 6.4 nF (10 discharges)

# PEP examples



Frascati R&D 1D PEP uRwell Active area: 40cm x 5cm

Frascati R&D 1D PEP uRwell Active area: 30cm x 30cm



FRANK

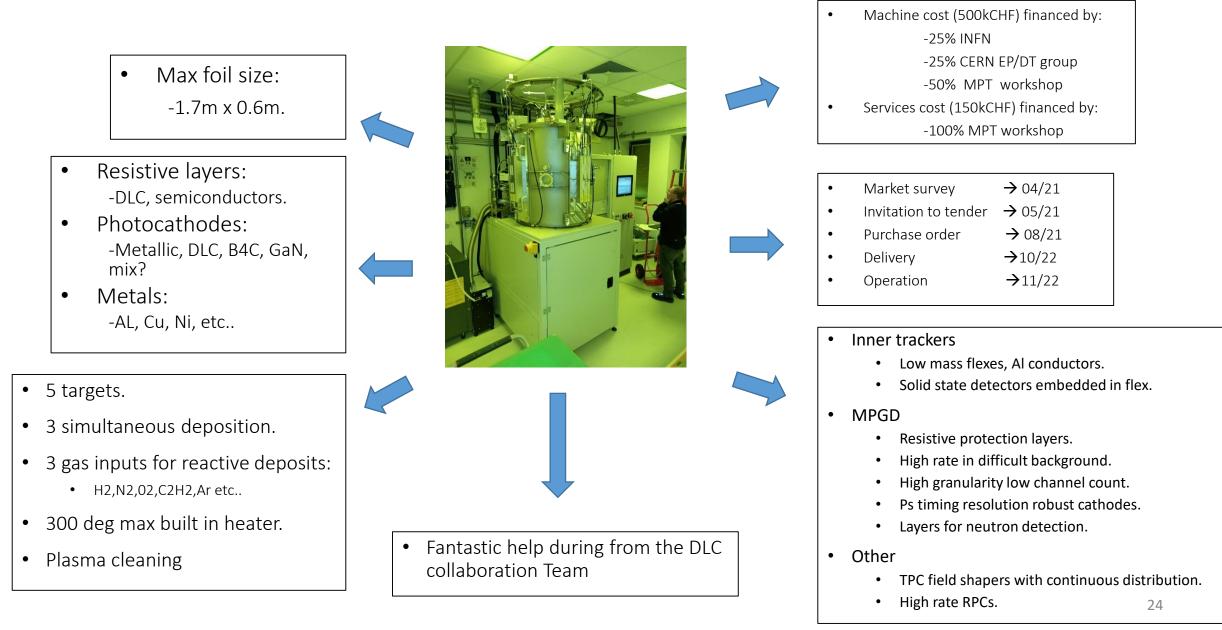


CLAS12 uRwell rolled in the oven for E-cleaning

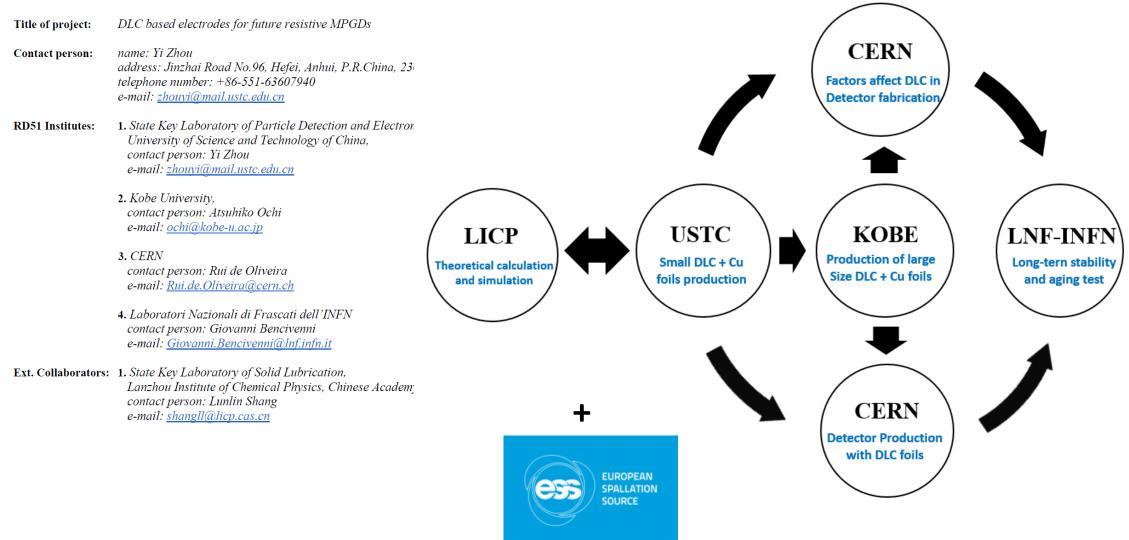
Whatever is the size, we can always reach a few nA @ 640V/100deg !

# New machine

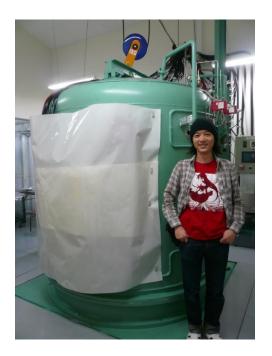
# Pulsed DC magnetron reactive PVD



# **DLC** collaboration



Kobe Japan B-sputter 4 m x 1m real

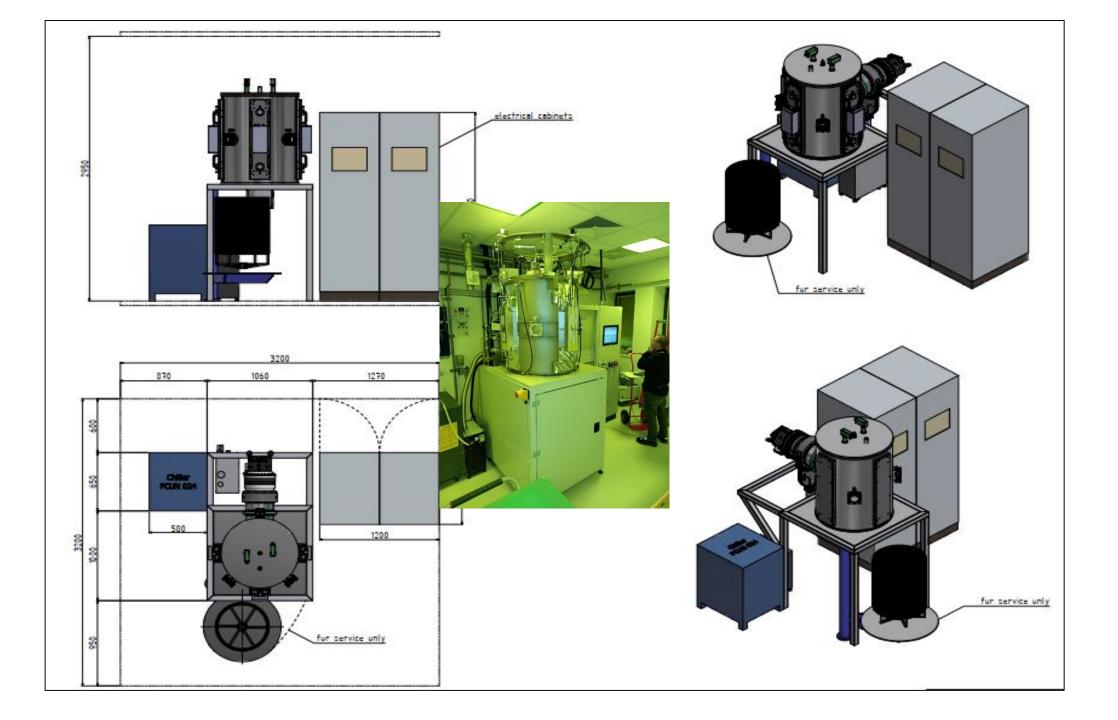


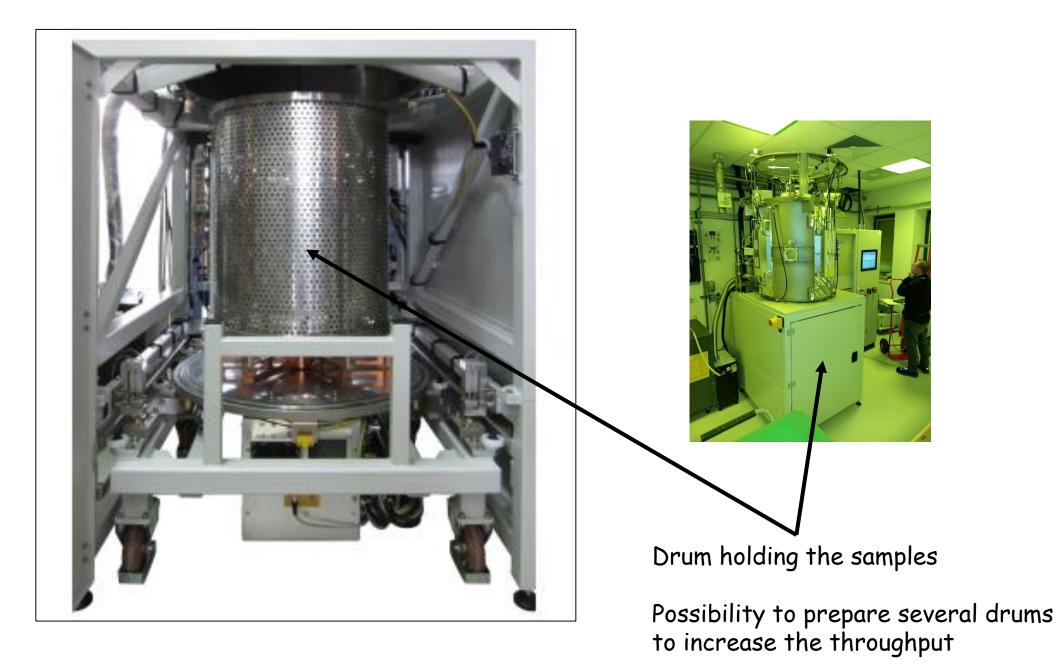
ESS 2.1 m x 400mm

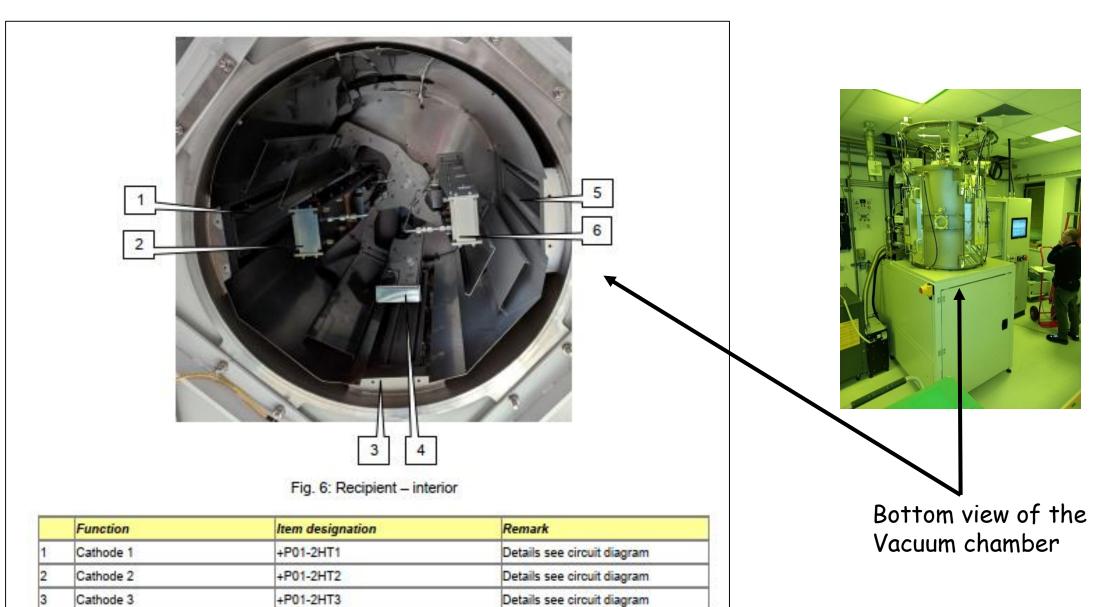


### China USTC 1.2 m x 500mm









Details see circuit diagram

Details see vacuum scheme

Details see vacuum scheme

+P01-1E1

+P01-2HT5

+P01-2HT4

Heater

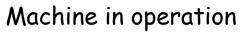
6

Cathode 5

Cathode 4

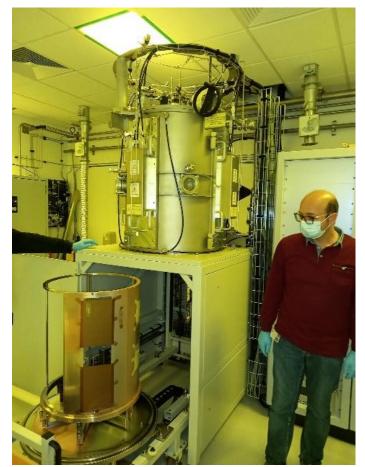
### pictures







### 70cm copper target installation



Drum unloading after processing

### Preliminary results obtained after few weeks

### The team

#### • Al , Cr and Cu (Ar)

- Done : excellent results  $\rightarrow$  this was the commissioning test

### • <u>DLC (Ar)</u>

- Done : from 30K to 300K  $\rightarrow$  just playing on the kapton roughness

#### <u>DLC-Cr-Cu (Ar)</u>

 Done : good adhesion, still need a little tuning to be perfect

### <u>DLC (Ar/N2)</u>

- Done : from 30K to some Gohms/square
- Playing on deposit thickness, kapton roughness, N2 concentration.

### • <u>DLC (Ar/C2H2)</u>, <u>DLC (Ar/CH4)</u>, <u>DLC (Ar/H2)</u>

- Not Done
- <u>B4C</u> , <u>GaN</u> , <u>Si</u> ,<u>Sio2</u>
  - Not Done

#### • <u>Uniformity test</u>

• Done : really encouraging → few % over 50cm

#### <u>Serge Ferry (MPT)</u>

- Expert in the field
- He co-designed the machine
- He wrote all the specifications
- He is now leading the preliminary test program

#### • <u>Givi</u>

- He participated to the commissioning
- Participation to preliminary test program

#### • Gianfranco Morello (Frascati)

- He participated to the commissioning
- Participation to preliminary test program

Better quantitative results will be given at the next RD51

# Next

- PEP uRwell:
  - Produce 2 more 1.5m × 0.5m PEP uRwell
  - Fully automatize the electrical cleaning to reduce the uRwell cost.
  - Continue the uRwell technology transfer to industry
- Vacuum machine:
  - Improve our knowledge on N2 doped high resistive value DLC layers
  - Start some test with C2H2
  - B4C?
  - We are ready to accept requests from the community for DLC layers.
- In next RD51 meeting I will give news on :
  - High rate SBU MM
  - High rate SBU uRwell
  - Capacitive sharing