

Research on fast grounding simulation method for the MPGD with resistive layer

Zhujun Fang^{1,2}, Siqi. He^{1,2}, Hang. Zhou^{1,2}, Yi. Zhou^{1,2}, Jianbei. Liu^{1,2,*}, Ming. Shao^{1,2}, Zhiyong. Zhang^{1,2}

¹State Key Laboratory of Particle Detection and Electronics, University of Science and Technology of China, Hefei 230026, China

²Department of Modern Physics, University of Science and Technology of China, Hefei 230026, China

1. Introduction

Applying resistive layer between electron multiplying region and readout array:

Advantage:

- High sparking resistance
- High gain with single-layer MPGD
- Better stability and robustness



2. Principle analysis

Several effects will cause the gain decrease:

• Flowing current effect

When the multiplied electrons flow into the ground via the resistive layer,

there is a current on the resistive layer surface, leading to a voltage drop.

Charge accumulation effect

The electron discharge speed is limited by the time constant of the circuit loop between the resistive layer and ground, causing a charge accumulation on the

New challenge:

- Detector gain decrease at high counting rate
- Non-linear relationship between detector gain, counting rate and irradiation area

Detector design optimization:

• **Fast grounding** of the resistive layer

nultiplier urrent **Resistive** layer Fast grounding FR4 Readout array

Fig. 1 µRWELL design without (upper) and with (lower) fast grounding resistive layer.

resistive layer surface and affecting the electric field.

• High voltage filter circuit influence

The protection resistor in the high voltage filter circuit connected to the upper copper layer of MPGD will also generate voltage drop on it.

3. Simulation methods

• Flowing current effect can be simulated based on constant circuit with MATLAB. There are two effective methods:



Calculating average potential drop U_{ave} of the irradiated area under G/G₀=1, then calculate G/G_0 under steady state through the dichotomy method

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Fig. 2 Flowchart of Kirchhoff equation-based simulation.

Fig. 3 Flowchart of Ohm's law-based simulation.

- Voltage drop caused by high voltage filter circuit is proportional to the total ion current and the resistance in the filter.
- The above three effects are equal and independent. The gain decrease under the negative feedback can be regarded as the superposition of the three effects, and can be effectively calculated by the dichotomy method.





Calculating G/G_0 under steady state through the dichotomy method.

Fig. 4 Flowchart of charge accumulation effect.



Counting rate (MHz/cm²)

Fig. 7 Influence of high voltage filter circuit, using ORTEC 142AH preamplifier in measurements.

5. Conclusion

gain

etector

Relative

- There are three effects influences detector gain: flowing current effect, charge accumulation effect, and high voltage filter circuit influence. The first two has the main contributions.
- Simulation method based on MATLAB is proposed. It is suitable for several kinds of fast grounding designs: stripgrounded, double-layer-point grounded, point grounded array. Experiments with µRWELL and Micromegas detector verify the correctness of the simulation. The experiments with large irradiation indicates a more significant gain decrease.

Reference: Z. Fang, et al., Simulation and optimization of fast grounding designs for micro-pattern gas detectors with a resistive layer, NIMA, vol. 1032, 2022. More information and discussion: fzj19@ustc.edu.cn