

8th International Meeting on Front-End Electronics

Thursday, 26 May 2011

Photon Science - Aula 3 (08:30 - 10:30)

time	[id] title	presenter
08:30	[17] Next generation ASIC for photon counting X-ray detectors	DINAPOLI, Roberto
09:00	[18] Adaptive Gain Integrating Pixel Detector	Dr MARRAS, Alessandro
09:30	[19] The DSSC project for XFEL: DEPFET and readout ASIC	PORRO, Matteo
10:00	[42] The LPD detector and readout chip for XFEL	HART, Matthew

Photon Science - Aula 3 (11:00 - 12:00)

time	[id] title	presenter
11:00	[20] STARX-32: a 3 cm ² Mixed Signal ASIC with Spectroscopic-grade Front-Ends and on-chip parallel A/D conversion for High speed X-ray Imaging with Semiconductor Pixel Detectors	BERTUCCIO, Giuseppe
11:30	[44] ASTEROID and VERITAS: two multi-channel ASICs for the readout of DEPFET arrays and pnCCDs for X-ray imaging, spectroscopy and synchrotron applications. Experimental results and new designs	HERMANN, Sven