

8th International Meeting on Front-End Electronics

Wednesday, 25 May 2011

ASIC development for LHC/SLHC - Aula 3 (08:30 - 10:30)

time	[id] title	presenter
08:30	[4] The FE-I4 chip for ATLAS	MENOUNI, Mohsine
09:00	[5] FEI4 chip in Chartered 130 nm	CLEMENS, Jean-Claude
09:30	[6] Timepix2/Velopix	LLOPART CUDIE, Xavier
10:00	[8] CBC (CMS Binary Chip) design for Tracker upgrade	JONES, Lawrence

ASIC development for LHC/SLHC - Aula 3 (11:00 - 11:30)

time	[id] title	presenter
11:00	[7] DC-DC conversion ASIC developments	MICHELIS, Stefano