



Contribution ID: 3

Type: **Oral**

MONOLITH - picosecond time stamping capabilities in fully monolithic highly granular silicon pixel detectors

Thursday, 2 March 2023 12:10 (20 minutes)

The MONOLITH ERC Advanced project aims at producing a monolithic silicon pixel ASIC with 50 μ m pixel pitch and picosecond-level time stamping. The two main ingredients are low-noise, fast SiGe BiCMOS electronics and a novel sensor concept, the Picosecond Avalanche Detector (PicoAD). The PicoAD uses a patented multi-PN junction to engineer the electric field and produce a continuous gain layer deep in the sensor volume. The result is an ultra-fast current signal with low intrinsic jitter in a full fill factor and highly granular monolithic detector. Testbeam measurements show that the proof-of-concept prototype is fully efficient and achieves time resolutions of 17ps averaged on the pixel surface, with 13ps at the center of the pixel and 25ps at the pixel edge.

A second prototype without gain layer but with improved electronics provides 20ps time resolution.

Primary author: Mr MILANESIO, Matteo (Universite de Geneve (CH))

Co-authors: PICARDI, Antonio (Universite de Geneve (CH)); IACOBUCCI, Giuseppe (Universite de Geneve (CH)); PAOLOZZI, Lorenzo (CERN); Dr CARDELLA, Roberto (Universite de Geneve (CH)); ZAMBITO, Stefano (University of Geneva); Mr MORETTI, Théo (Universite de Geneve (CH))

Presenter: Mr MILANESIO, Matteo (Universite de Geneve (CH))

Session Classification: CMOS

Track Classification: CMOS