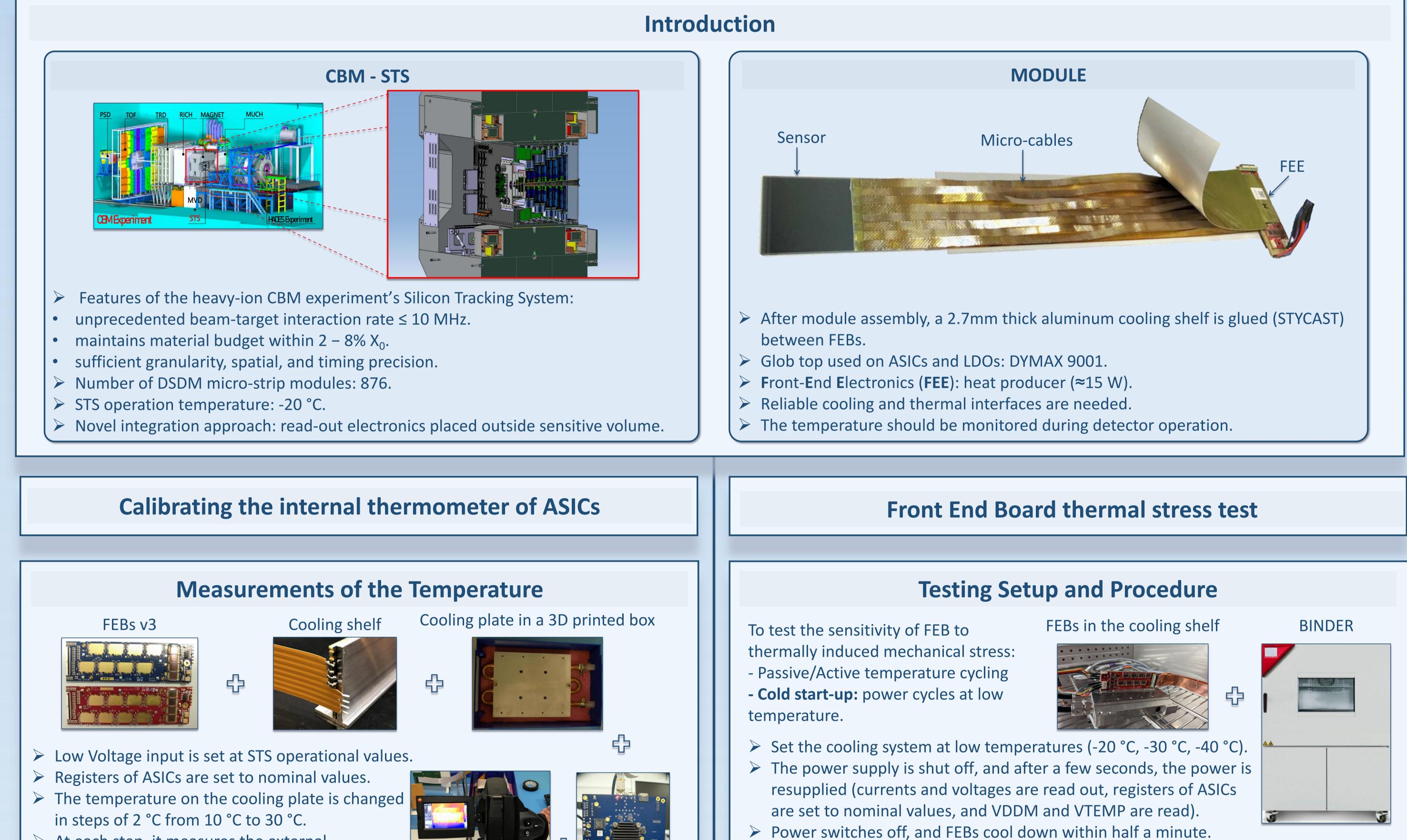
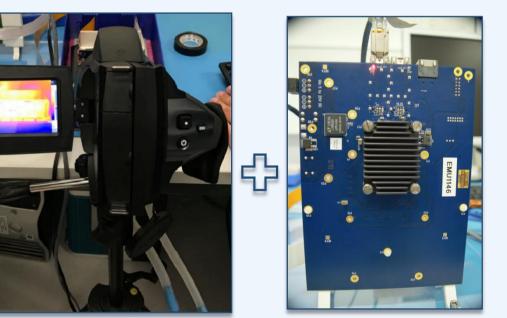
Temperature calibration and thermal stress tests of the Front-End Electronics of the CBM Silicon Tracking System

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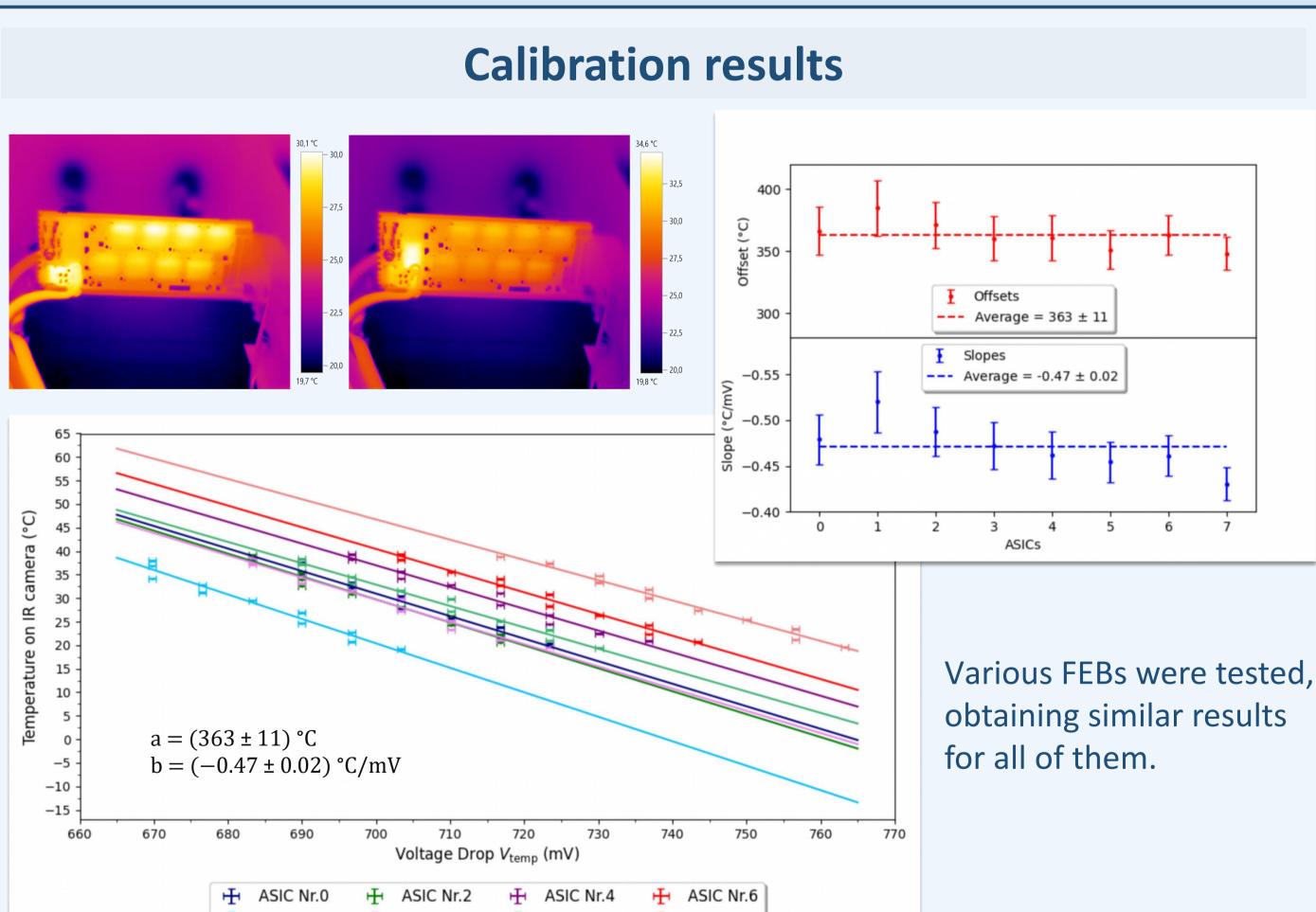


- > At each step, it measures the external temperature of the ASIC and its internal temperature.



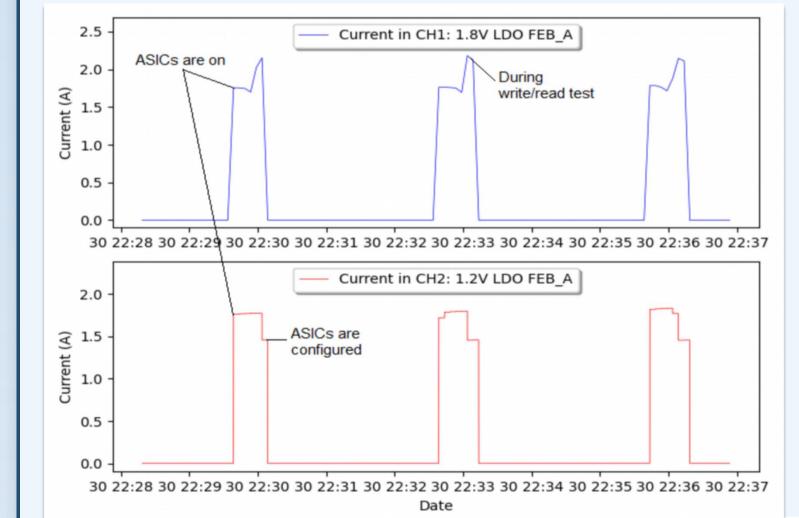
> These two temperatures have a linear dependence.

EMUxGBT Thermal camera



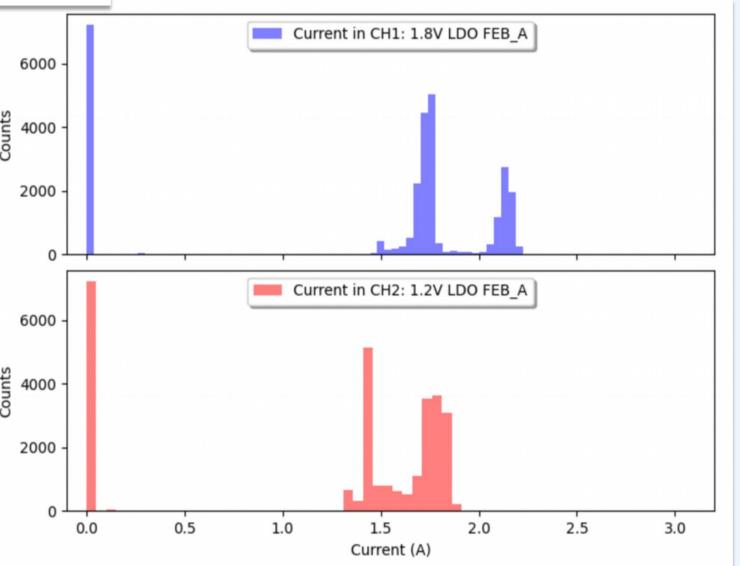
Observables during cycling

This process was repeated more than 1100 cycles for same sets.



- More than 50 passive cycles were done in previous measurements.
- More than 100 active cycles without failures.
- > We expect our electronics to stand frequent power cycles during the detector operation.

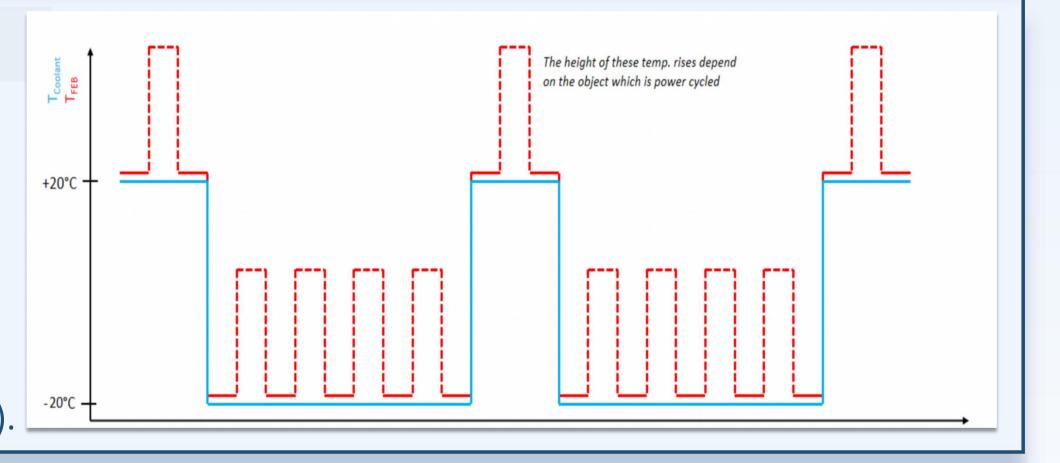
- For most of the FEBs, the power consumption behaved normally.
- Deviation for power consumption indicates malfunctioning.
- Observation of other measured parameters helps investigating the source of the failure.



ASIC Nr.1 ASIC Nr.3 H ASIC Nr.5 H ASIC Nr.7

Conclusions + Next Steps

- Calibration parameters have been used in lab setups (Thermal cycling and Module operation testing of the E16 experiment) and will be used in the final STS.
- > FEBs failures occurred during thermal stress procedures, most of them due to malfunctioning FEB powering.
- **Glob top protection of the LDO voltage regulator** wire bonds is suspected of causing problems.
- \succ As next steps: a conclusive set of tests with the newest
- available material will be performed:
- FEB8-v2 and v3 with the newest front end chip SMX2.2.
- 4 FEBs to be tested simultaneously .
- Using the first of series production components for further testing campaigns.
- > Cold start-up cycling to be performed:
- With coolant at -20 °C (updated operation temperature).





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