# High resolution imaging and timing UNIVERSITY OF **LEICESTER** using capacitive division

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#### Abstract

The capacitive division image readout (C-DIR) is a mechanically and electronically simple charge centroiding readout for single photon imaging detectors such as microchannel plate (MCP) detectors. Its purely capacitive nature endows it with a) very high signal bandwidth allowing MCP-limited time resolution, and b) low capacitance measurement nodes, allowing improved signal to noise charge measurement and correspondingly finer spatial resolution at high throughput. We describe an implementation of an MCP detector with C-DIR, optimised to provide combined high spatial and temporal resolution in single photon counting operation. The C-DIR is instrumented with high-speed front-end electronics utilising a fast waveform digitizer with a sample rate in excess of 1Gsample/s. We present results of the spatial and temporal resolution, and throughput of the detector system, and discuss the possible design Major noise sources limiting resolution:

- Electronic noise dominated by readout capacitance
  - C-DIR typically 10-20 pF
- Partition noise Poisson statistics of quantised charge collection
  - Not present with induced charge technique used in C-DIR
- > Charge centroid error statistical uncertainty in charge centroid
  - Very small scales as
  - footprint size and  $\sqrt{Q}$
- > Waveform capture + digital filtering  $\rightarrow$  flexible analysis



A schematic of the C-DIR capacitive network showing the readout nodes

variations which trade off performance parameters against each other.

Spatial resolution/throughput performance envelope using the same raw data

### Measurements



- > C-DIR close coupled to Amptek A250 to minimize input capacitance
- > A250s have very low noise ~500 electrons FWHM with C-DIR
- > A pinhole mask was illuminated with a 405 nm pulsed laser via a diffuser

> The mask has 50  $\mu$ m pinholes at 1 mm pitch in the centre, and 100  $\mu$ m pinholes in arc

> The mask was held against the input window – no allowance for parallax or diffraction made



Sample of image data analysed. The missing area is due to a compromised photocathode

C-Dir waveforms captured on LeCroy 2GSa./s 12-bit 'scope'

The illumination setup with 2 lasers: one with spatial filter for spot projection and one for mask illumination

#### Resolution vs shaping time - 1.2e6 gain

## Technique

#### "Image Charge"

- Charge localised on a resistive layer
- Charge signal capacitively induced onto C-DIR readout
- Signal divided among corner nodes

#### Advantages:

- Simplicity prototyping PCB
- Small capacitive load low noise
- Capacitive high speed
- Excellent spatial and temporal resolution – broad performance envelope
- Disadvantages
- > Non-linearity correctable





- Increased perimeter capacitance using surface mount capacitors
- > The wires identify the readout nodes

#### **Optimised C-DIR**

- > 2-layer C-DIR with larger edge electrodes to produce larger perimeter capacitance
- > Optimised for linearity
- > 25 mm<sup>2</sup> active area visible as the checkerboard patterned area
- Three layers of isolated conductors separated by thin insulator

#### C-DIR device using prototyping breadboard



Optimised C-DIR with improved linearity

### **Temporal resolution**

- C-DIR is a purely capacitive readout technique it's very fast
- MCP-limited time resolution possible
- Simultaneous imaging and picosecond timing can be achieved
- > So far demonstrated using NINO amplifier/discriminator + HPTDC

- > Waveform sequenced saved to PC and digitally filtered with Semi-Gaussian filter using scipy in python
- Shaping times: 10, 20, 40, 80, 160, 500, 1000, and 5000 ns
- Four peak heights were determined per event
- Pinhole FWHM data was analysed for pinholes at the centre and edge of the imaging area – see data right
- > These are the first preliminary results optimised with a closecoupled low noise preamplifier but are a good match to theory









#### Pinhole mask analysis

- **Further work**
- > Optimise spatial resolution and timing in large readout arrays – SEE
- > 4096 pixel array (right) instrumented every 9 pixels > passive pixels provide capacitive charge sharing network
- Instrument using multichannel ASICs:

Charge amplitude for centroiding obtained from time-over-threshold



The event timing resolution from the C-DIR detector with A test mask image of 0.5 mm holes at 1mm pitch. a FWHM of ~200 ps with a broad, low amplitude shoulder Distortion is caused by the NINO input impedance

Instrumented node

1

An existing 4096 pixel MCP detector readout developed by Photek Ltd.

SAMPIC waveform digitizer > TOFPET ASIC – using ToT See Amelia Markfort poster this session – "Can machine learning reduce the number of anode readouts .."



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