Session Program

30 January 2023 to 3 February 2023

28nm Mixed-Signal Design Workshop

Analog design

CERN, 572/R-013

Monday 30 January

13:30

Analog design
Session | **Location:** CERN, 572/R-013

13:30–14:45 **Analog simulation with Explorer and Assembler**

Speaker
Helga Dornelas

14:45

15:00

Analog design
Session | **Location:** CERN, 572/R-013

15:00–17:00 **Lab session**

Speaker
Helga Dornelas

17:00

Tuesday 31 January

09:00

Analog design
Session | **Location:** CERN, 572/R-013

09:00–10:15 **Mixed-signal simulation with Xcelium and Virtuoso**

Speaker
Helga Dornelas

10:15

10:30

Analog design
Session | **Location:** CERN, 572/R-013

10:30–12:00 **Lab session**

Speaker
Helga Dornelas

12:00

13:30

Analog design
Session | **Location:** CERN, 572/R-013

13:30–14:45 **Analog Backend VXL Best Practices**

Speaker
Philippe Carriere

14:45

15:00

Analog design
Session | **Location:** CERN, 572/R-013

15:00–17:00 **Lab session**

Speaker
Philippe Carriere

17:00

Wednesday 1 February

09:00

Analog design
Session | **Location:** CERN, 572/R-013

09:00–10:15 **Lab session**

Speaker
Philippe Carriere

10:15

10:30

Analog design
Session | **Location:** CERN, 572/R-013

10:30–12:00 **DRC/LVS with PVS**

Speaker
Philippe Carriere

12:00

13:30

Analog design
Session | **Location:** CERN, 572/R-013

13:30–14:45 **Lab session**

Speaker
Philippe Carriere

14:45