



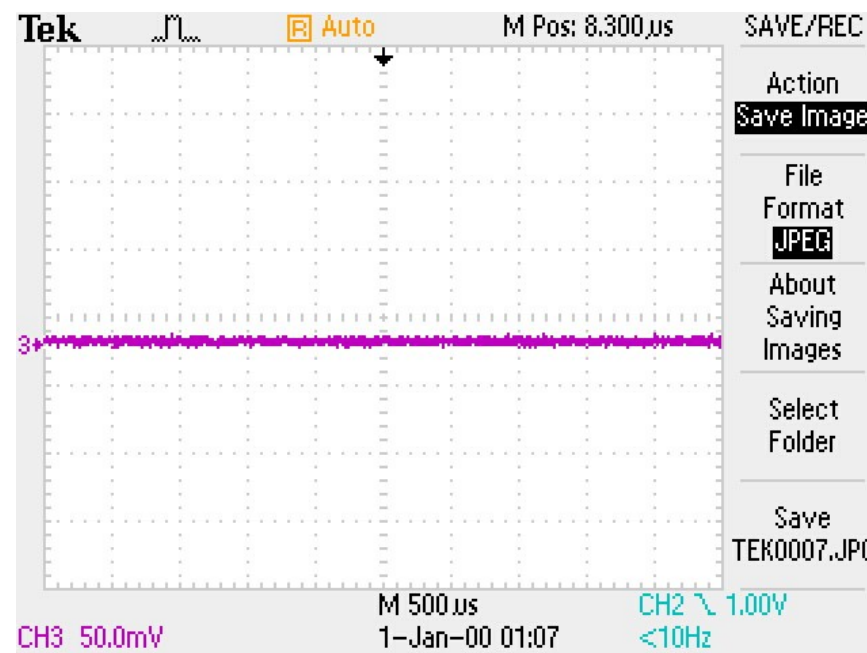
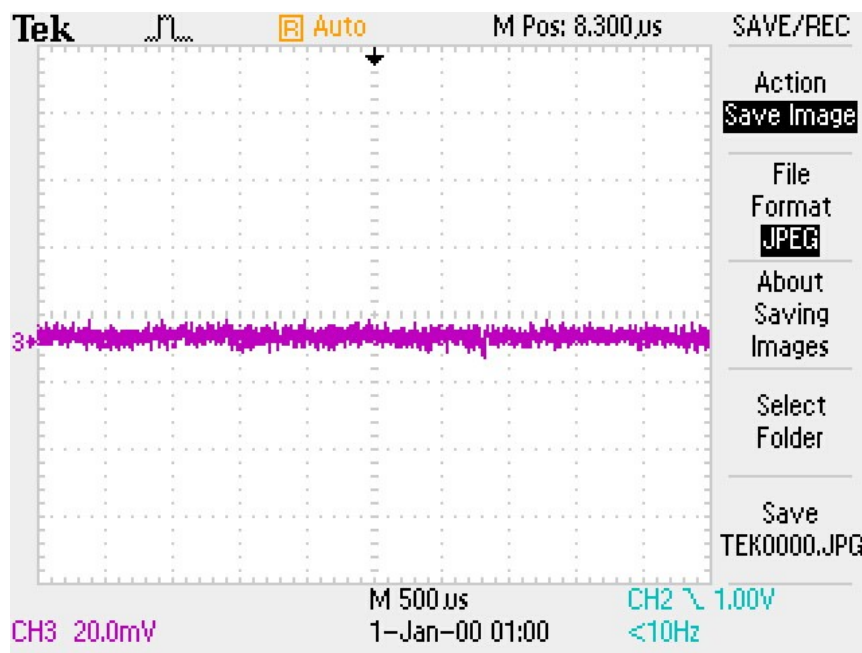
RD50-MPW3 NOISE MEASUREMENTS

19/01/23



Helmut's standalone firmware

Bernhard's peary firmware

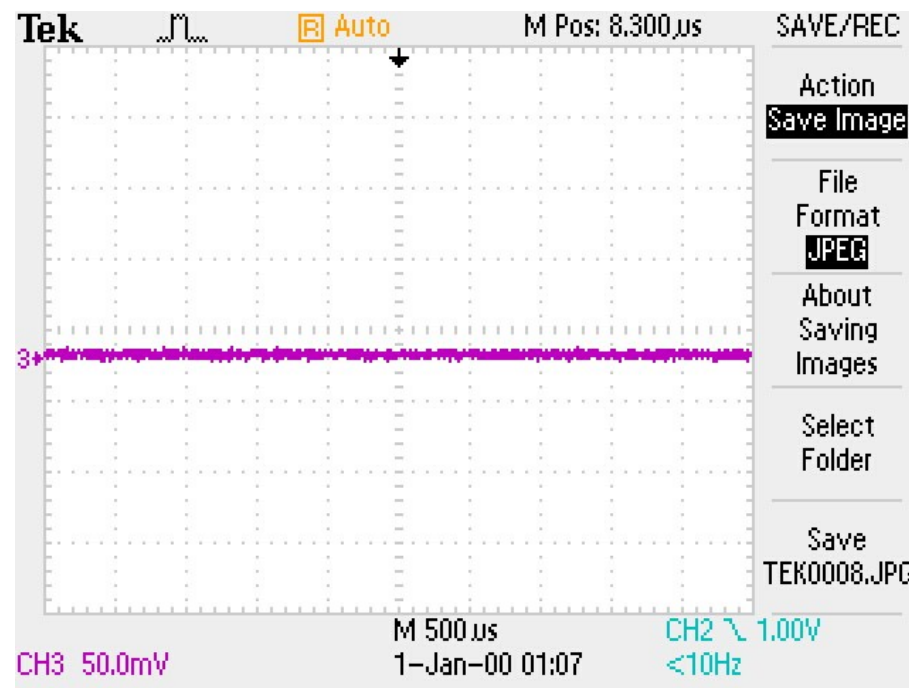
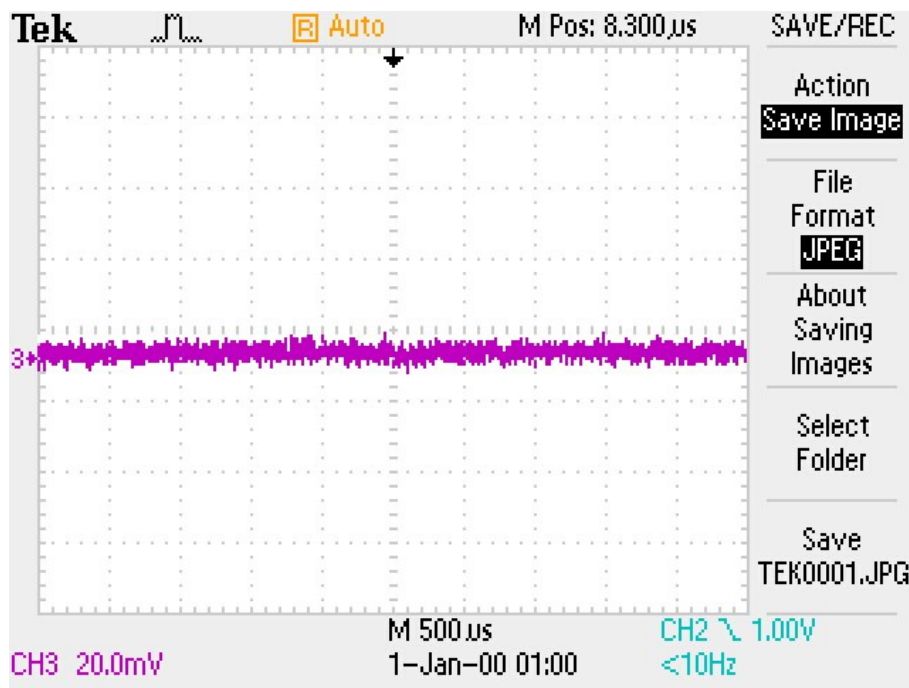


- Comparison of SFOUT (analogue output) noise between the 2 firmwares as a function of threshold voltage



Helmut's standalone firmware

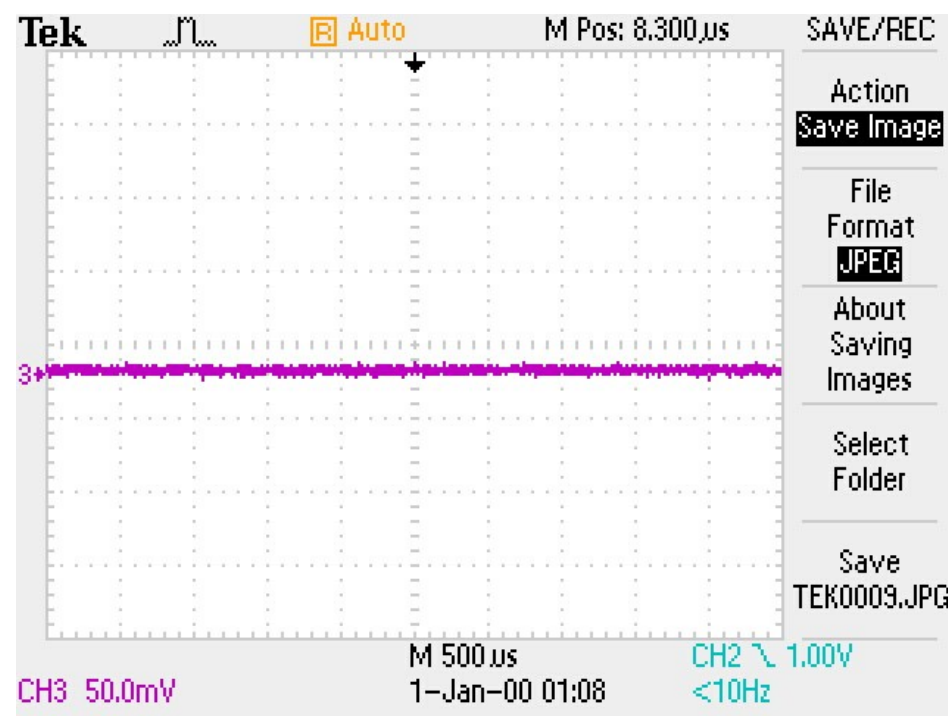
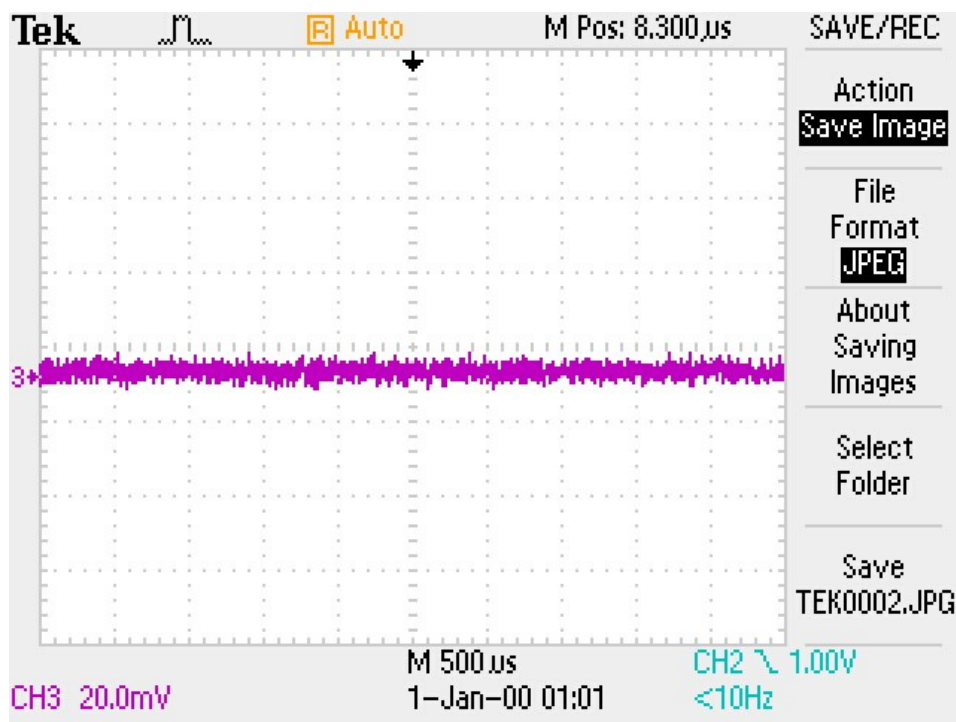
Bernhard's peary firmware





Helmut's standalone firmware

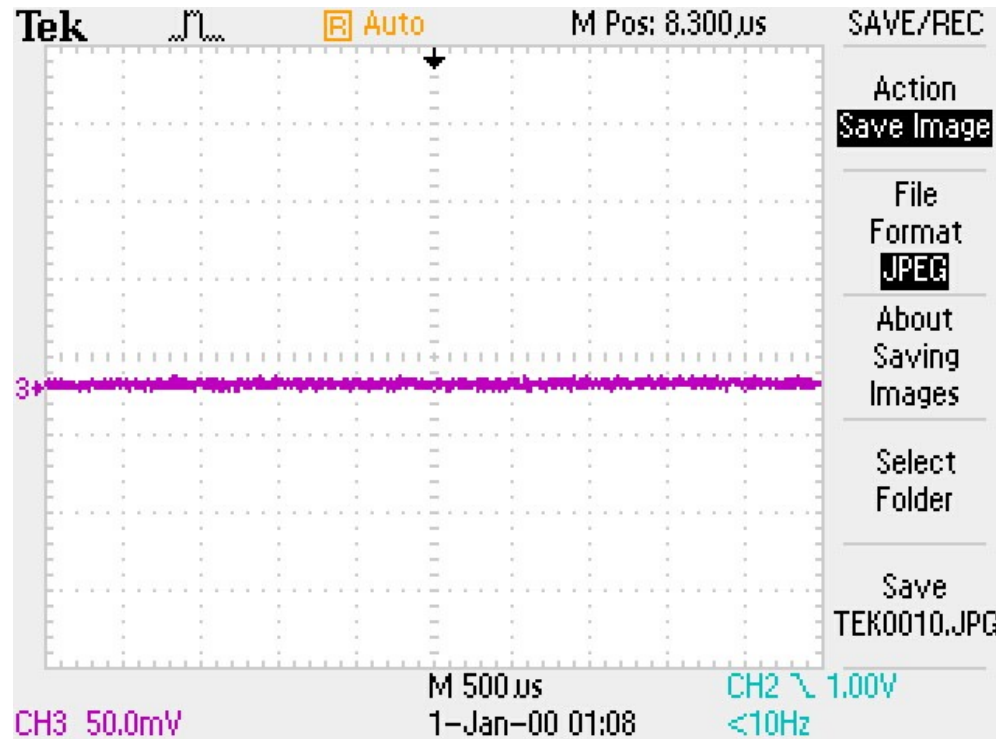
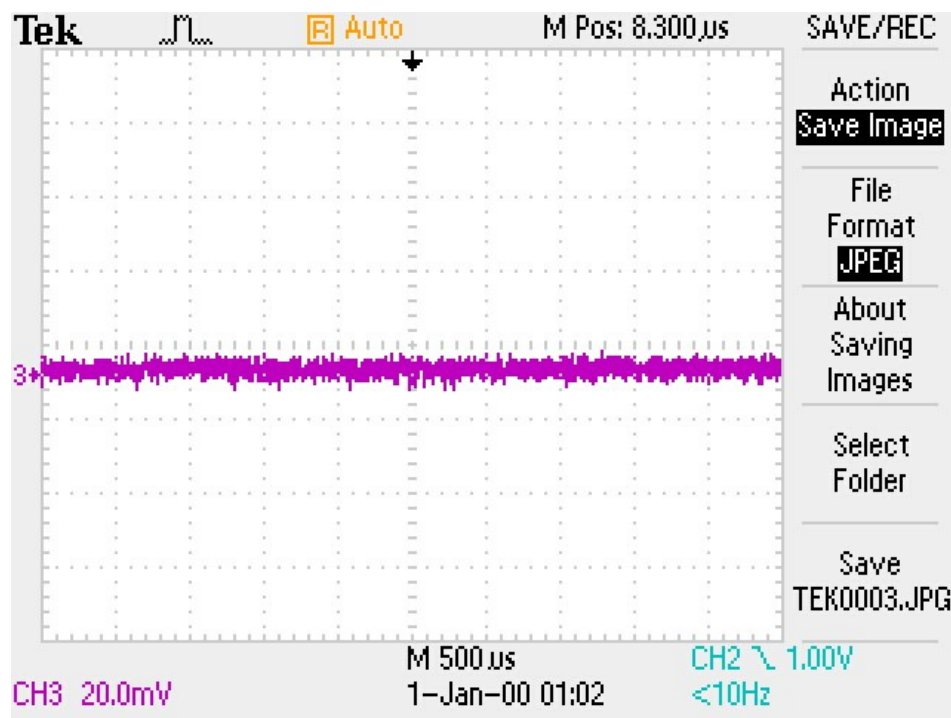
Bernhard's peary firmware





Helmut's standalone firmware

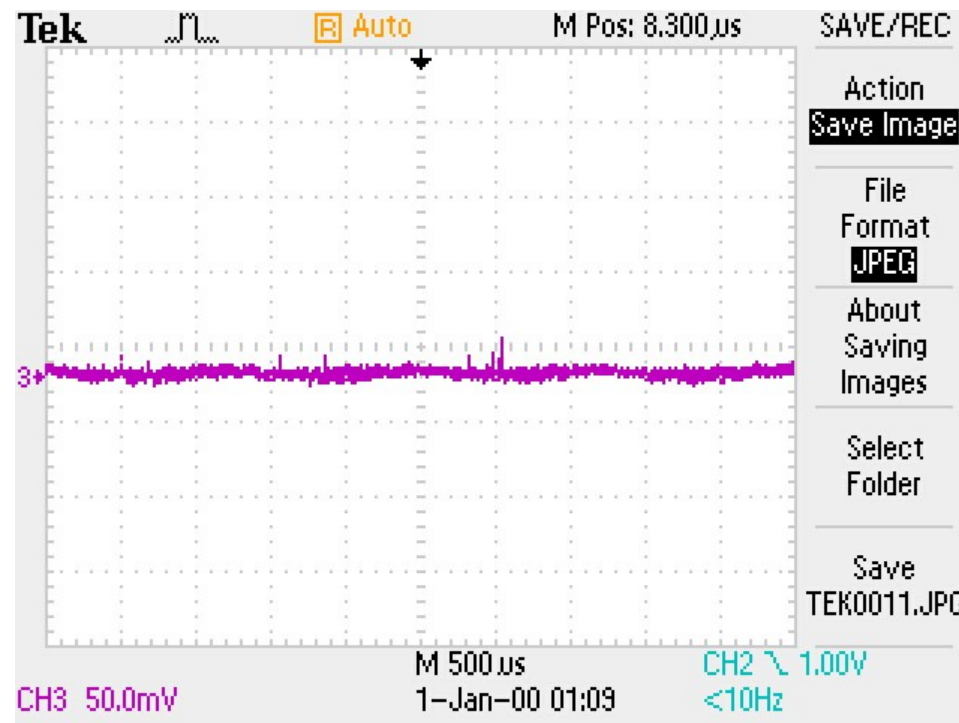
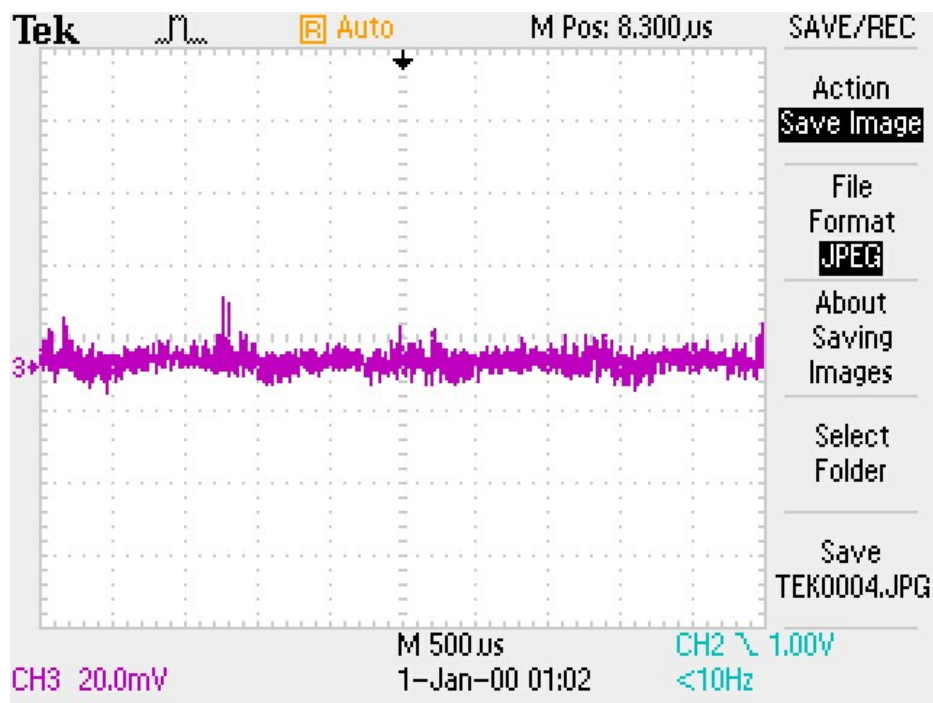
Bernhard's peary firmware





Helmut's standalone firmware

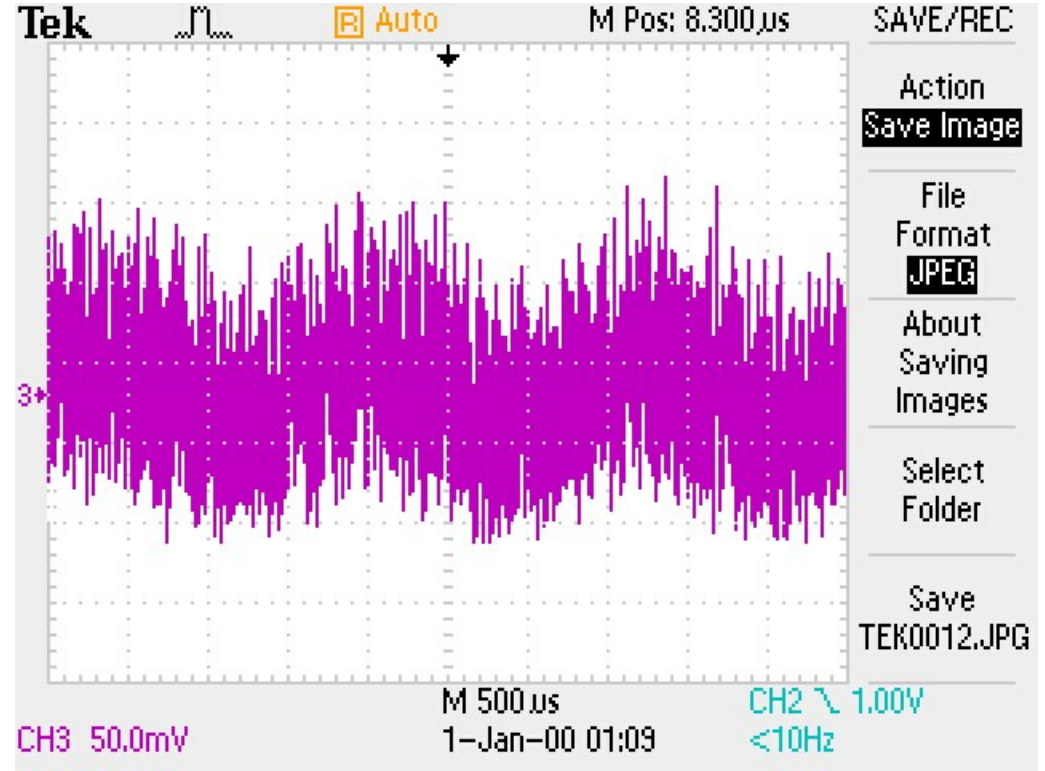
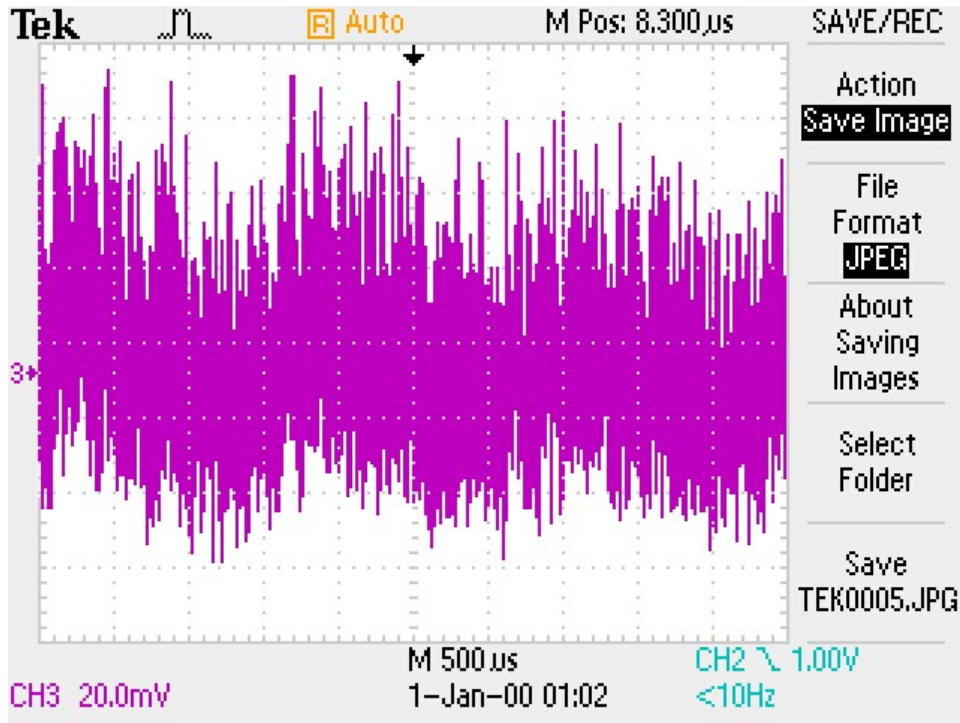
Bernhard's peary firmware





Helmut's standalone firmware

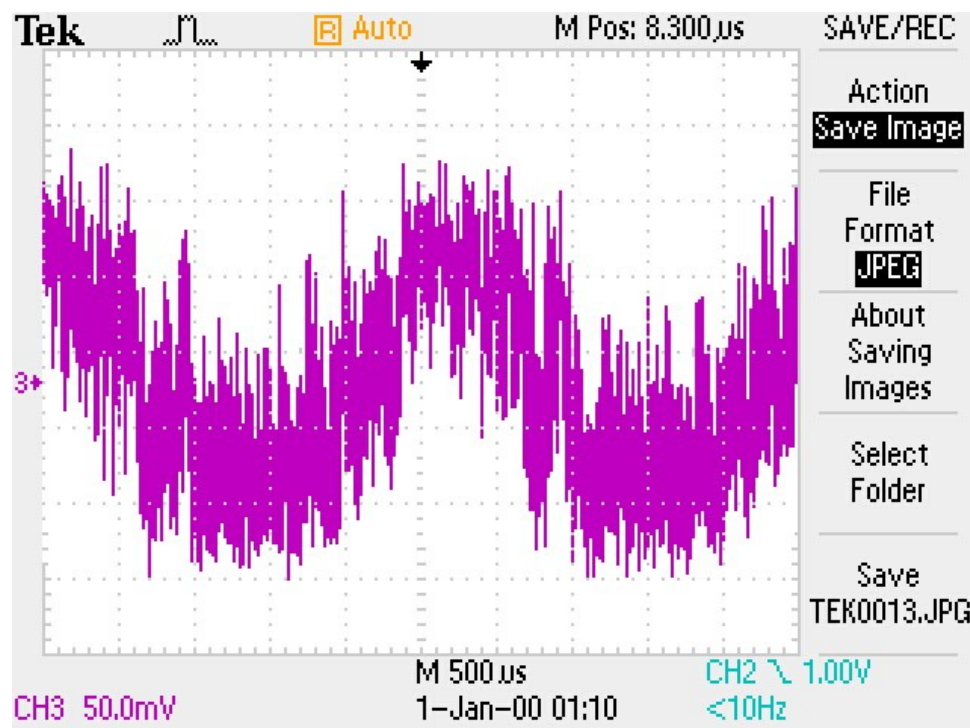
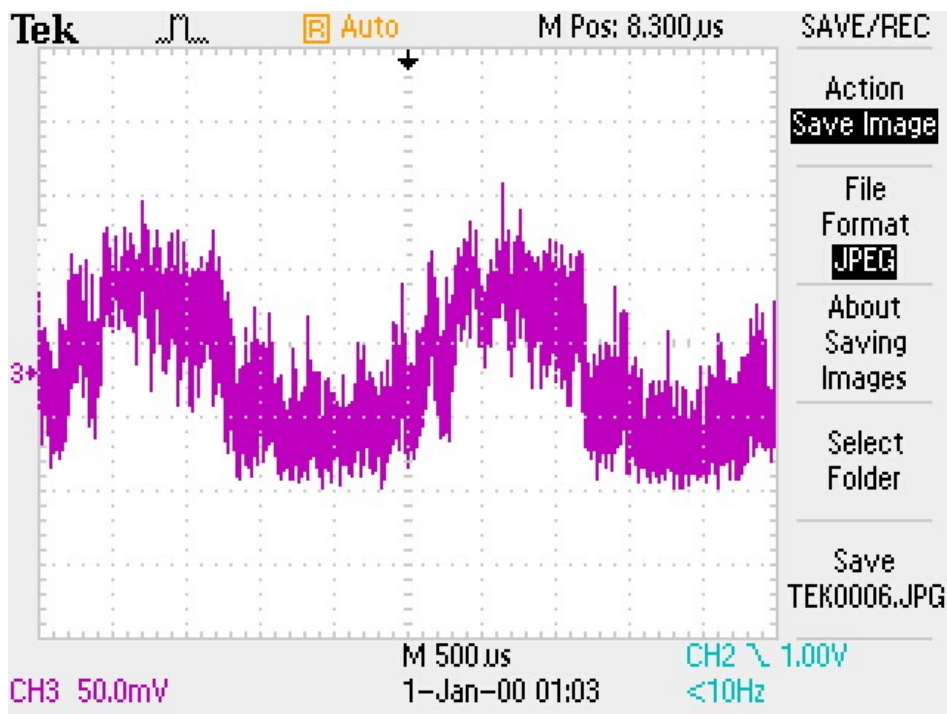
Bernhard's peary firmware





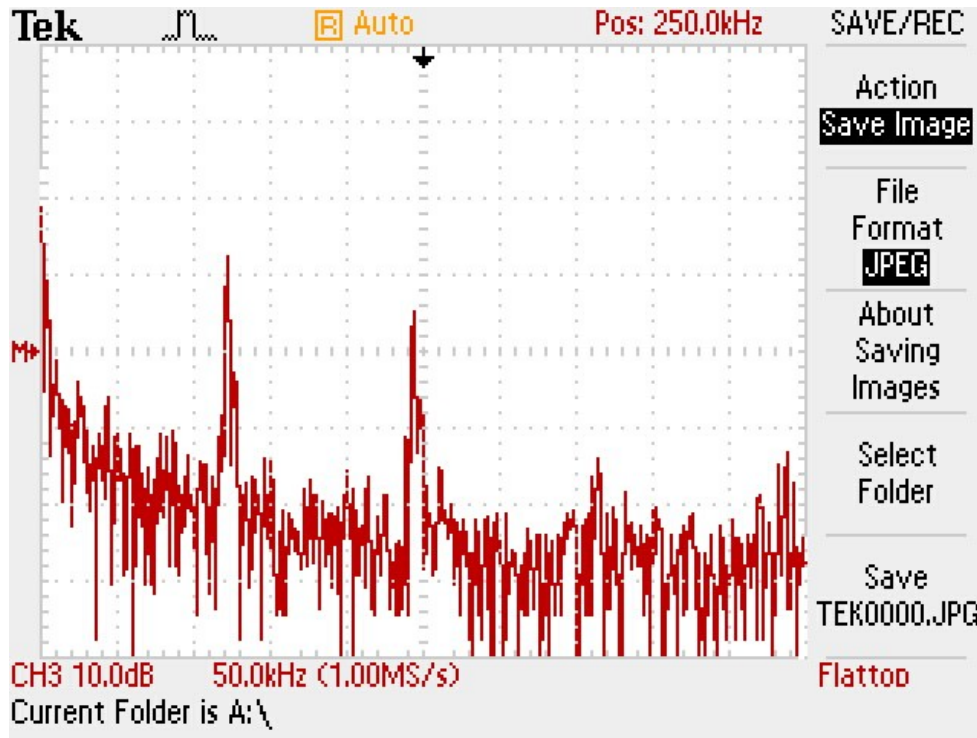
Helmut's standalone firmware

Bernhard's peary firmware

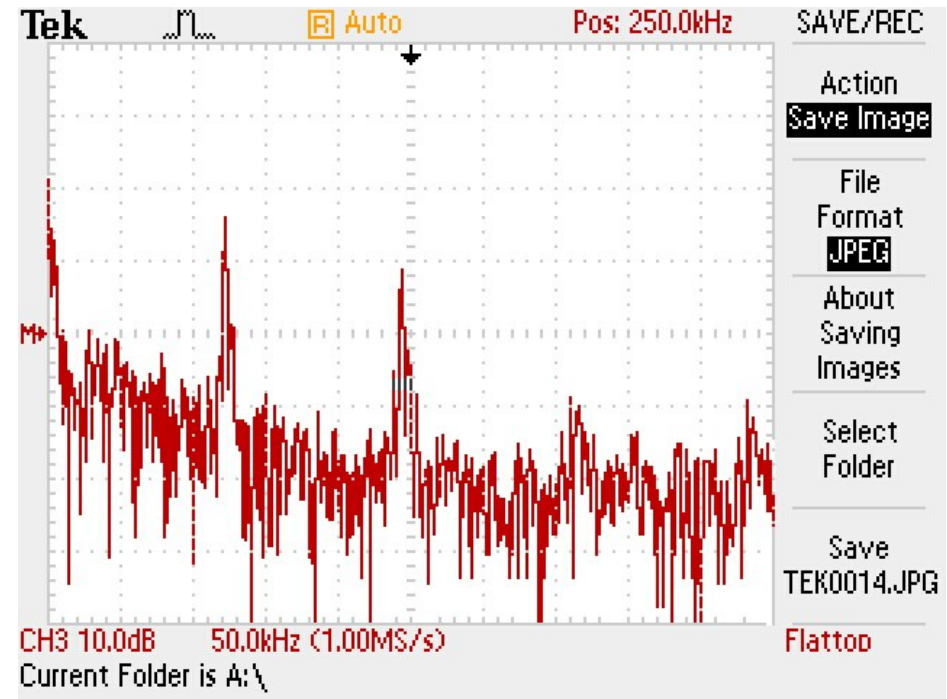




Helmut's standalone firmware



Bernhard's peary firmware



- Comparison of SFOUT (analogue output) noise between the 2 firmwares @0.9V threshold in frequency domain
- Low frequency noise components at approximately the same frequencies for both



- Both firmwares show noise on SFOUT increases as a function of threshold
- Slightly less noise observed in the standalone firmware (only slightly)
- Noise appears to coincide with comparator triggering (likely pickup from digital activity)
- All bias voltages and power supply levels measured (VDDA, VDDC, 2V5, 1V3 etc) – No significant differences found between voltage levels (all within 45mV)