

# Design patterns and best practices

*Thursday, 15 June 2023 11:15 (1 hour)*

Programming for Heterogeneous Architectures - lecture 4

- Good practices: single precision, floating point rounding, avoid register spilling, prefer single source
- Other standards: SYCL, HIP, OpenCL
- Middleware libraries and cross-architecture compatibility
- Reusable parallel design patterns with real-life applications

## Summary

**Presenter:** CAMPORA, Daniel (University of Maastricht)