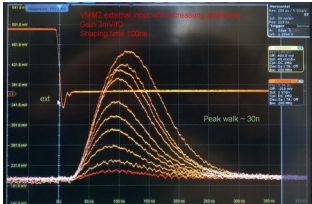


DRD1 Community Meeting WG5 Electronics



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EP-DT
Detector Technologies

Plan for the report

- Main chapter on readout system:
 - Main components, known systems
 - Goals: charge sensitivity, photons, timing, cluster counting
- Common areas of interest identified for electronics:
 - ASICs: applications, availability, runs, operations, support
 - FPGAs: procurements, trigger and filtering algorithms
 - Front End cards
 - Back-end boards and DAQ
- Topics connected to the readout:
 - Grounding, electronic noise reduction, impedance matching, X-talk reduction.
- Big experiments uses custom electronics, but for smaller experiments and R&D groups the availability of easy to use systems and/or usage of simplified version of experiments systems:
 - SRS with up to date readout (optical readout) and concentrator systems. Application to large number of channels (calorimetry)
- a few smaller sections on other topics like HV, ampmeters and monitoring system