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## P1.48: A prototype Radiation Energy Measuring Integrated Circuit with an asynchronous current-pulse reset block providing analog-to-digital conversion in 28 nm CMOS

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In this work, we present a prototype Radiation Energy Measuring Integrated Circuit (REMIC). This chip has been fabricated in 28 nm CMOS node, works in single photon counting mode and consists of 100 pixels of size  $50\text{ }\mu\text{m} \times 50\text{ }\mu\text{m}$  (Fig. 1). Each pixel is equipped with a cascoded-inverter-based charge sensitive amplifier (CSA) with Krummenacher feedback [1], and three reset circuits: switch-based [2], click-clack [3] and current-pulse reset [4].

The working principle of the latter is to discharge the CSA feedback capacitor after a pulse by injecting small current pulses, each of which is counted by the pixel logic. Importantly, the number of pulses required to return the CSA output to baseline is proportional to the amplitude of the CSA output pulse and hence to the particle energy. The amount of the injected current can be regulated by a digital-to-analog converter (DAC), allowing a trade-off between speed of operation and resolution of energy measurement (Fig. 2).

Having 12-bit pixel counters responsible for current-pulses counting, REMIC enables fast and precise colorful imaging. Simulated static power consumption per pixel is  $7\text{ }\mu\text{W}$ , whereas dynamic power consumption depends on current-pulse reset configuration and may be about  $60\text{ }\mu\text{W}$  or less. The chip is currently under measurements, and in the contribution, we will present the most recent results, possibilities and conclusions.

[1] F. Krummenacher, Nucl. Instrum. Meth. A 305 (1991) 527

[2] H.-S. Kim et al., IEEE J. Solid-State Circuits 48 (2013) 541

[3] R. Kłeczek et al., ESSCIRC 2019—IEEE 45th Eur. Solid State Circuits Conf. (ESSCIRC), 85

[4] P. Kaczmarczyk and P. Kmon, 2023 JINST 18 C03010

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