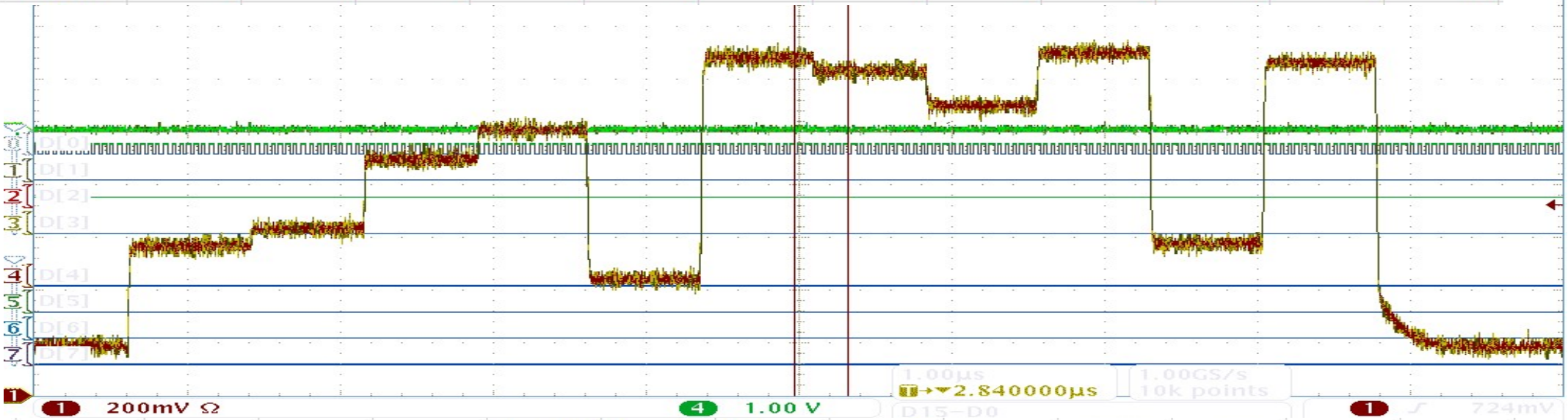
Bias Voltages [mV] Oscilloscope DVM

	VN_CS_BB	VN	VNCASC	VPBIAS	VNSF	VPTRIM	VPCOMP	VBLR	VPFB	VNFB	VSENSEBIAS	SFOUT_BUFF
320MHz ON	559	626	892	1003	422	1278	1225	1091	1307	560	1269	176
320MHz OFF	562	624	895	1002	424	1276	1223	1094	1303	562	1269	76
ON-OFF	-3	2	-3	1	-2	2	2	-3	4	-2	0	100

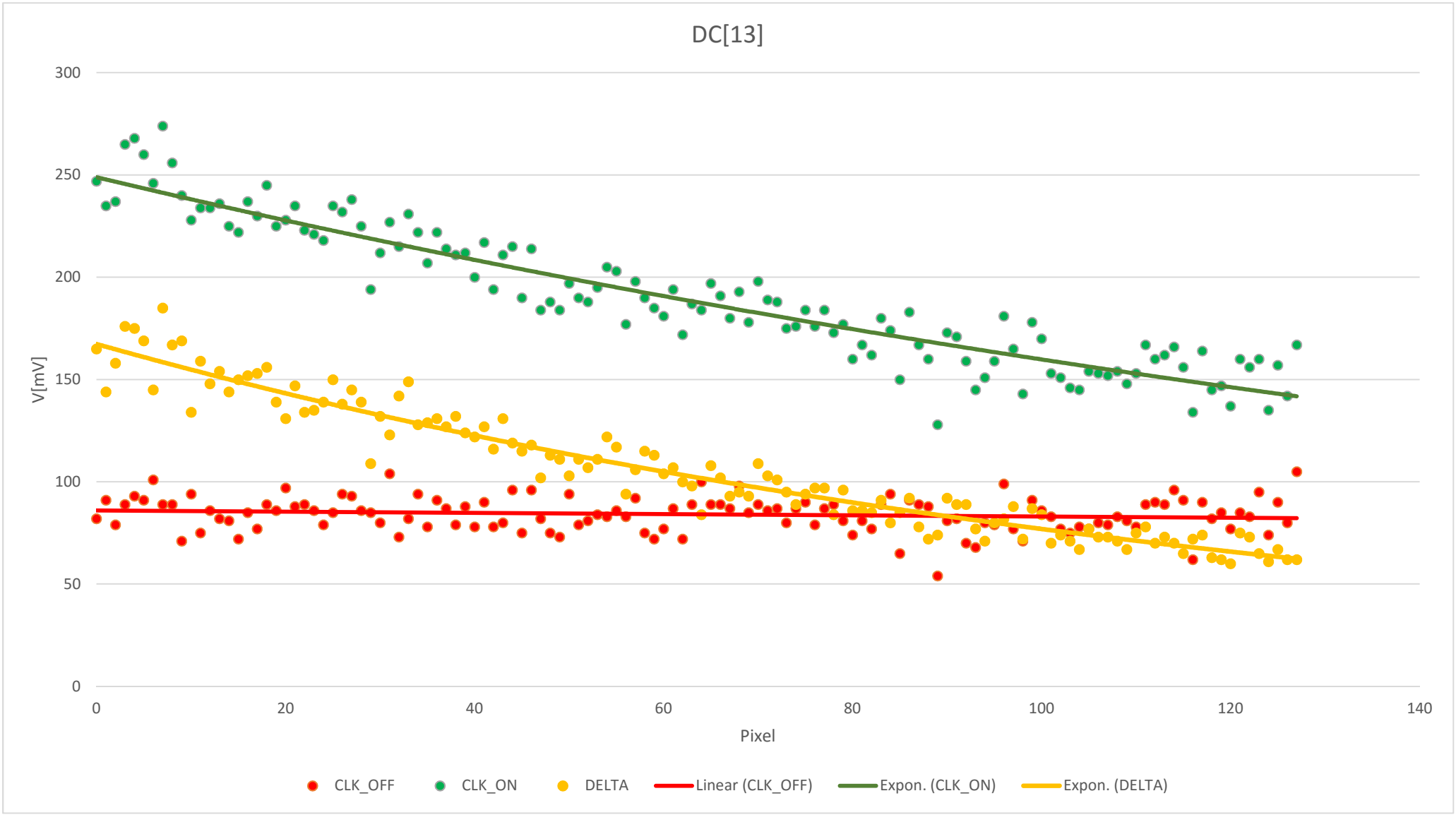


Bias Voltages [mV] Electrometer

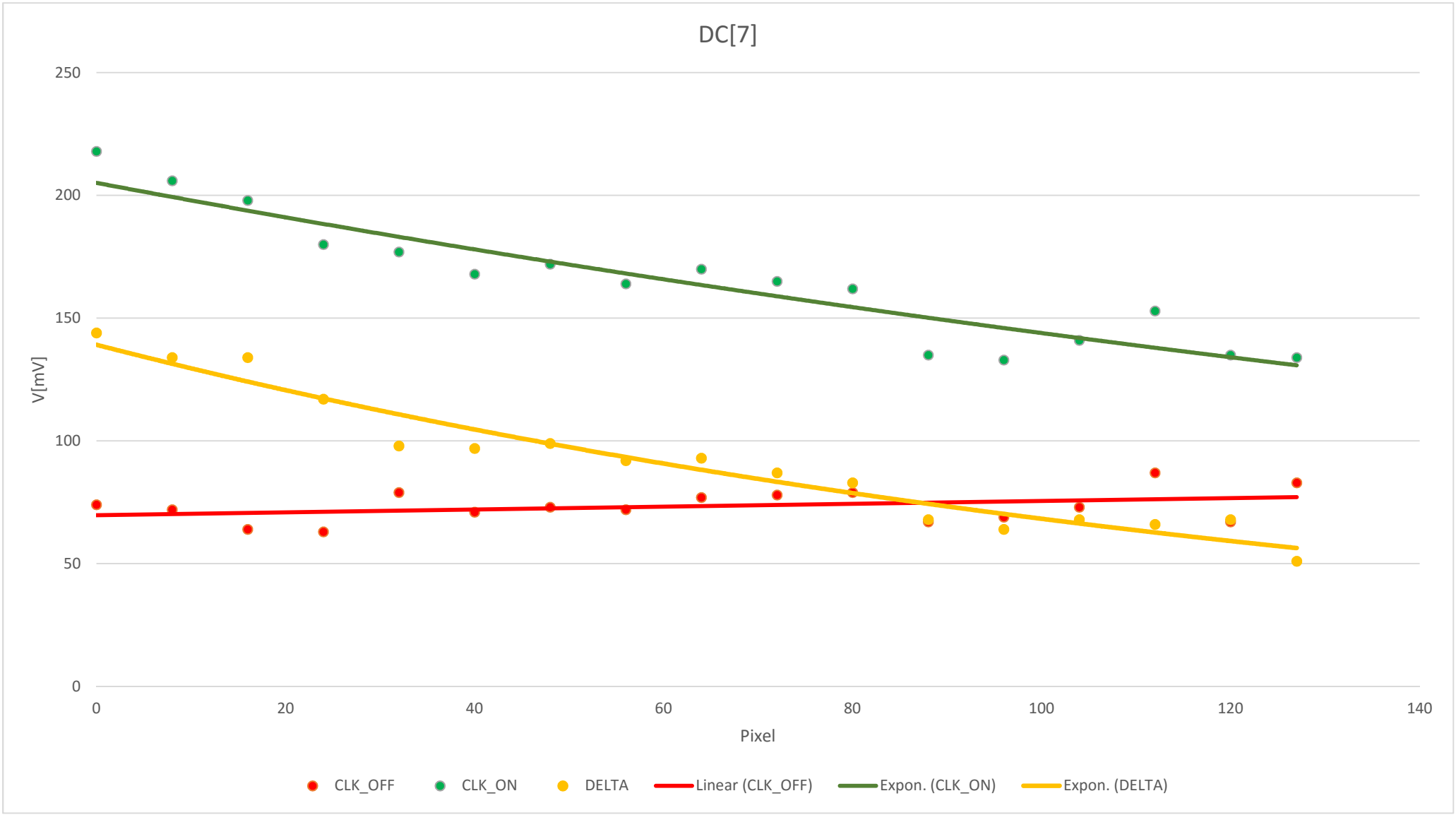
	VN_CS_BB	VN	VNCASC	VPBIAS	VNSF	VPTRIM	VPCOMP	VBLR	VPFB	VNFB	VSENSEBIAS	SFOUT_BUFF
320MHz ON	584	649	918	1031	445	1311	1256	1121	1342	585	1300	197
320MHz OFF	585	648	921	1030	446	1309	1254	1124	1337	587	1300	97
ON-OFF	-1	1	-3	1	-1	2	2	-3	5	-2	0	100

	Name	Nominal	Current	Voltage	Description
CH1	VPCOMP	44	10.1 μA	1.23 V	for comparator
CH2	VPTRIM	27	153 nA	1.24 V	for trim-DAC
CH3	VNSENSBIAS	13	2 nA	1.31 V	for DNWELL bias circuit
CH4	VBLR	25	6.6 nA	1.2 V	for high-pass filter
CH5	VNSF	18	1.03 μA	418.9 mV	for source follower
CH6	VNFB	45	400 nA	584.4 mV	for reset circuit
CH7	VPFB	25	6.6 nA	1.28 V	not used in this chip
CH8	VPBIAS, VNCASC	26	2.98 μA, 995 nA	1.02 V, 927 mV	for pre-amplifier
CH9	VN	42	6.02 μA	616 mV	for pre-amplifier
V_REF	VN_CS_BB	/	35 μA	540 mV	reference voltage

SFOUT offset CLK_ON CLK_OFF CLK_ON - CLK_OFF



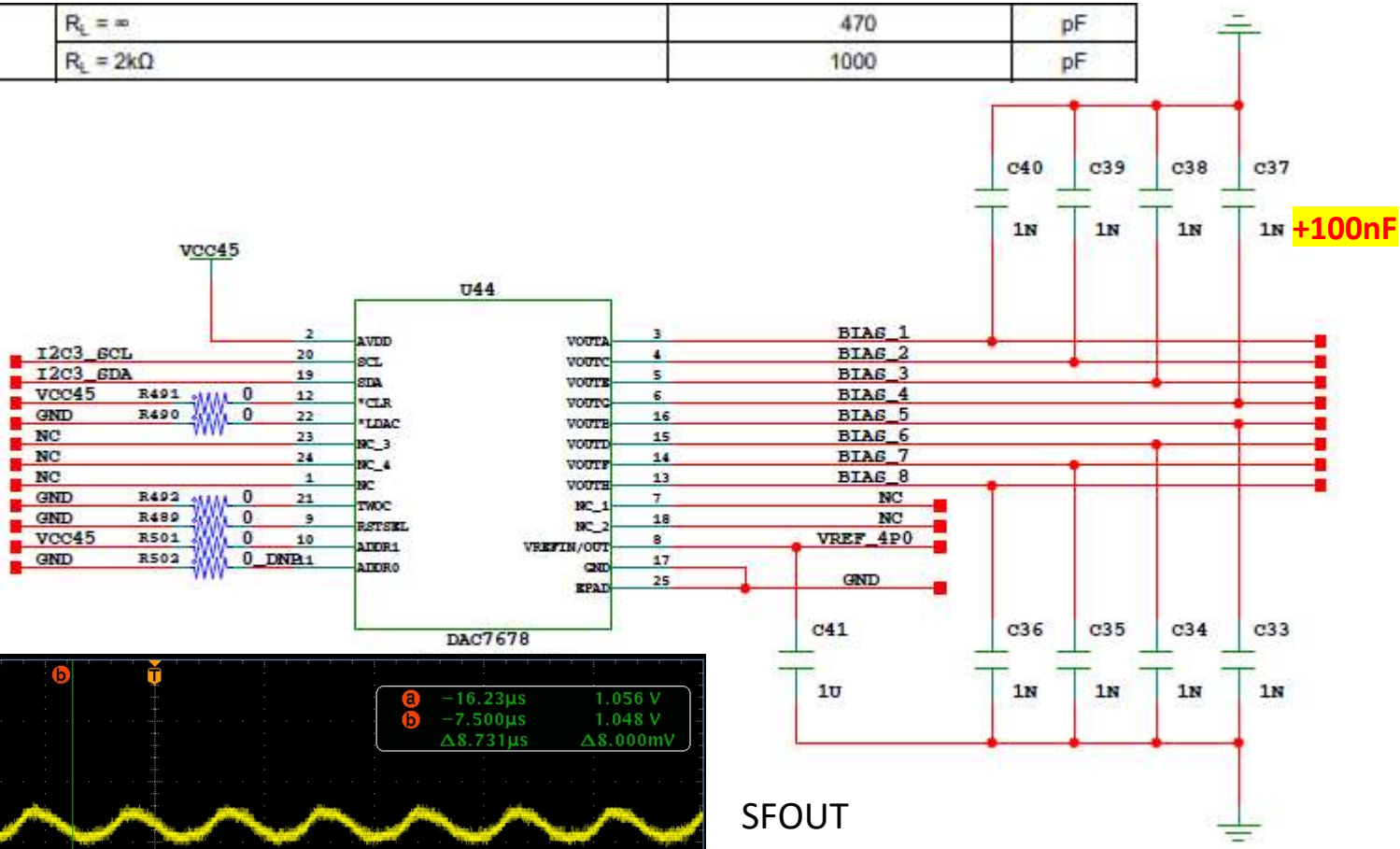
SFOUT offset CLK_ON CLK_OFF CLK_ON - CLK_OFF



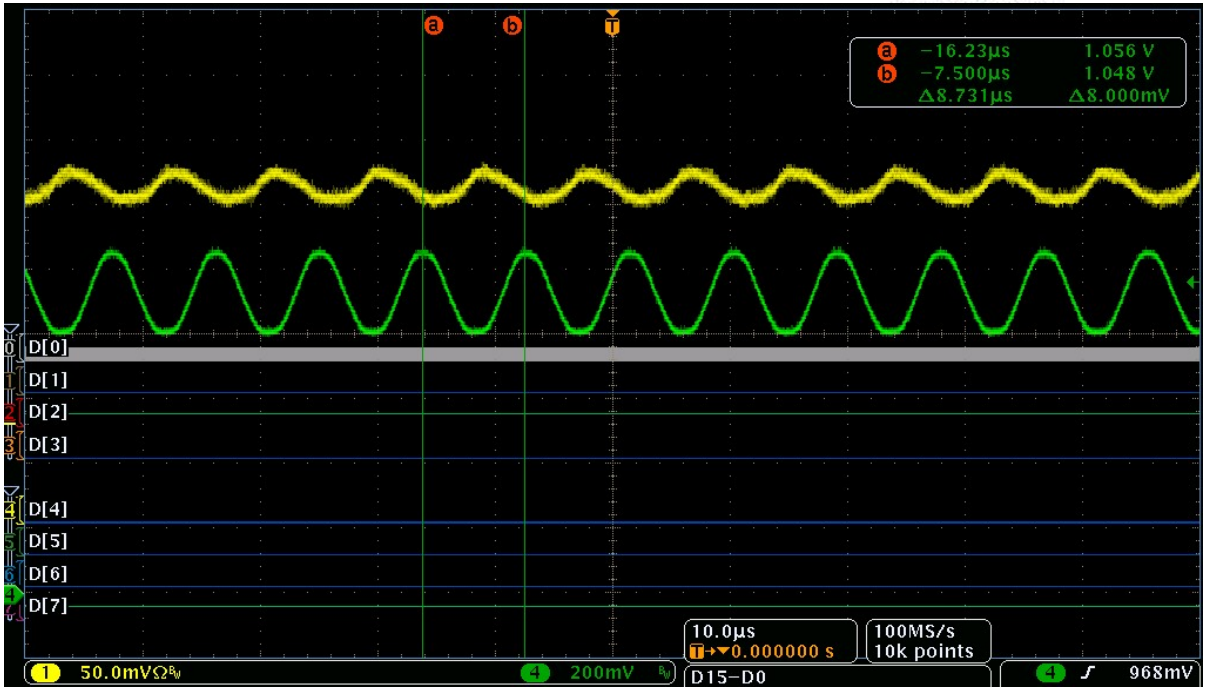
BL TH Measurements:

Capacitive load stability	$R_L = \infty$	470	pF
	$R_L = 2k\Omega$	1000	pF

CARboard



SFOUT



BL

BaseLine effect on SFOUT ????