Figure 1: HGCAL electronics system overview; aFE unit is shown on the left and a BE unit on the right. The final system will consist of around 30k FE units and 100 BE units.
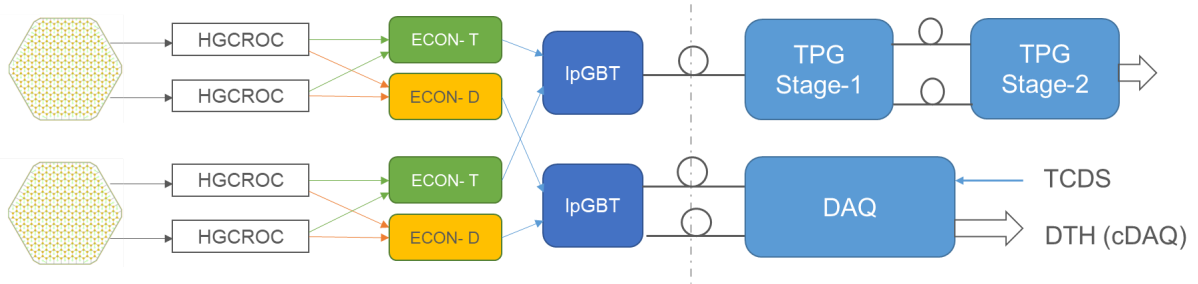

Figure 2: Diagram of the distribution of randomly produced TCs compared with the binomial expectation. The expected truncation from the ASIC buffer going full is observed.