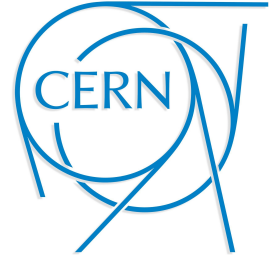




# ATLAS LAr Calorimeter Commissioning for LHC Run-3

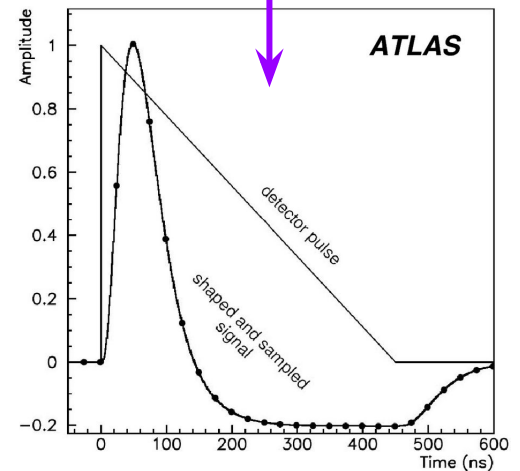
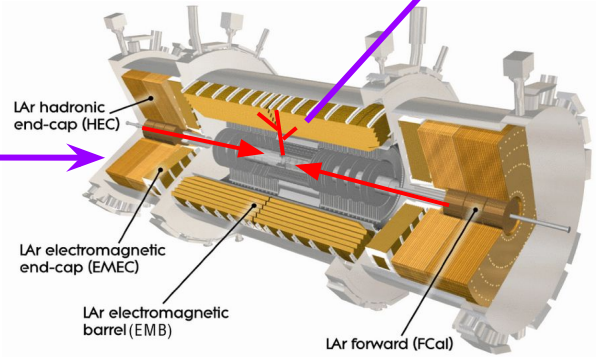
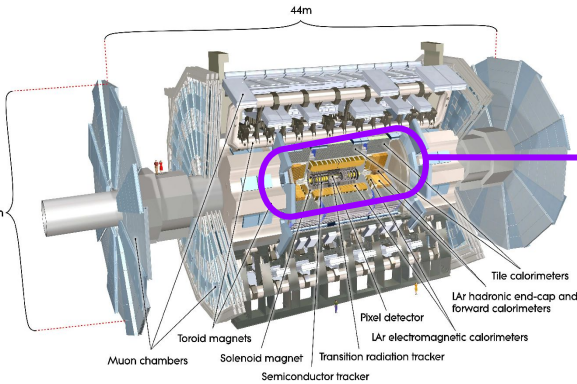
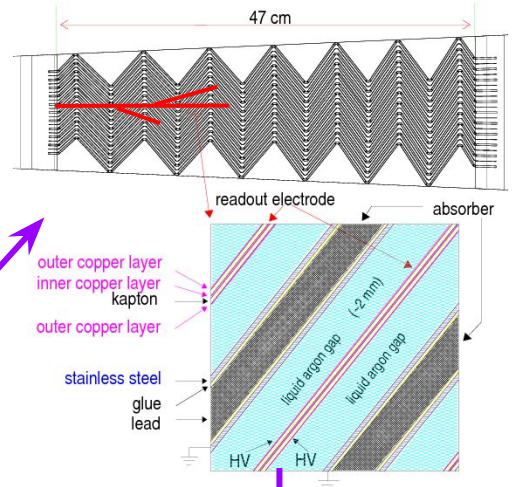


TWEPP 2023  
Florent Bernon  
On behalf of the Liquid Argon Calorimeter  
Group



# ATLAS Liquid Argon Calorimeter

- Used to measure the energies of electromagnetic particles (photons, electrons) and hadronic particles in forward region
- Provides signals of triggering
  - 40 million events per second
  - Electromagnetic shower ionised the liquid Argon
  - Induces triangular electric signal in electrode



# LHC to HL-LHC

Increase in luminosity in two phases in 2022 and 2029 to reach the HL-LHC

- Run 3  $\mu = 50$
- Run 4  $\mu = 200$

Requires an upgrade of the detectors

- The triggering system for the ATLAS Liquid Argon Calorimeter (LAr) was replaced during phase-I



# Phase-I of the LAr upgrade

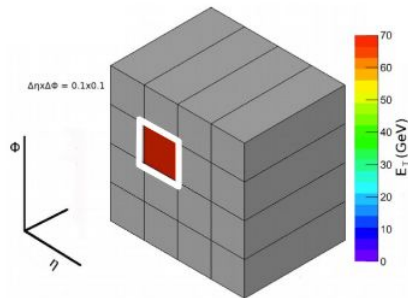
A new LAr trigger system for high pile-up environment with a total of 34048 SCs.

- Finer granularity than Trigger Towers.
- Better trigger energy resolution.
- Higher efficiency in selecting physics objects.

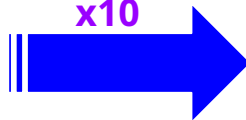
## Trigger Tower (TT)

- No longitudinal segmentation
- Fixed size in  $\Delta\eta \times \Delta\phi = 0.1 \times 0.1$
- Up to 60 cells from 4 layers
- Only ~5.4k TT from 180k cells
- Analog trigger

## Trigger Towers (TT)



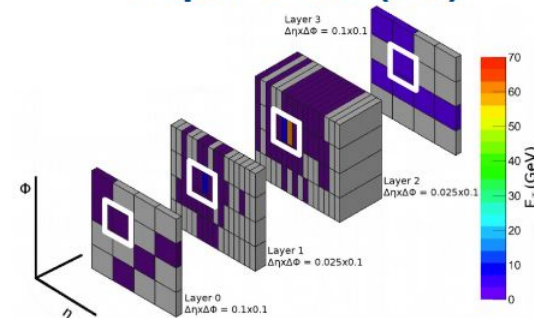
x10



## Super Cells (SC)

- Lateral & longitudinal segmentation
- Increased granularity in Front and Middle to  $\Delta\eta \times \Delta\phi = 0.025 \times 0.1$
- Up to 8 cells from 1 layer
- ~34k SC from 180k cells
- Digital trigger

## Super Cells (SC)



# New electronics for Phase-I

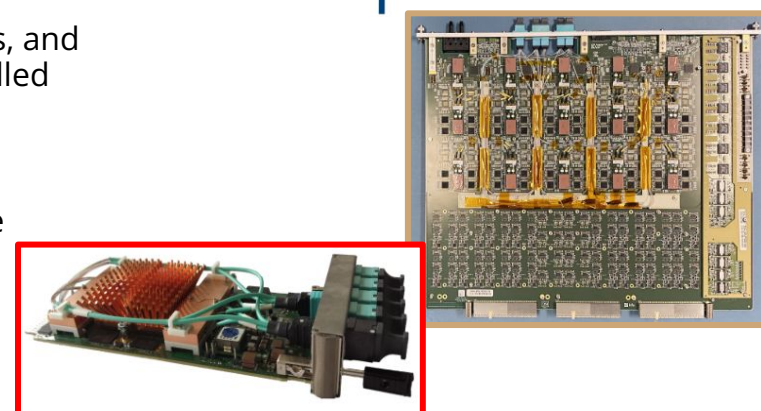
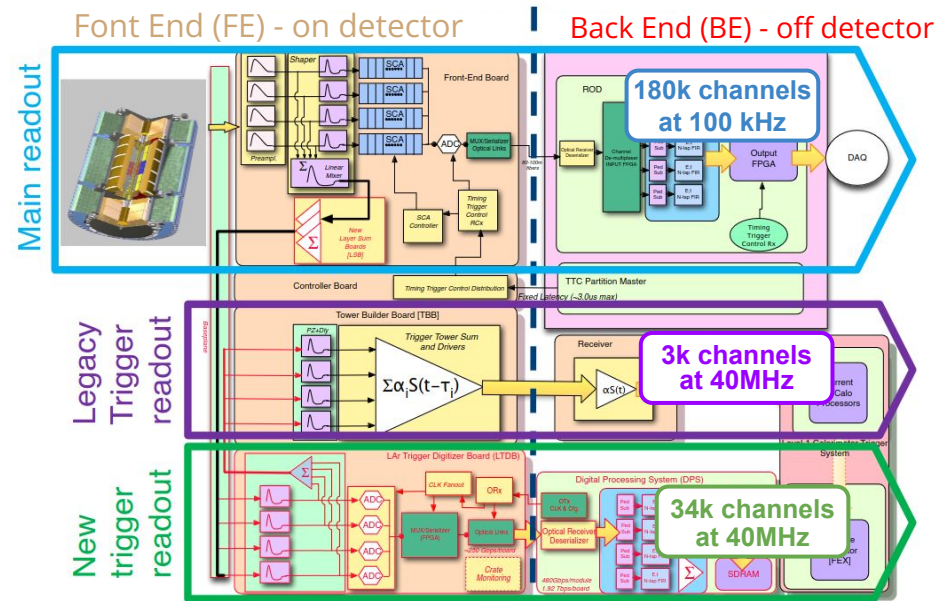
The entire readout system is affected by this upgrade

The front end electronics of the **legacy trigger** was dismantled then reassembled, some components replaced:

- Baseplane
- Front End Board
- Layer Sum Board

New cards added for the **digital trigger**

- **LAr Trigger Digitizer Board**
  - Send analog signals to Tower Builder Board, digitize analog signals, and send digital signals to the back-end. A total of 124 LTDBs are installed
- **LAr Digital Processing System**
  - LAr Carrier
  - **LAr Trigger prOcessing MEzzanine**
    - Receives ADC counts from a LTDB via 40 optical fibers with the speed of 5.12 Gbps for each fiber, computes energy and pulse timing in a FPGA and sends energy to Feature Extractors. 116 LATOMEs are installed.





# LTDB phase selection

To be able to decode the data received by the LATOME from the LTDB

- 320 MHz clk from PLL and GBTx clk should not be in phase

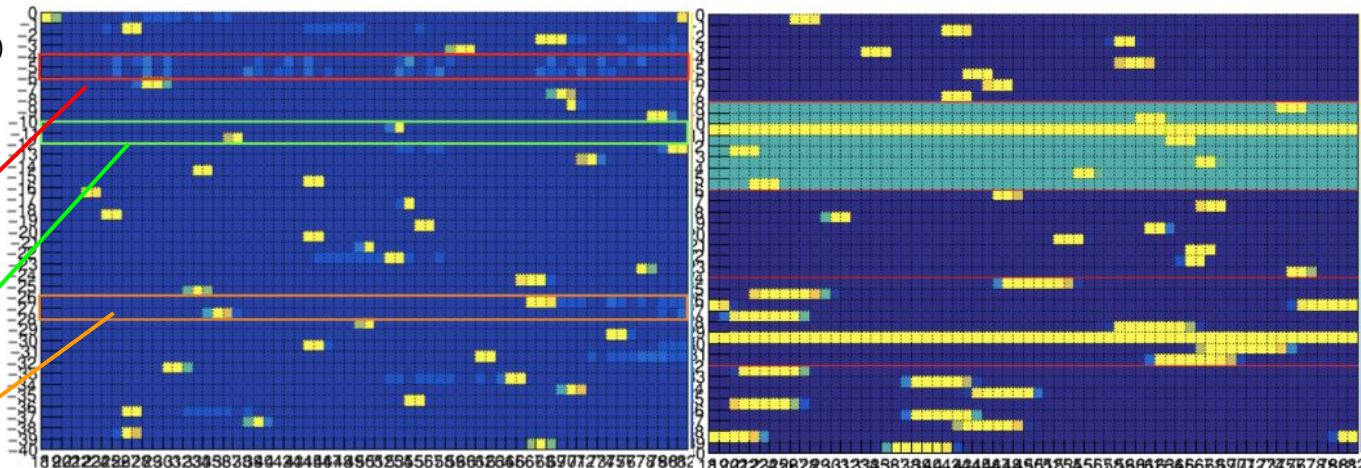
Develop a calibration to choose the delay between the 2 phases:

- Works per pair of fibers (1 LOCx2)
- For each delay → look at the number of errors received by the LATOME
  - If errors are received → delay is tagged as wrong
- 64 delays in total
- Should be done after each power cycle
  - Was very instable
  - Was done more often (~1/week)

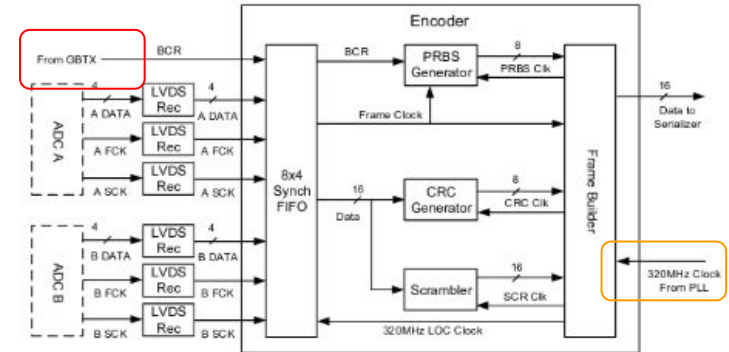
Solution found:

- GBTx reset
- LATOME transceiver reset

0 peak or severals  
1 peak only  
1 peak and values outside



## LOCic encoder



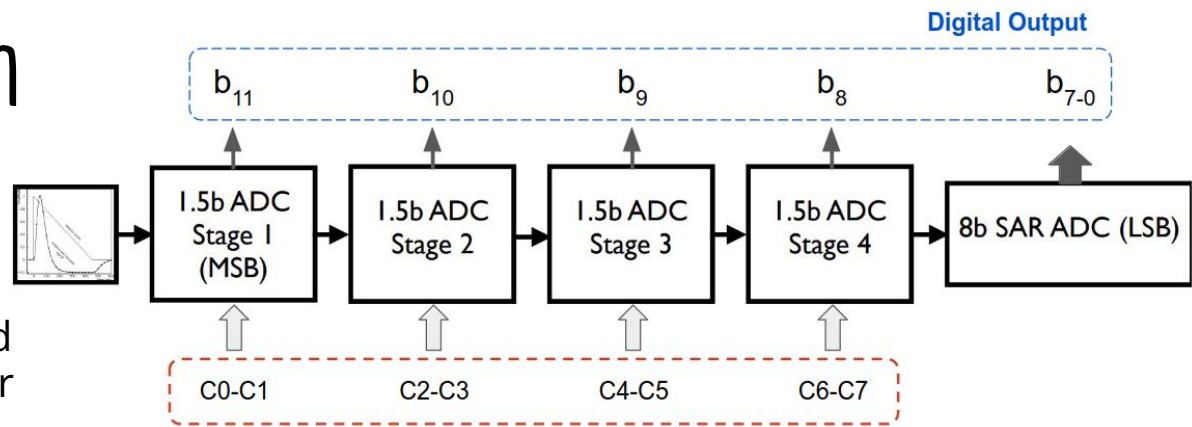
# LTDB ADC calibration

- NevisADC is a pipeline ADC
  - 4 stage of 1.5 bit MDACs
  - 8 bit SAR ADC

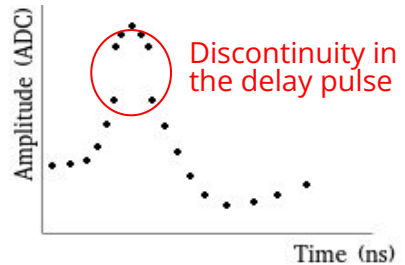
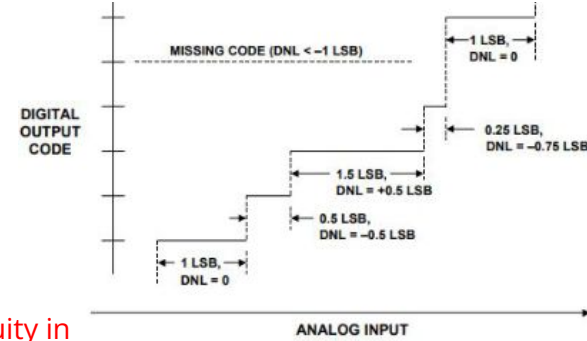
- Calibration constants are loaded in the MDAC units to account for gain errors

- Miscalibration can cause Differential Nonlinearity (DNL) errors
  - will be perceived as ADC jumps in the digitized signal

- DNL is nonlinearity of the code transitions of the converter
  - It can be measured as the deviation of quantization steps from 1 LSB



Calibration Constants



The new calibration constants solve all ADC jumps !

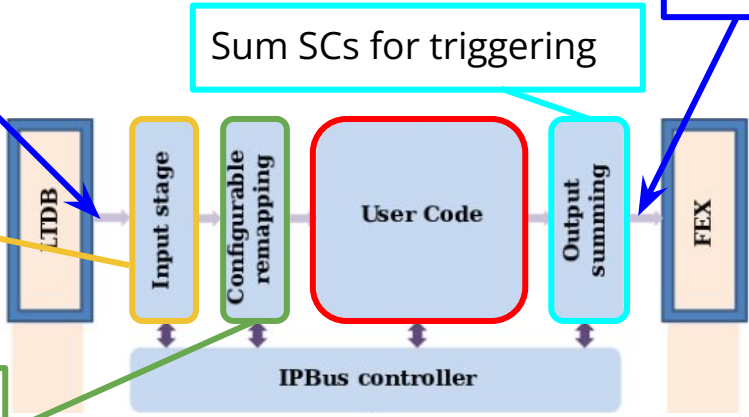
# LATOME firmware

Input: 5.12GHz/fiber  
40 fibers

8 SCs x 16bit = 128 bit sent  
as 1 frame at 320MHz

Frequency is reduce to 240MHz  
Mapping in function of SCs

Energy reconstruction  
Bunch Crossing identification  
Baseline correction  
Saturation detection



Output: 11.2GHz/fiber  
40 fibers



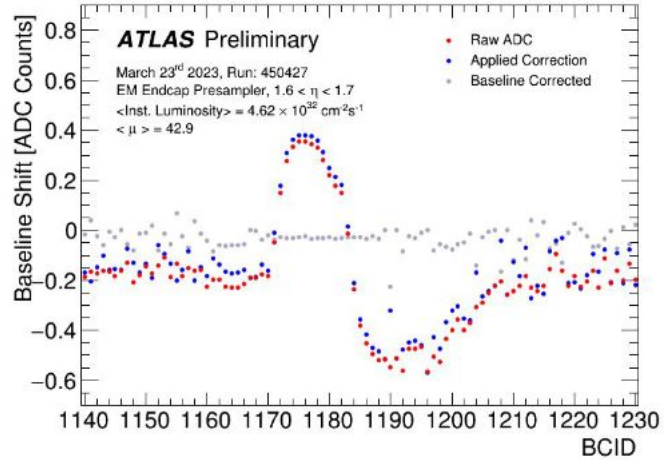
FPGA resource occupancy

- 91% of logic unit
- 95% of block memory

$$E_T = \sum_{i=0}^3 \bar{A}_i (S_i - p - b)$$

$$E_T \tau = \sum_{i=0}^3 \bar{B}_i (S_i - p - b)$$

$S_i$  → ADC samples from LTDB  
 $p$  → Pedestal level (Cool DB)  
 $b$  → Baseline correction  
 $A_i$  and  $B_i$  → OFCs (Cool DB)





# Digital Trigger front end slow control & monitoring

## LTDB 124 boards

- Control:
  - Configuration of all the boards
  - Calibration
  - General status
- Monitoring:
  - Optical power of the control fiber
  - Tension/current
  - Fan speed
  - FPGA temperature
  - SCA temperature/register
  - Data fiber status

**Run Control**

BARREL C     BARREL A  
 EMEC-C     EMEC-A

POWER ON    POWER OFF  
 INIT    SHUT/DOWN  
 CONFIG    LINK-CONFIG

Links Status

COPY STATE

Color code

■ Off  
■ On  
■ Disabled  
■ Unknown  
■ Error

Expert

ENVOISABLE LTDB  
 CLKDBS SCAN A SIDE  
 CLKDBS SCAN C SIDE

Naming convention

BARREL C (H)    BARREL A (I)

EMEC C (C)    EMEC A (A)

Servers A, Servers C

Main > 104 > I04R

### LTDB\_47\_I04R

Light 1044 UV	Light 1045 UV	Light 1046 UV	Light 1047 UV	Light 1048 UV
SCA 1(Online) S1010 3.898 V S1011 3.898 V S1012 3.784 V S1013 3.948 V S1014 5.591 A S1015 5.762 V S1016 5.813 V S1017 5.079 A S1018 6.724 V S1019 6.740 V S1020 1.587 A S1021 10.956 V S1022 10.998 V S1023 10.951 V S1024 0.000 V S1025 6.598 V S1026 6.610 V S1027 1.221 A S1028 TRUE S1029 TRUE S1030 0.059 V	SCA 2(Online) S2010 19.000 S2011 19.000 S2012 15.000 S2013 19.200 S2014 191.000 S2015 175.000 S2016 251.000 S2017 19.000 S2018 19.000 S2019 15.000 S2020 19.200 S2021 191.000 S2022 175.000 S2023 251.000 S2024 19.000 S2025 19.000 S2026 15.000 S2027 19.200 S2028 191.000 S2029 175.000 S2030 251.000	SCA 3(Online) S3010 19.000 S3011 19.000 S3012 15.000 S3013 19.200 S3014 191.000 S3015 175.000 S3016 251.000 S3017 19.000 S3018 19.000 S3019 15.000 S3020 19.200 S3021 191.000 S3022 175.000 S3023 251.000 S3024 19.000 S3025 19.000 S3026 15.000 S3027 19.200 S3028 191.000 S3029 175.000 S3030 251.000	SCA 4(Online) S4010 19.000 S4011 19.000 S4012 15.000 S4013 19.200 S4014 191.000 S4015 175.000 S4016 251.000 S4017 19.000 S4018 19.000 S4019 15.000 S4020 19.200 S4021 191.000 S4022 175.000 S4023 251.000 S4024 19.000 S4025 19.000 S4026 15.000 S4027 19.200 S4028 191.000 S4029 175.000 S4030 251.000	SCA 5(Online) S5010 19.000 S5011 19.000 S5012 15.000 S5013 19.200 S5014 191.000 S5015 175.000 S5016 251.000 S5017 19.000 S5018 19.000 S5019 15.000 S5020 19.200 S5021 191.000 S5022 175.000 S5023 251.000 S5024 19.000 S5025 19.000 S5026 15.000 S5027 19.200 S5028 191.000 S5029 175.000 S5030 251.000

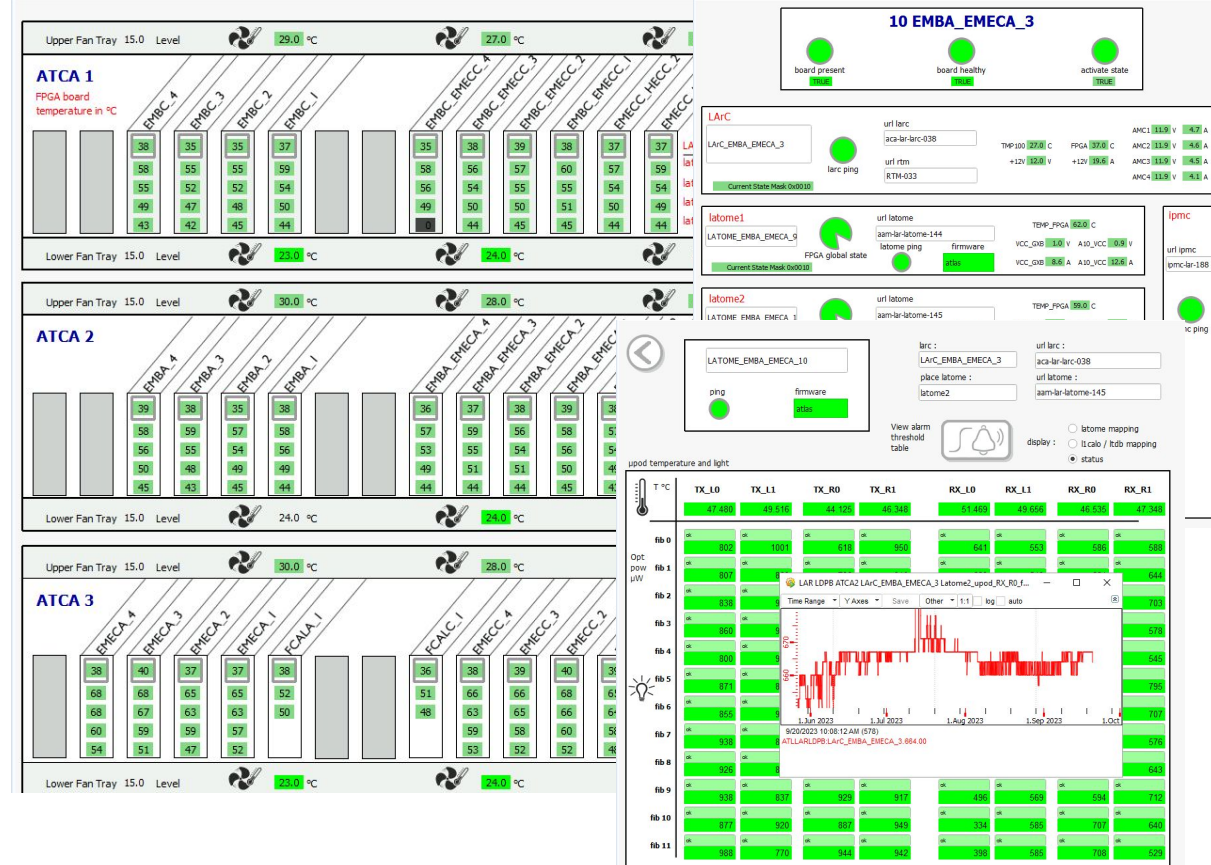
# Digital Trigger back end slow control & monitoring

## ATCA crate

- Fan speed level
- Region temperature (left, center, right)
- Rack temperature

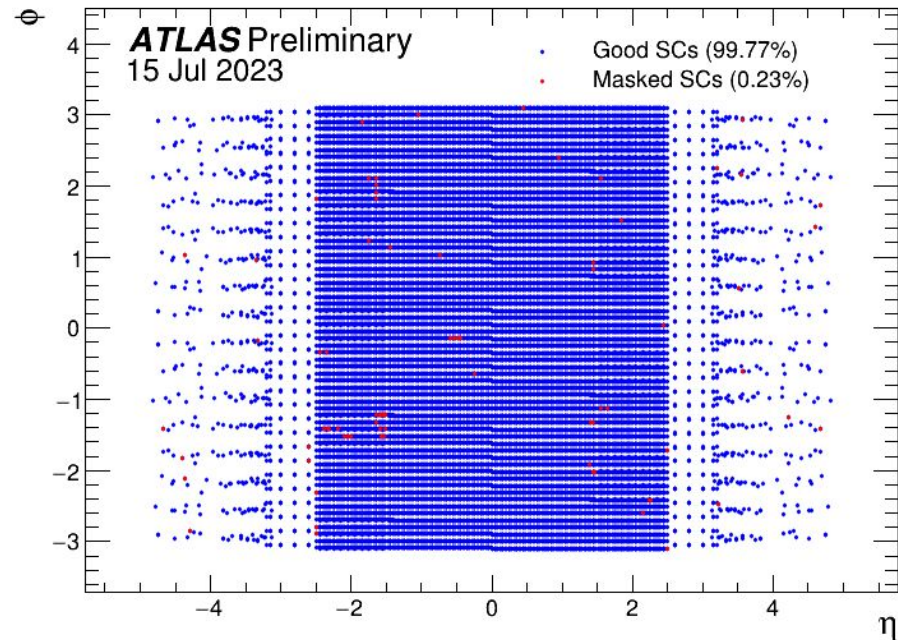
## LDPB boards

- Monitoring:
  - State
    - ID
    - Ping
    - Firmware
  - Tension/current
  - FPGA temperature
  - Optical power data fiber



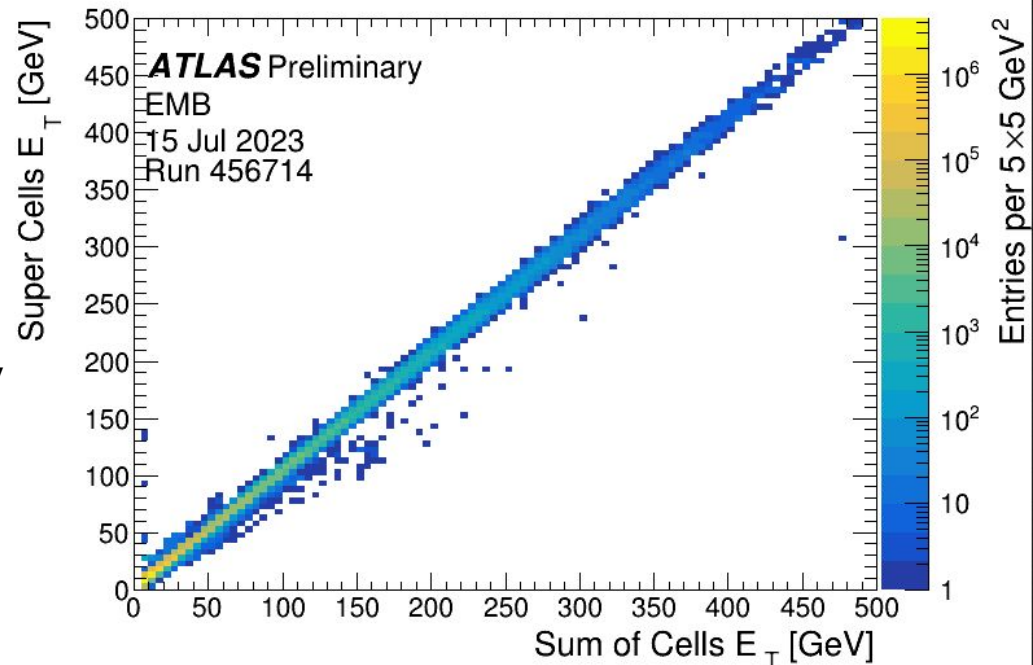
# Digital Trigger status

- SC status reported on 15 July 2023 for a pp collision run at  $\sqrt{s} = 13.6\text{TeV}$
- 99.7% of SCs (33970 out of 34048) show (in blue) have no issues and return the expected timing and energy
- 78 SCs (in red) are dead, it comes from known problematic Digital Trigger Front End Board channels (LTDBs or with known calibration issues)



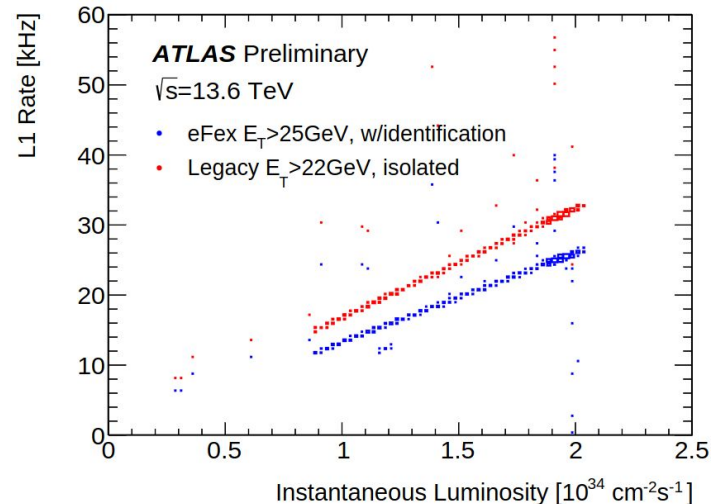
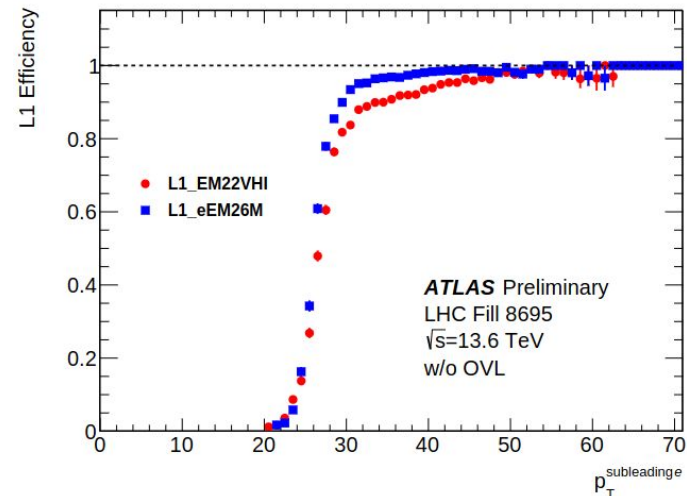
# Energy comparisons DT - main readout

- Supercells transverse energies ( $E_T$ ) are compared to the sum of the corresponding LAr cells in the main readout
- The data from the run with the pp collision at  $\sqrt{s} = 13.6$  TeV on 15 July 2023 is used for the comparison
  - Bad SCs are not included
  - A good agreement is observed between the two readouts



# Phase-I trigger performance

- Cell energies correspond well between calorimeter cells and SCs
- The single electron triggers for the **legacy system** and the **Phase-I system** are compared
- Phase-I EM trigger item shows better performance:
  - Sharper efficiency turn-on curve
  - Lower trigger rate ( $\sim 80\%$  of legacy EM item) at the same ET threshold
- Phase-I EM items used as primary trigger now
- Plan to turned on the rest of the phase-I system beginning of 2024





# Summary

- The LAr digital trigger system has been installed in the long shutdown 2 of LHC and is going to replace the legacy trigger system in 2024
  - Phase-I EM trigger item enabled since May 2023
- The main readout of LAr is also working well
- More than 99% of SCs are functional for the trigger system

## Next step

- Enable Phase-I trigger for all items and decommissioned the legacy trigger for the restart of run 3 in 2024

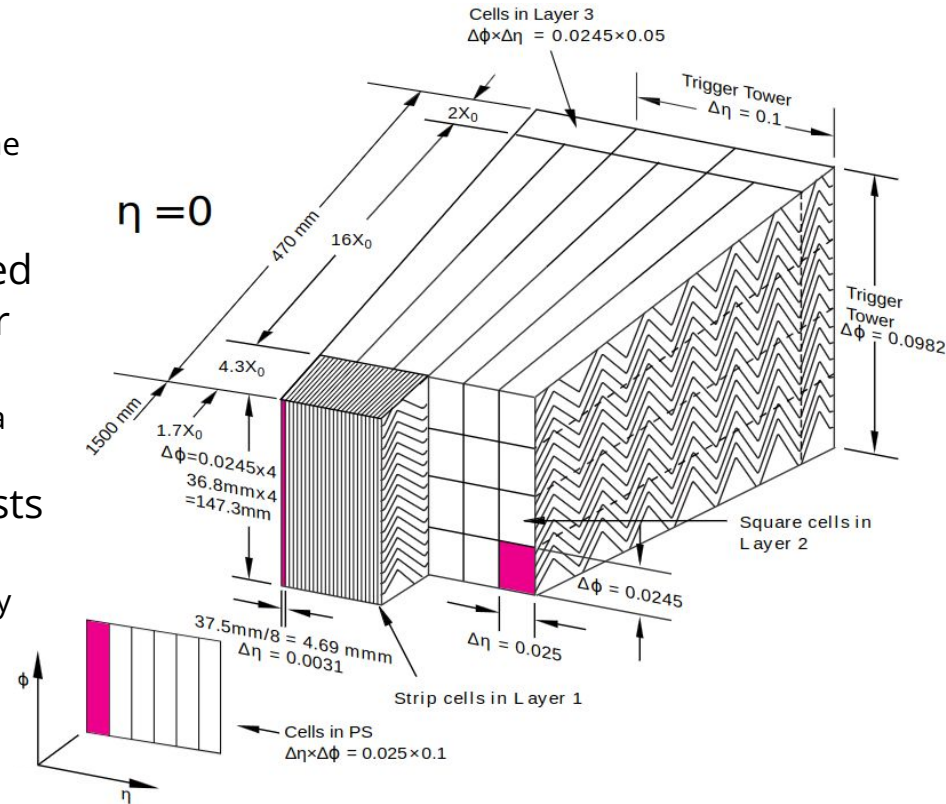
# Thank you for your attention

Any questions ?



# LAr calorimeter layers

- LAr cells are the readout units with the finest granularity
  - The size of LAr cells is varying in different parts of the calorimeters
  - In total there are **182418** LAr cells
- The readout units for the trigger are composed of the sum of LAr cell signals call trigger tower
  $\Delta\eta \times \Delta\phi = 0.1 \times 0.1$ 
  - A new trigger system was introduced in Run 3 with a smaller units
- The barrel electromagnetic calorimeter consists of:
  - **Presampler:** correction of the energy loss caused by dead material
  - **Front layer:** fine granularity along  $\eta$
  - **Middle layer:** most of energy deposit
  - **Back layer:** recovery of  $e/\gamma$  longitudinal energy leakage



# Legacy readout electronics

The readout system is composed of front-end (on detector) and back-end (off detector) electronics.

Front-end:

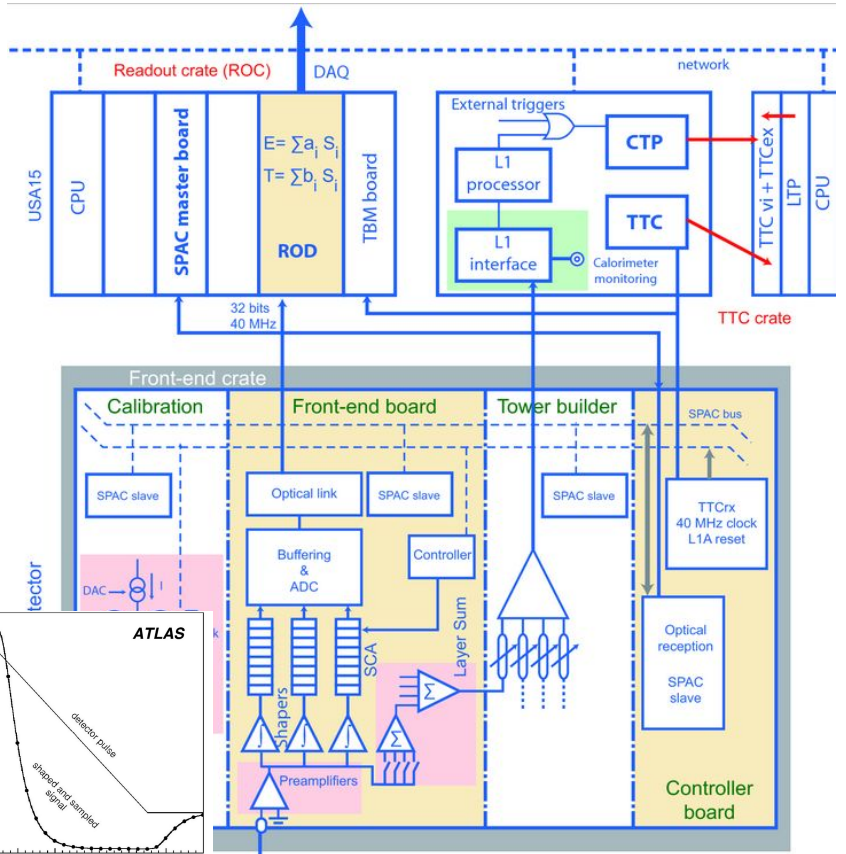
- **Front End Boards (FEBs):**
  - Analog signals from LAR cells are amplified, shaped to bipolar analog signals, and digitized using 12-bit ADC.
  - **Layer Sum Boards (LSB)** on FEBs sum the analog signals.
- **Tower Builder Boards (TBBs):**
  - Analog signal sums from LSB are received in TBB, which forms trigger towers with a granularity of  $\Delta\eta \times \Delta\phi = 0.1 \times 0.1$

Back-end:

- **Read Out Drivers (RODs):**
  - RODs receive digital signals from FEBs and compute the energy, time phase, and quality of the signal.
- **Level-1 calorimeter (L1Calo) system:**
  - Analog signals from trigger towers are sent to L1Calo, which identifies physics objects and sends the results to the Central Trigger Processor (CTP).

Main readout: FEB → ROD

Legacy trigger: LSB → TBB → L1Calo





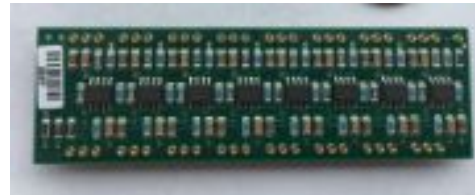
# Front-end electronics

## Baseplane:

New baseplanes provide an additional slot for LTDB and distribute analog signals of SCs from FEBs to LTDB.

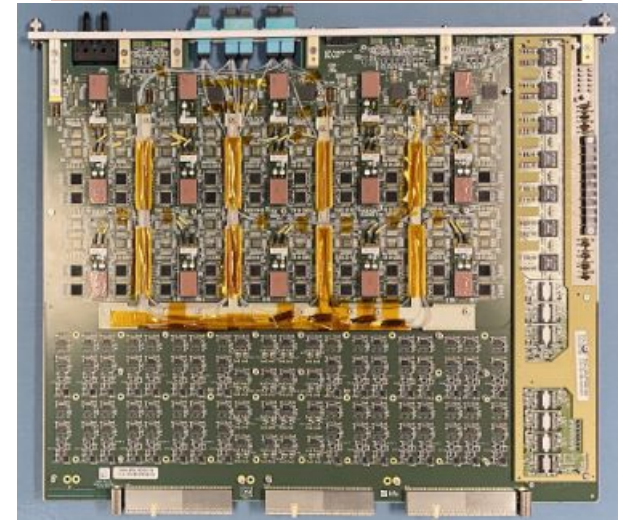
## Layer Sum Board (LSB):

A plug-in card of the FEB provides a sum of analog signals for SCs. 2328 LSBs are replaced.



## LAr Trigger Digitizer Board (LTDB):

Send analog signals to Tower Builder Board, digitize analog signals, and send digital signals to the back-end. A total of 124 LTDBs are installed





# Back-end electronics

## **Intelligent Platform Management Controller (IMPC):**

Manage the power, cooling, and interconnect needs of intelligent devices.



## **LAr Trigger prOcessing MEzzanine (LATOME):**

Receives ADC counts from a LTDB via 40 optical fibers with the speed of 5.12 Gbps for each fiber, computes energy and pulse timing in a FPGA and sends energy to Feature Extractors. 116 LATOMEs are installed.

## **LAr Carrier (LArC):**

Transmit data from LATOMEs to the readout system, distribute clocks and trigger signals synchronized to the LHC beam clock. 30 LArCs are installed.



# Performance of the LAr calorimeter

These plots show the summed energies for a beam splash event from March 2023

- LAr cell energy sums distributed in a hypothetical tower grid with  $\Delta\eta \times \Delta\phi = 0.025 \times 0.025$
- The particles were delivered by Beam 1 (B1) and entered from the positive  $\eta$  (A) side

