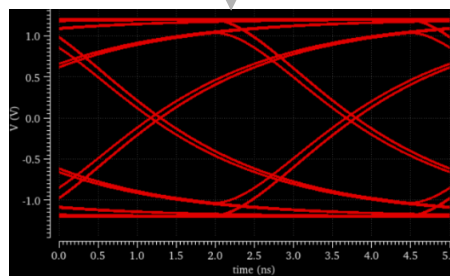
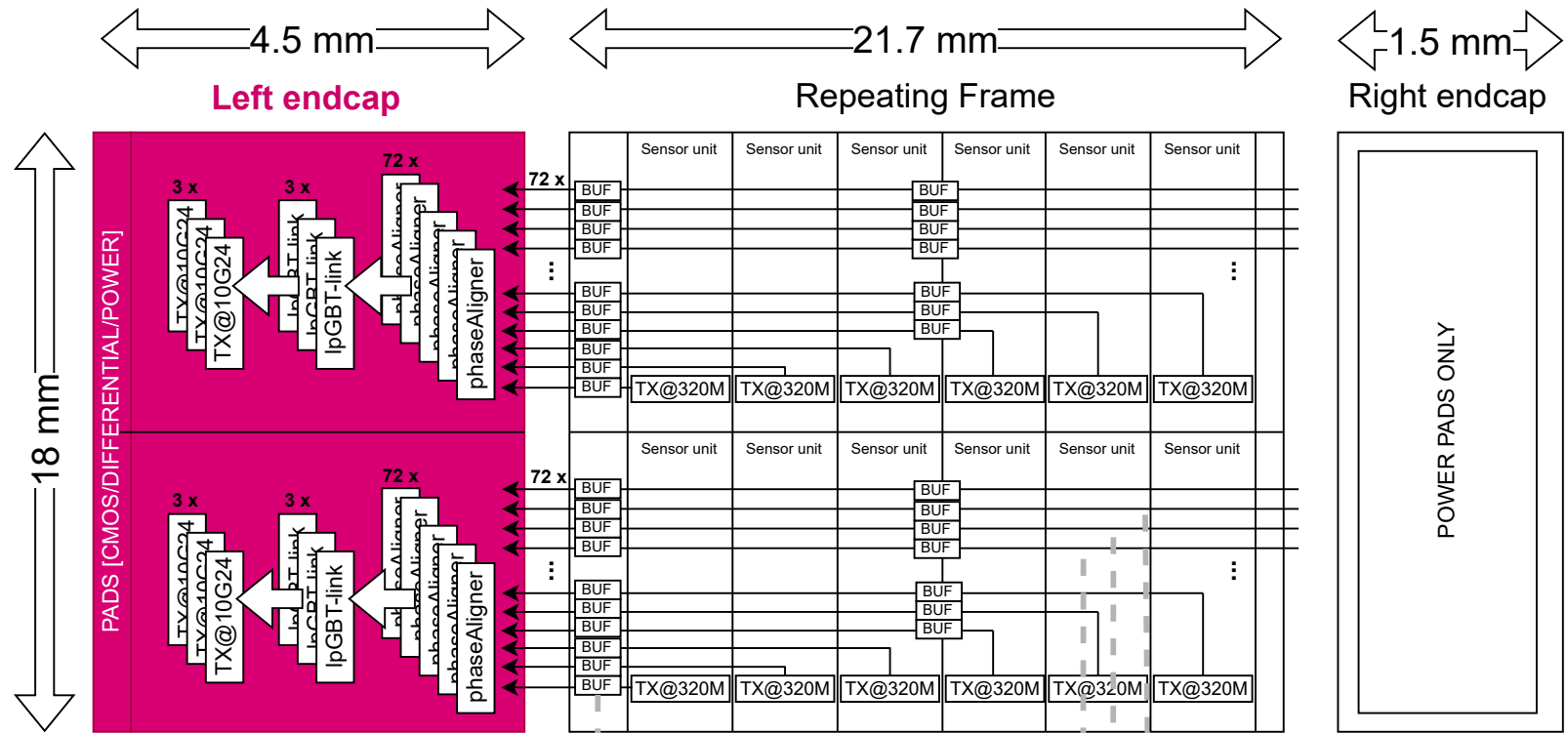
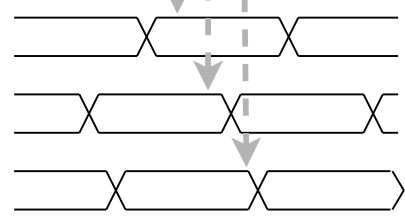


Figure 1. Diagram of the stitched sensor prototype with the main focus on the left endcap architecture



Jitter introduced at each rebuffering stage (BUF)



Each transmission line has different phase delay