

# A Charge-integration Pixel Detector Readout Chip Features High Frame Rate with in-pixel ADCs: Appendix

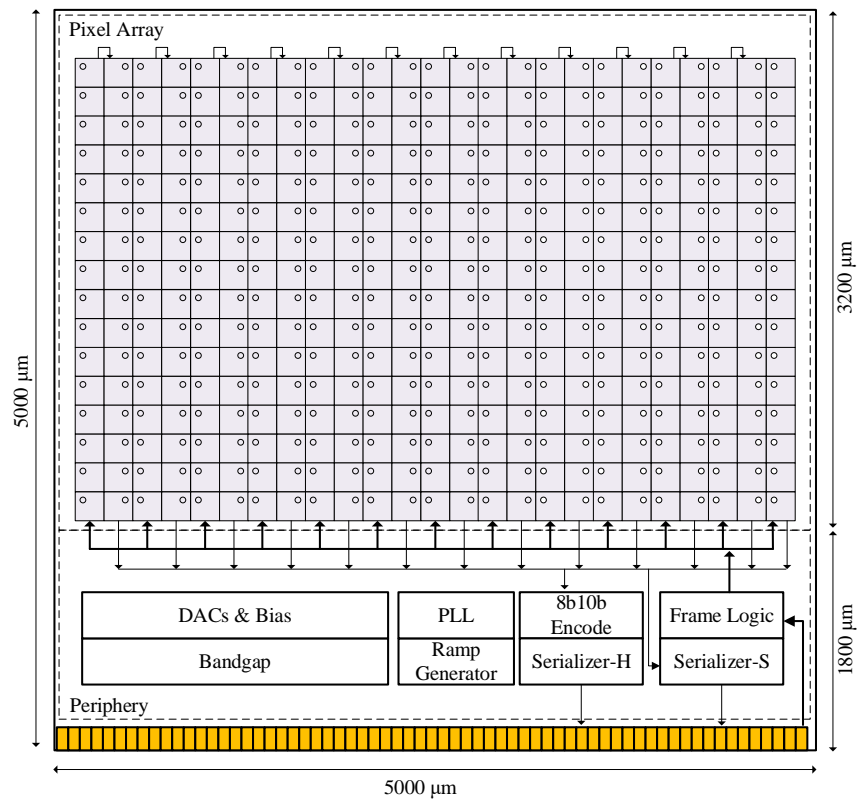


Figure 1 Block Diagram of the HYLITE200S Chip. Serializer-H: the high-speed serializer. Serializer-S: the slow-speed serializer.

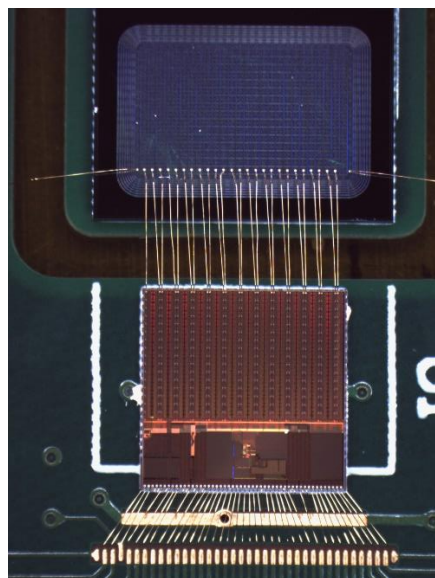


Figure 2 Photo of Wire Bonding Between the HYLITE200S Chip and the Sensor

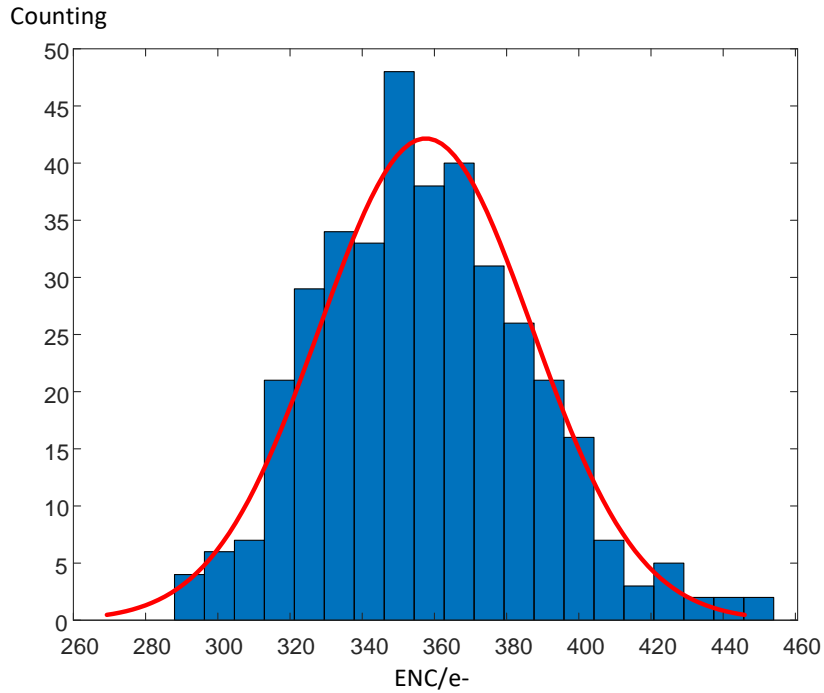


Figure 3 Histogram of Noise of the single-photon Charge Injection

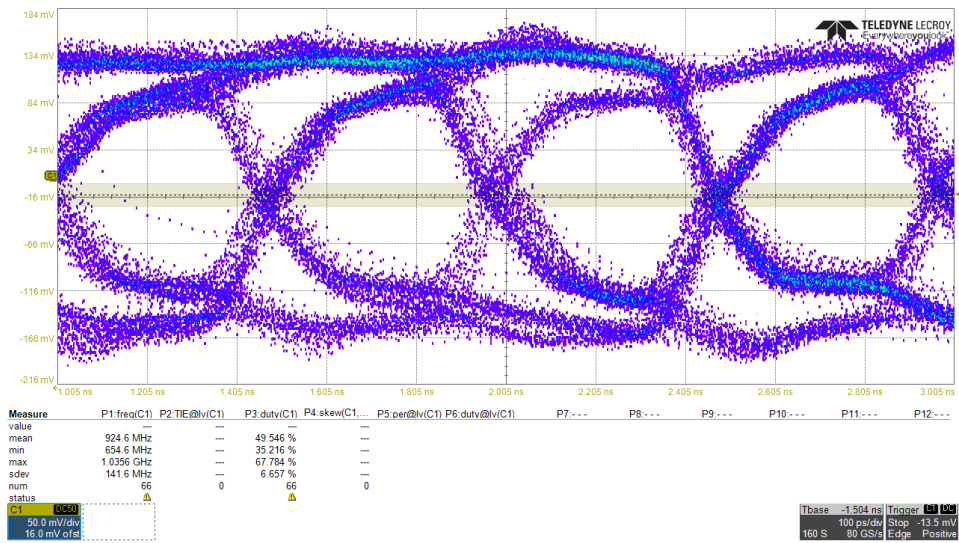


Figure 4 Eye Diagram of the HYLITE200S High-Speed Serializer at 2Gbit/s.

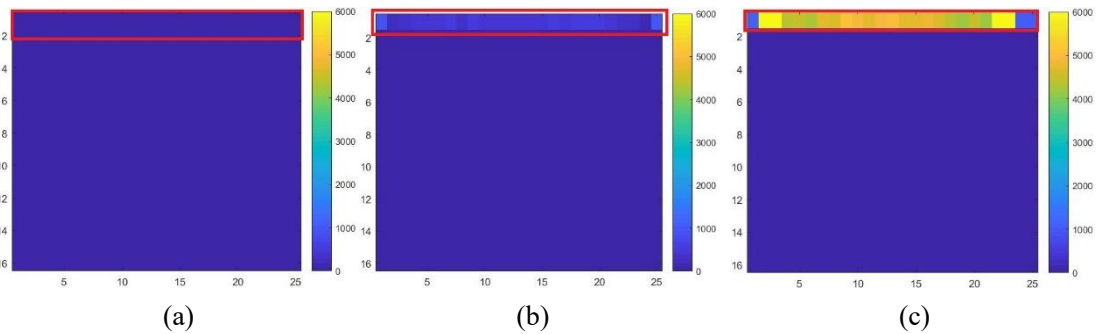


Figure 5 Responses to Different Luminosity Lasers of the Co-Debug Module. (a) No laser incidence. (b) Low-luminosity laser incidence. (c) High-luminosity laser incidence.