The quality control programme for ATLASX ITk strip tracker module assembly

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Abstract

The assembly of the ATLAS Inner Tracker requires the construction of 19,000 silicon strip sensor detector modules in eight different geometries. Modules will be assembled and tested at 31 institutes on four continents, from sensors, readout chips and flexes. In order to adhere to the module specifications defined for sufficient tracking performance, a rigorous programme of quality control and assurance was established to cover components at every stage of assembly. This contribution presents an overview of the QA/QC programme for ITk strip tracker modules, results from the pre-production phase (5% of the production volume) and proposed adjustments for production.

Introduction

2029: Start of HL-LHC operations to provide more data to experiments. Detector upgrade required to handle increased rates, radiation ITk strips: Part of ATLAS tracker upgrade. Will have four barrel layers, six endcap disks per side Building block: **Modules**. Requires assembly, testing of multiple parts from many institutes.



Barrel

module

shapes:

Modules consist of silicon **sensors**, readout and

readout chips

powering electronics glued on

Individual strips **wirebonded** to



Long strip

Short strip

Barrel and endcap geometries **ITk layout** require different module geometries **ITk strips** just outside **ITk pixels** inner detector Wire-bonds Require **finer granularity** closer to interaction point **R**0 **R1** HCCStar Wire-bonds r [mm] 1400 ATLAS Simulation Preliminary <u>Endcap</u> ITk Layout: 23-00-03 1200 module **ENDCAP DISKS** shapes: 1000 η = **2.0** 800 **R**2 **R**3 Senso 600 ~97mm η **= 3.0** BARREL n = 4.0 ~97mm 3500 R4 R5 z [mm]

Assembly and QA/QC steps







Module QC

- Final QC step of modules before mounting: Electrical test and thermal cycling
- Ensure stable **noise** after assembly, stable noise and current through **temperature changes**







- among 31 institutes
- QA/QC procedures defined and ongoing. Crucial for catching issues and ensuring a quality detector for HL-LHC





