TWEPP 2023 Topical Workshop on Electronics for Particle Physics

Tuesday 3 October 2023

Programmable Logic, Design and Verification Tools and Methods - Sirocco Room (09:00 - 10:00)

-**Conveners: Johan Alme**

time	[id] title	presenter
09:00	[78] FLX-182, the hardware platform for ATLAS readout during High Luminosity LHC	ILIC, Nikolina
09:20	[156] The CMS HGCAL trigger data receiver	SHUKLA, Raghunandan
09:40	[35] Front-End RDMA Over Converged Ethernet, real-time firmware simulation	BORTOLATO, Gabriele

Programmable Logic, Design and Verification Tools and Methods - Sirocco Room (11:20 - 12:20)

-**Conveners: Alex Kluge**

time	[id] title	presenter
11:20	[123] SystemC framework for architecture modelling of electronic systems in future particle detectors	CERESA, Davide
11:40	[37] Verification Environment for ALTIROC ASIC of the ATLAS High Granularity Timing Detector	SCARFI', Simone
12:00	[93] Model and analysis of the data readout architecture for the ITS3 ALICE Inner Tracker System	VIQUEIRA RODRIGUEZ, Manuel

Programmable Logic, Design and Verification Tools and Methods - Sirocco Room (15:20 - 16:20)

-**Conveners: Johan Alme**

time	[id] title	presenter
15:20	[171] Towards Single-Event Upset detection in Hardware Secure RISC-V processors	Prof. PRINZIE, Jeffrey
15:40	[21] Fault Tolerance Evaluation Study of a RISC-V Microprocessor for HEP Applications	WALSEMANN, Alexander
16:00	[103] Integrating lpGBT into the Common Readout Units (CRU) of the ALICE Experiment	DAVID, Erno