

## **Session Program**

**1-6 Oct 2023**



## **TWEPP 2023 Topical Workshop on Electronics for Particle Physics**

### ***Programmable Logic, Design and Verification Tools and Methods***

Geremeas, Sardinia, Italy  
Calaserena Village, Geremeas, Quartu S.Elena, Sardinia (Italy)

## Tuesday 3 October

09:00

### Programmable Logic, Design and Verification Tools and Methods

**Session** | **Location:** Sirocco Room | **Convener:** Johan Alme

09:00–09:20

#### FLX-182, the hardware platform for ATLAS readout during High Luminosity LHC

##### Speaker

Nikolina Ilic

09:20–09:40

#### The CMS HGCAL trigger data receiver

##### Speaker

Raghunandan Shukla

09:40–10:00

#### Front-End RDMA Over Converged Ethernet, real-time firmware simulation

##### Speaker

Gabriele Bortolato

10:00

11:20

### Programmable Logic, Design and Verification Tools and Methods

**Session** | **Location:** Sirocco Room | **Convener:** Alex Kluge

11:20–11:40

#### SystemC framework for architecture modelling of electronic systems in future particle detectors

##### Speaker

Davide Ceresa

11:40–12:00

#### Verification Environment for ALTIROC ASIC of the ATLAS High Granularity Timing Detector

##### Speaker

Simone Scarfi

12:00–12:20

#### Model and analysis of the data readout architecture for the ITS3 ALICE Inner Tracker System

##### Speaker

Manuel Viqueira Rodriguez

12:20

15:20

### Programmable Logic, Design and Verification Tools and Methods

**Session** | **Location:** Sirocco Room | **Convener:** Johan Alme

15:20–15:40

#### Towards Single-Event Upset detection in Hardware Secure RISC-V processors

##### Speaker

Prof. Jeffrey Prinzie

15:40–16:00

**Fault Tolerance Evaluation Study of a RISC-V Microprocessor for HEP Applications**

**Speaker**

Alexander Walsemann

16:00–16:20

**Integrating IpGBT into the Common Readout Units (CRU) of the ALICE Experiment**

**Speaker**

Erno David

16:20