

TWEPP 2023 Topical Workshop on Electronics for Particle Physics

Tuesday, 3 October 2023

Tuesday posters session (13:40 - 15:20)

[id] title	presenter	board
[177] Development of a multi-purpose DAQ system for Timepix4-based detectors.	Dr BIESUZ, Nicolo Vladi	
[80] Upgrade of the ATLAS Level-0 TGC Endcap Muon Trigger	KAWAMOTO, Chihiro	
[20] A high time resolution and dynamic range monolithic pixel detector-ASIC for Micro Vertex Detector in PANDA experiment	ZHANG, Hui	
[41] Test Result of the New ASD2 Chips for Phase-II Upgrade of the ATLAS MDT Chambers at HL-LHC	PENSKI, Katrin Elisabeth	
[88] Design of the OBELIX monolithic CMOS pixel sensor for an upgrade of the Belle II vertex detector	Dr PHAM, Thanh Hung	
[99] Prototype of a 10.24Gbps Data Serializer and Wireline Transmitter for the readout of the ALICE ITS3 detector.	VITKOVSKIY, Arseniy Dr VITKOVSKIY, Arseniy Mr ROSSEWIJ, Marcel GROMOV, Vladimir	
[109] Electrical / piezo-resistive effects in bend Alpide - Monolithic Active Pixel (MAP) sensors	ROSSEWIJ, Marcus Johannes	
[119] 32-channels mixed-signal processor for the tracking system of the GAPS dark matter experiment	RICEPUTI, Elisa	
[128] Integration of EDWARD readout architecture in full-field fluorescence imaging detector	GORNI, Dominik	
[135] MIMOSIS2 validations using Cadence Protium platform and its Black Box flow	BERTOLONE, Gregory	
[178] AstroPix: A novel HV-CMOS pixel sensor for space-based experiments	STRIEBIG, Nicolas	
[179] Development and performance of a pixel chip for the readout of GEM detectors for high-rate particle tracking	WANG, Hulin	
[197] RD50-MPW: A monolithic High Voltage CMOS pixel chip with high granularity and high radiation tolerance	ZHANG, Chenfan ZHANG, Chenfan	
[199] Characterization of the BigRock 28 nm Fast Timing Analog Front End	Ms KRIEGER, Amanda Simone Ms KRIEGER, Amanda Simone	
[226] The first full size full functionality ETROC2 (16x16) prototype for CMS MTD Endcap Timing Layer (ETL) upgrade	LIU, Tiehui Ted	
[6] Development of the Continuous Readout Digitising Imager Array Detector	MARRAS, alessandro	
[64] Design of a very low power 12 bits 40 MS/s ADC based on a time-interleaved SAR architecture	WILLIAM, Bontems DZAHINI, Daniel	
[66] The CMOS Pseudo-Thyristor: A Zero-Static Current Circuit for Pixelized Detector Front-End Stage	Dr WU, Jinyuan	
[90] 28 nm front-end channels for the readout of pixel sensors in future high-rate applications	GAIONI, Luigi	
[146] Test and performance of the LiTE-DTU ASIC for the HL-LHC upgrade of the CMS ECAL barrel	COSSIO, Fabio	

[200] Design Updates for HPSoC: A very high Channel Density Waveform Digitizer with sub-10ps resolution	Dr MOSTAFANEZHAD, Isar	
[43] An open-source IP-Core for Multi-Voltage Thresholding signal acquisition with FPGAs	ELISEEV, Dmitry Dr ELISEEV, Dmitry	
[76] Performance profiling and design choices of an RDMA implementation using FPGA devices	VASILE, Matei	
[48] HGTD PEB DC/DC Power Block in Low Temperature and Magnetic Field Operation	ZHAI, Mingjie	
[53] A high frequency radiation hardened DC/DC-converter with low volume air core inductor	KAMPKÖTTER, Jeremias	
[127] DC Power Circuit Evaluation for the Development of the Barrel Calorimeter Processor V2	GOADHOUSE, Stephen	
[136] Magnetic resilience studies for power supplies	Mr IOVENE, Alessandro Dr GIORDANO, Ferdinando SELMI, Simone Ivo	
[227] GaN based DC-DC converters for high energy physics applications	Mr PLUMED ASENSIO, Guillermo	
[27] Software mitigation scheme to increase availability under the radiation of microcontroller-based design for LHC accelerator environment	ZIMMARO, Alessandro	
[57] SiC based beam monitoring system for particle rates from kHz to GHz	WAID, Simon Emanuel	
[94] Radiation Tolerance Tests for SFP+ Transceivers	HASHIMOTO, Daisuke	
[139] Multi-Channel Radiation-Tolerant Humidity Monitoring System for the CMS Inner Cold Sub-detectors	KAPIC, Amar	
[45] Adaptability and efficiency of the CMS Level-1 Global Trigger firmware implementation for Phase-2	HUBER, Benjamin	
[62] System Design and Prototyping for the CMS Level-1 Trigger at the High-Luminosity LHC	WILLIAMS, Tom	
[79] Status of the MDT Trigger Processor for the ATLAS Level-0 Muon Trigger at the HL-LHC	ROJAS CABALLERO, Rimsky Alejandro	
[18] System level tests of large-scale multi-module prototype structures of the ATLAS ITk pixel detector	CHEONG, Sanha	
[24] Prototype electronics for the silicon pad layers of the future Forward Calorimeter (FoCal) of the ALICE experiment at the LHC	BOURRION, Olivier	
[61] 40MHz trigger-less readout of the CMS Drift Tube muon detector	MIGLIORINI, Matteo	
[63] CMS HGCal Electronics Vertical Integration System Tests	VOJINOVIC, Milos	
[96] A prototype 4D-tracking demonstrator based on the TimeSPOT developments.	LOI, Angelo LOI, Angelo	
[118] On-beam system test of the new readout electronics for the CMS Electromagnetic Calorimeter upgrade	CAMPANA, Mattia	
[125] The status of the Back-end card for the JUNO experiment	YANG, Yifan Dr YANG, Yifan	
[34] The Prototype Design of PEB - a Component of the HGTD In-detector Electronics for the ATLAS Phase-II Upgrade	ZHANG, Jie	
[50] Design and First Experience of the Prototype Pixel Layers for the ALICE FoCal	BODOVA, Tea	