

Highly granular sandwich calorimeters for Experiments

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Introduction

Purpose of this presentation:

- Give an overview of the existing Detector Concepts based on Particle Flow and their Highly Granular Calorimeter Systems (“sandwiched”)
- Remind the needed tools to assess their performances
- Give a status of the existing technologies & achievements
- and of the achievement and required developments for the Concepts

Role of Detector Concepts

In the ECFA Study and in the Roadmap Implementation

Primary goal of the detector WG is to demonstrate, as input to the next EPPSU, that detectors can be built that match the precision physics potential of future Higgs factories

- The level of realism of such demonstrations should be comparable between different Higgs factory proposals

The other main goal is to provide guidance for coherent detector R&D efforts to address the priority requirements of Higgs factory experiments

- And to support their funding requests

Software is the underlying tissue

- Detector models for performance evaluation, physics benchmarking and optimisation studies
- Validation and proof of feasibility from R&D

But some Engineering advice and inspiration is also needed:

- Rate, occupancy and bandwidth estimates
- Powering and cooling requirements
- Mechanical constraints

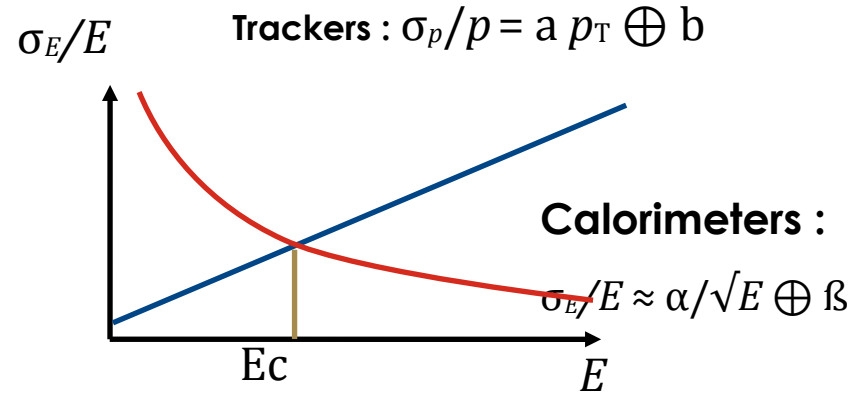
Indispensable ingredient to strategic detector R&D

Support for detector concepts
(software development,
engineering advice)
not part of DRD funding
(in the past: big labs)

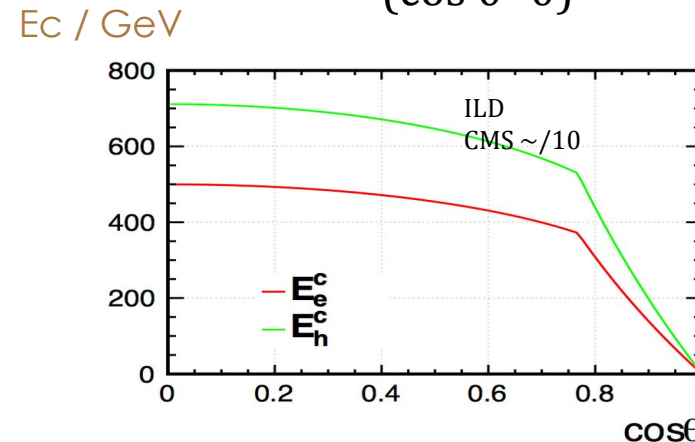
Particle Flow Approach

Paradigm of Particle Flow:

- Charged particles are almost always best measured by trackers;
- Calorimeters are only needed for neutrals
 - h^0, γ
- Track-Calorimeter clusters purely spatial (maybe time ?) linking avoids back propagation of calorimeters uncertainties.



($\cos \theta = 0$)



Highly-Granular Calorimeters at Higgs Factories for Particle Flow Approach based detectors

→ Large Tracker

- Precision and low X_0 budget
- Pattern recognition

→ High precision on Si trackers

- Tagging of beauty and charm

Large acceptance

→ Highly Granular Imaging Calorimetry

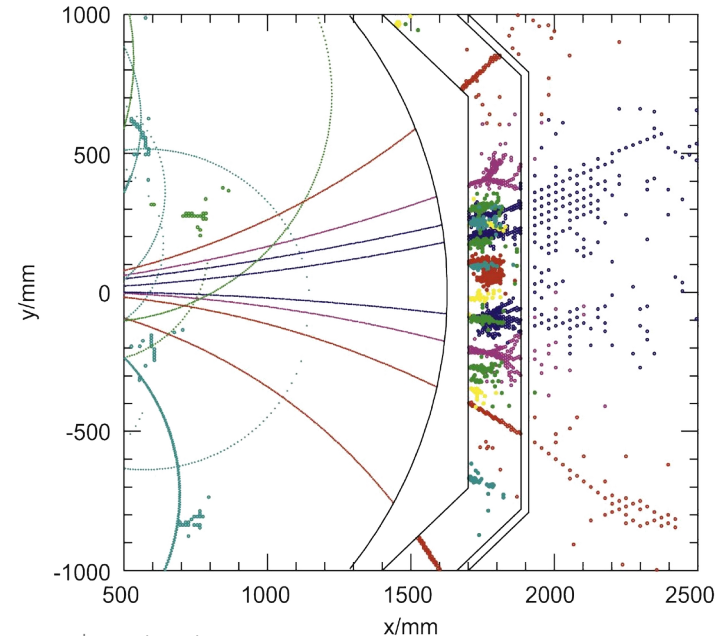
typical size : 0.5 – 3 cm

Particle Flow Algorithms :

- Jets = 65% charged Tracks + 25% γ ECAL + 10% h^0 E+HCAL
- TPC $\delta p/p \sim 5 \cdot 10^{-5}$; VTX $\sigma_{x,y,z} \sim 10 \mu\text{m}$

τ tagging + timing

H. Videau and J. C. Brient, "Calorimetry optimised for jets," (CALOR 2002)



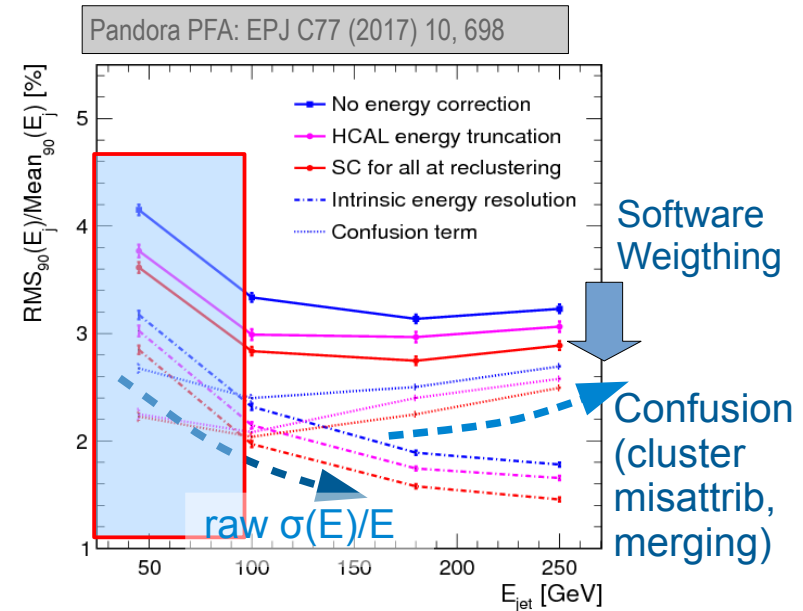
Reconstruction & Optimisation

Performances: (for a given configuration of detectors)

- Will depend on the Reconstruction SW (ex. PandoraPFA, ARBOR, APRIL, SW compensation, ML, ..)
- *and* it's proper tuning (~ generic ? not universal)
 - JER, τ reconstruction, b physics, ...

Optimisation (best configuration):

- on **simulation**, needs:
 - proper HW description
 - proper Electronics description (Digitization)
- needs the tuning o the Reconstruction SW for each



Low E jets \Rightarrow where PFA brings most

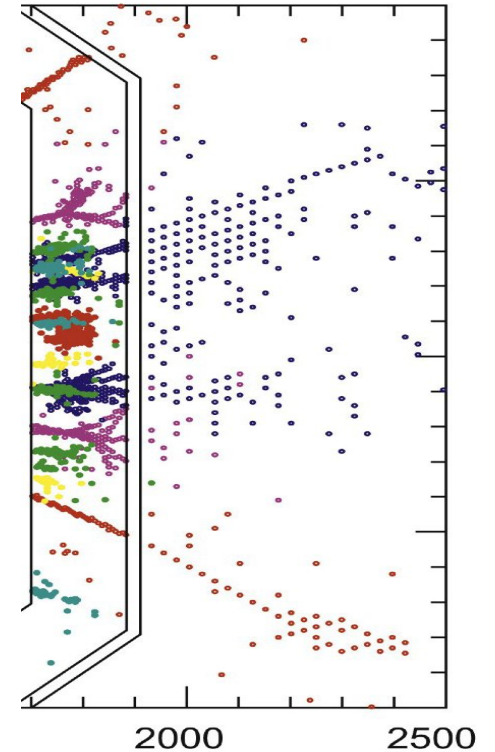
Geometrical on Calorimetric System

Ideally:

- 1 calorimeter fits all, uniform, covering $\sim 4\pi$, containing all but ν, μ
 - Costs, Mechanics, Performances for EM vs Had.

In practice:

- ECAL $\sim 25 X_0$ (= containment of photons), $\leq 1 \lambda_{\text{had}}$: absorber = W, Pb, Cu
 - PFA: laterally : shower separations : mip (h^\pm, μ); γ, e^\pm ; γ, h^0 ; mip, $\gamma\text{-}\gamma$ (τ physics)
longs: shower profile, had. shower start., timing
- HCAL $\sim 5\text{--}6 \lambda_{\text{had}}$ (= containment of h^0 's)
 - PFA: shower separation ($h^\pm - h^0$), Shower shape (compensation, leakage), ...
- Geometry:
 - PFA: As close as possible (tracking), Compatible Granularities, No Dead Spaces



Performances

Estimations of RAW performances:

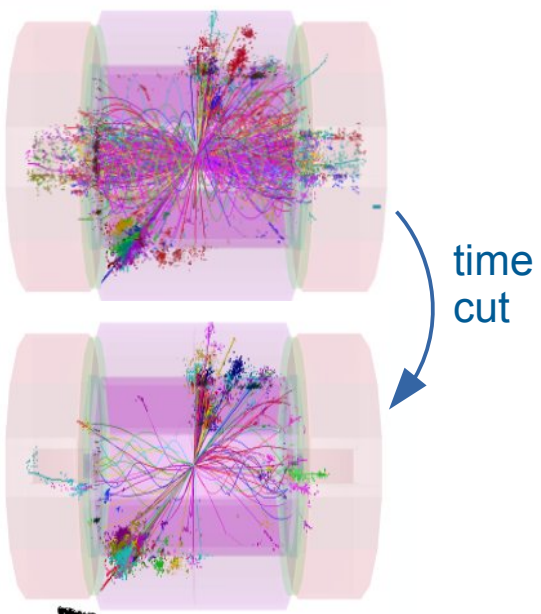
- Single particle response :
 - GEANT4 simulation \otimes Digitization [Sensors \oplus Electronics]: cell's $E, t \leftrightarrow$ Prototypes in Beam {See presentation from D. Newbold, A. Irles, K. Kruger}
- Two-particle response:
 - Particle separation from beam test data

Physics response :

- Cases: JER, τ , b, ...
 - On realistic **Full Simulation** \otimes Digitization \otimes Reconstruction(PFA)
 - **FastSim** tuned on Full Sim \otimes Reconstruction
- Different level of realism, certainty

Timing in Calorimeters: 0.1-1 ns range

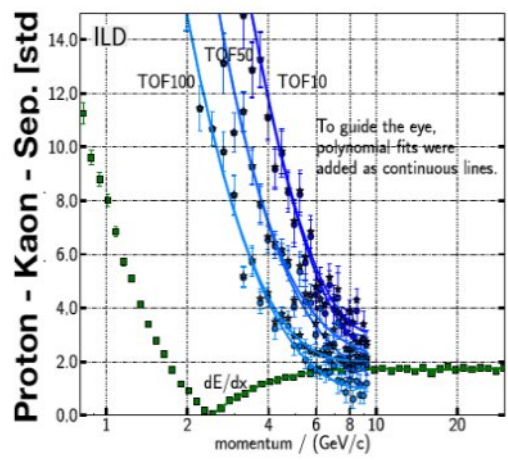
Cleaning of Events



[CLIC CDR: 1202.5940]
 adapted from L. Emberger

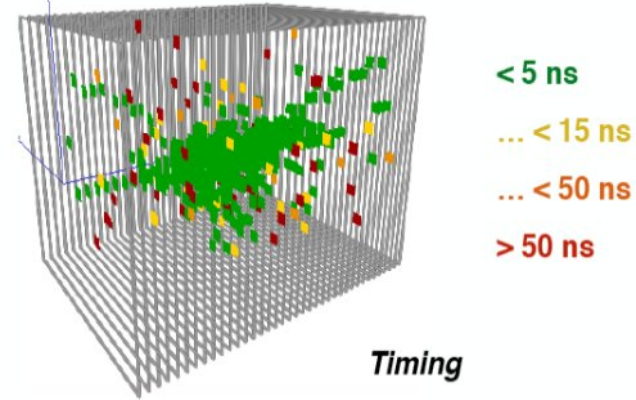
Particle ID by Time-of-Flight

- Complementary to dE/dx
 - here with 100 ps on 10 ECAL hits



Ease Particle Flow:

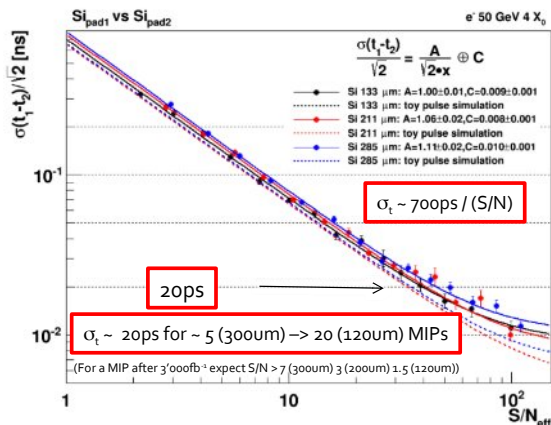
- Identify primers in showers
- Help against confusion *better separation of showers*
- Cleaning of late neutrons & back scattering.
- Requires 4D clustering



Timing Studies

2015 CMS HGCAL CERN timing test beam

Time resolution vs S/N ratio



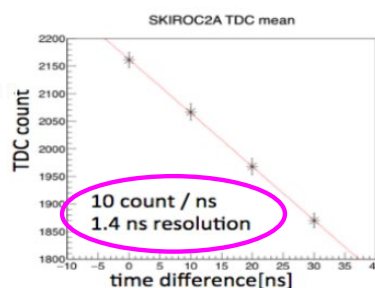
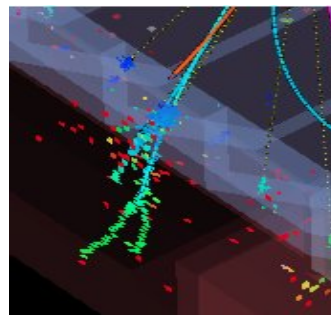
CMS Experiment at LHC, CERN
 Data recorded: Thu Jan 1 01:00:00 1970 CEST
 Run/Event: 1 / 1
 Lumi section: 1



Transparent cells => no timing
 Solid cells => timing information $\sim 50\text{ps}$

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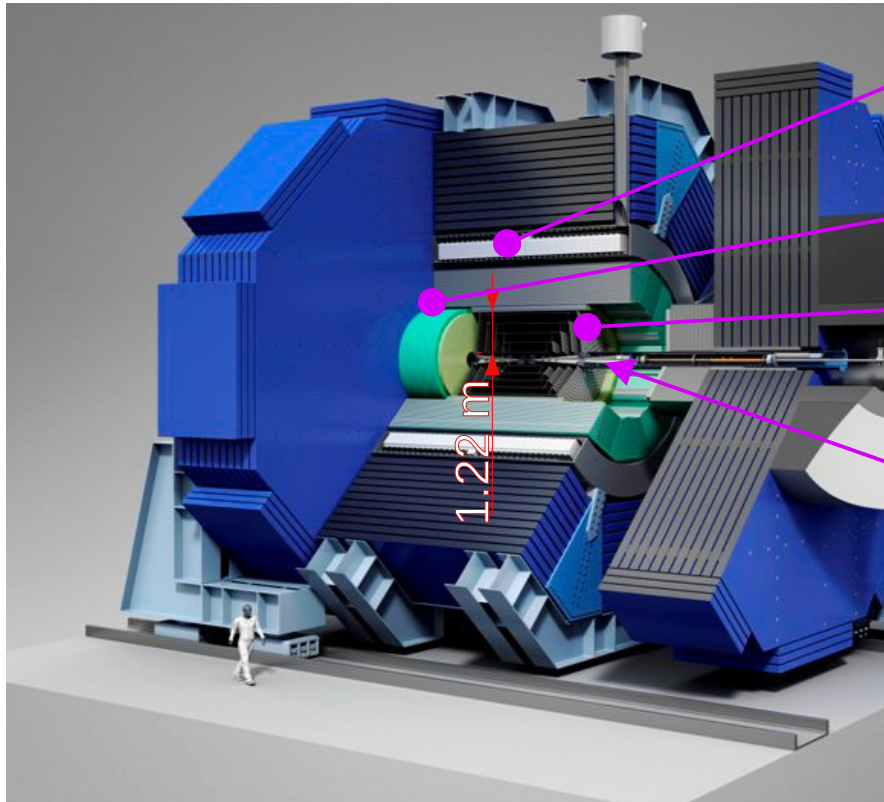
Option 1) Bulk Timing



Option 2) Dedicated layers with fast sensors (LGADs, MAPs, ...)

© H. Videau

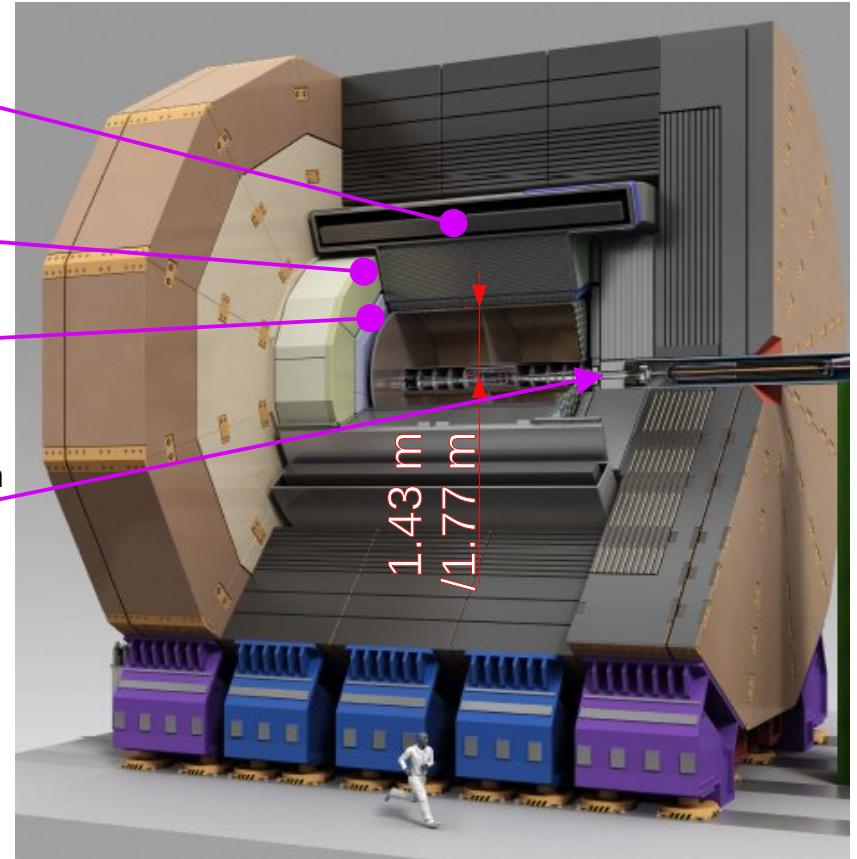
ILC: ILD & SiD



DBD (2013), dd4sim model

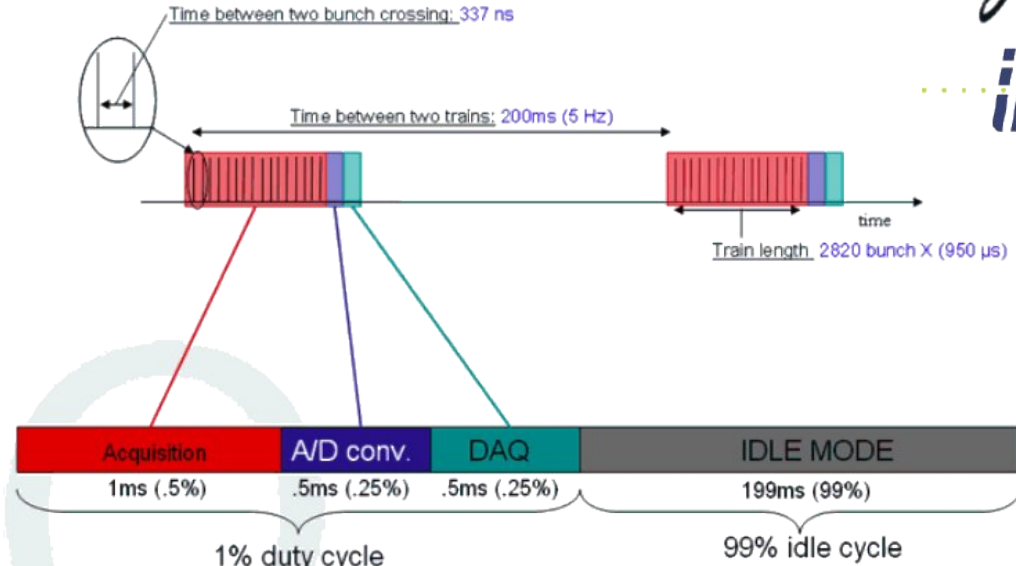
Vincent.Boudry@in2p3.fr

- Coil
5T vs 3.5T
- HCAL
 $4.5\lambda_i$ vs $6\lambda_i$
- ECAL
SiW vs SiW
Scint-W
1.22m vs 1.77/
1.43 m
- FCAL



DBD (2013), dd4sim model

ILC Calorimetric conditions

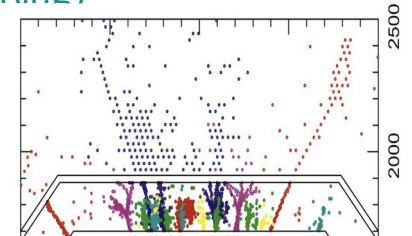


N.B. Final numbers may vary

e+e- physics



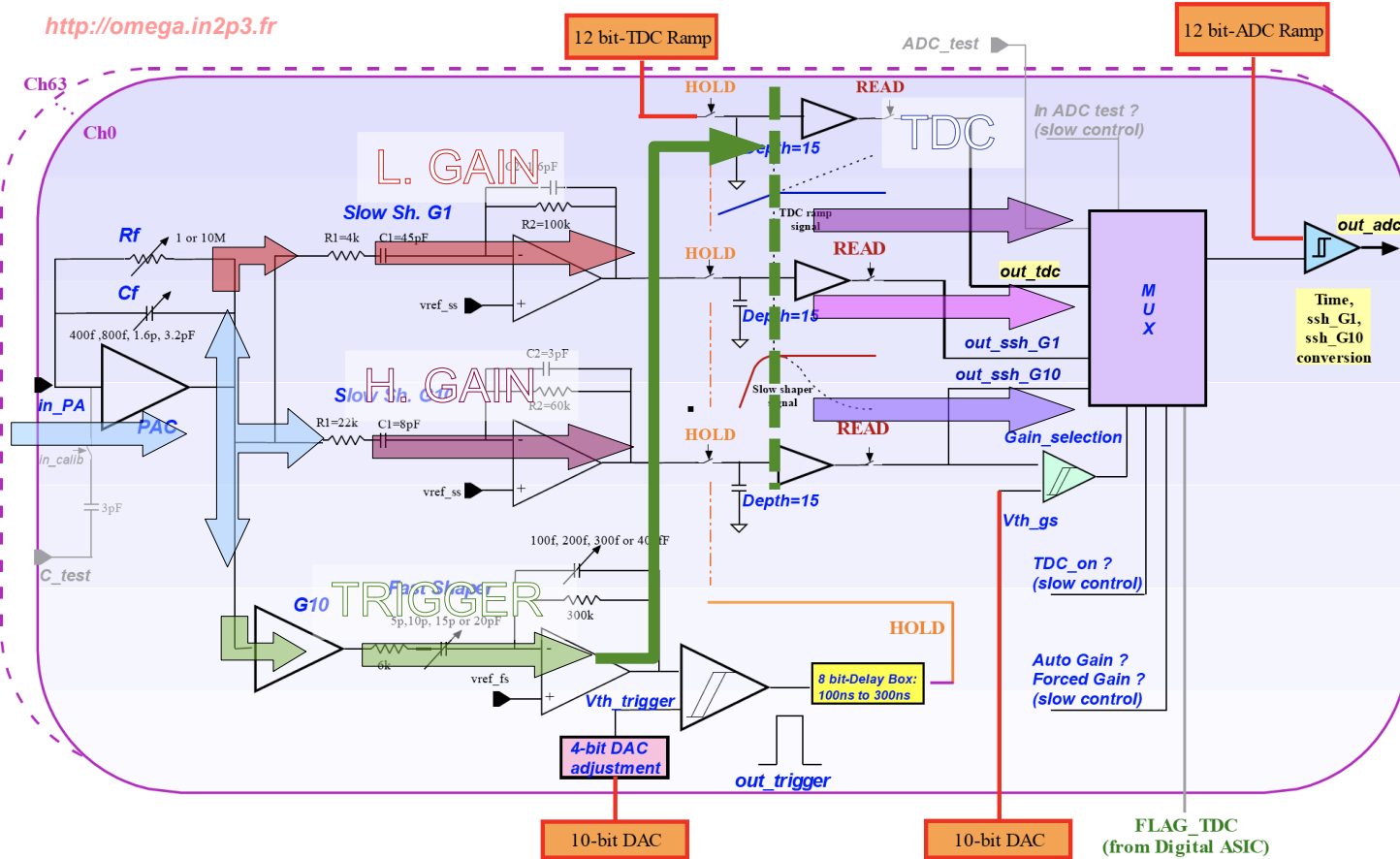
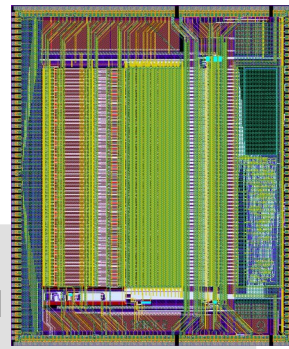
- Energy ranges: 250 – 500 GeV
 - 90 and 1000 GeV envisaged
- Low occupancy in central region
 - but at low angles (FCAL / Ring)
- Self-triggering, local buffering and conversion



Pulsed operation:

- Very limited (1%) acquisition time
- bunch train ~ 1 ms
 - largely spaced bunches (2-400 ns)
- Ample time for delayed conversion and readout

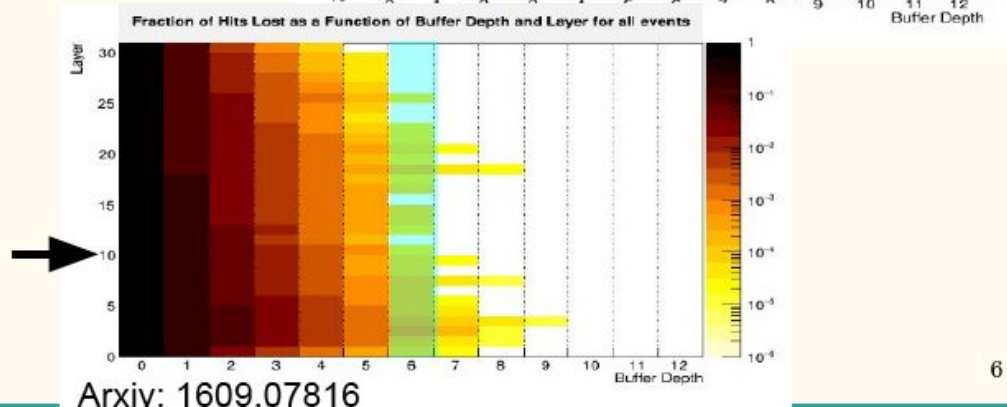
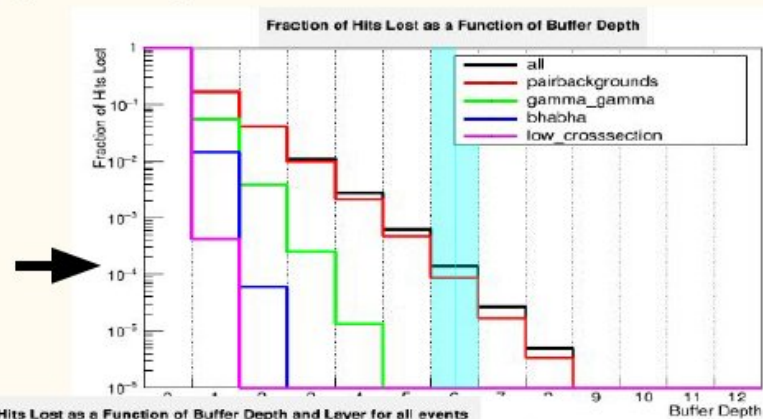
<http://omega.in2p3.fr>



- **64 channels**
- **Auto-triggered**
 - per cell adj.
 - 1 cell triggers all
- **Preamp**
+ **2 Gains** + Auto-select
+ **TDC (~1.4ns)**
- **15 (x2) analogue memories**
- **Dyn range 0.1 ~ 2500 mips**
 - mip in 320 μm (4 fC)
 - 12 bits ADC's
- **616 config bits**
- **Low consumption**
 - 25 $\mu\text{W}/\text{ch}$
with 0.5% ILC-like duty cycle
- **Power-Pulsed**

KPiX Studies - Buffer Multiplicity

- Forward multiplicity might be more than 4 buffer KPiX (current design) could handle
 - Recent optimization studies indicate that 6 buffers will be adequate, taking into account all known processes.
- 6 buffers also improve fractional hit loss within detector at shower max and radially
- Must study KPiX to see if more buffers might be added while preserving architecture (preconceptional ideas only)

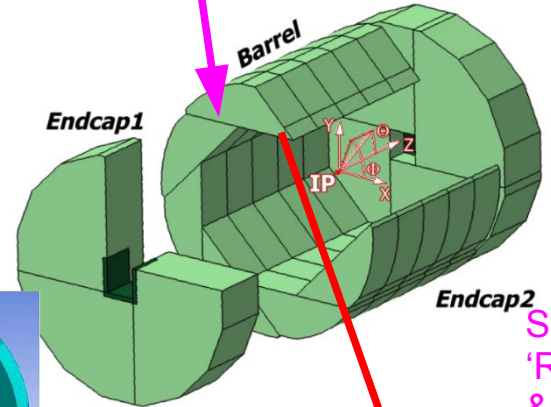
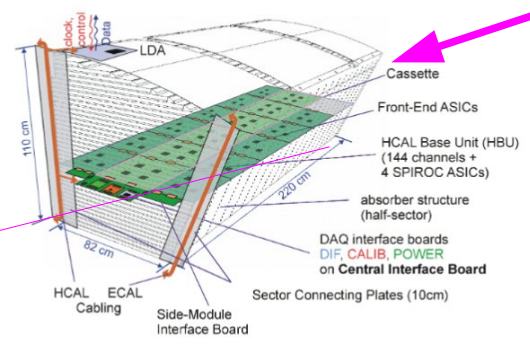


Geometries & Services

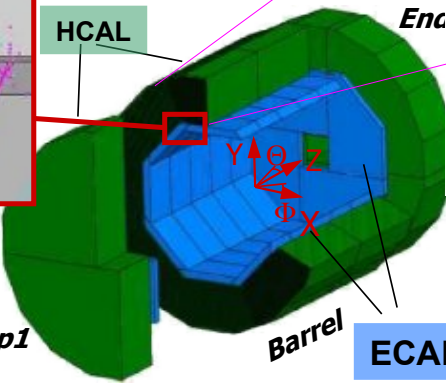
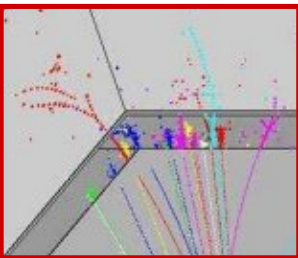


HCAL elec 'accessibility'

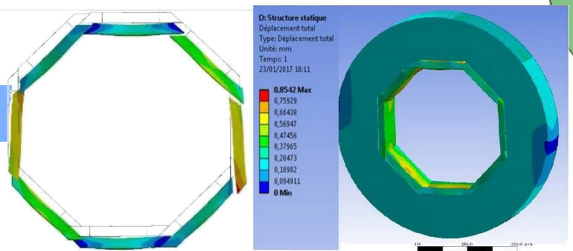
Prism vs diapragm



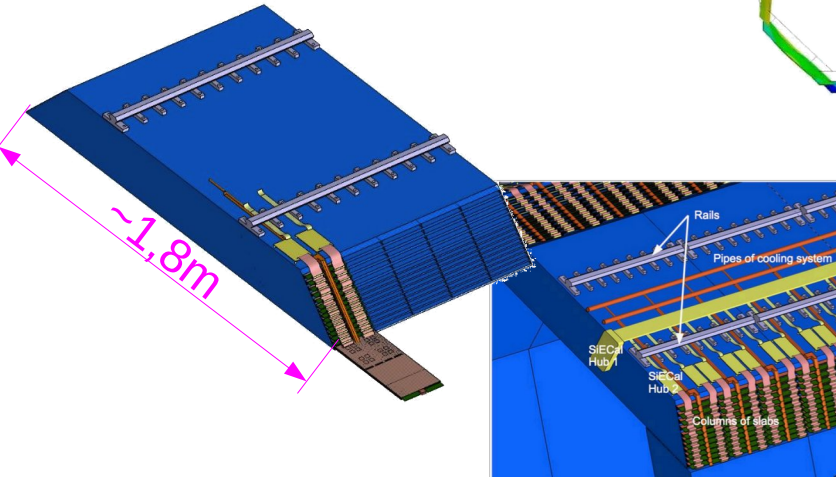
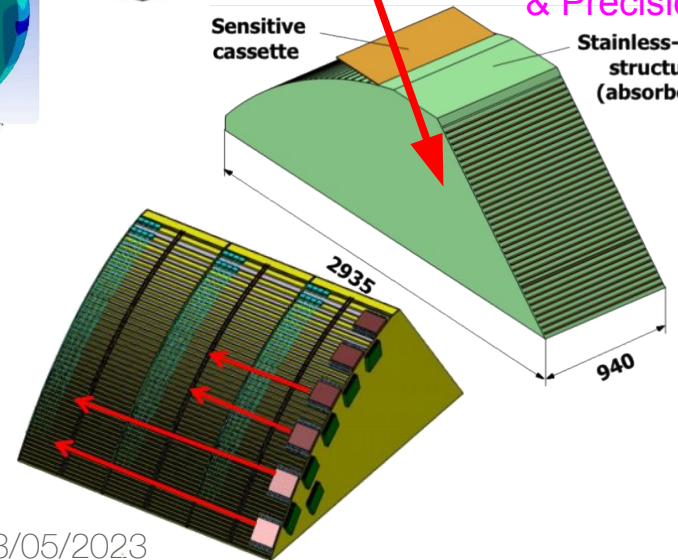
Structural 'Robustness & Precision'



(SiD = 12 fold)

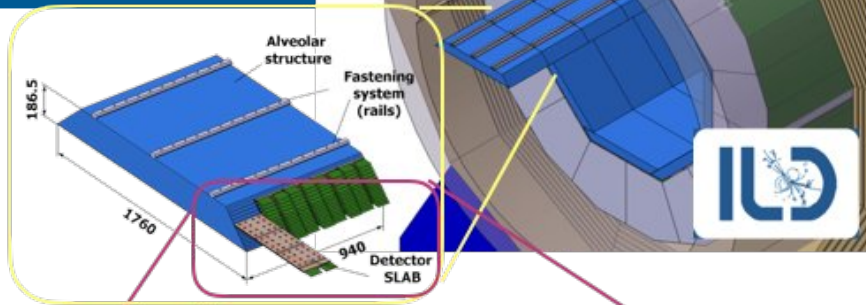


Sensitive cassette
Stainless-steel structure (absorbers)

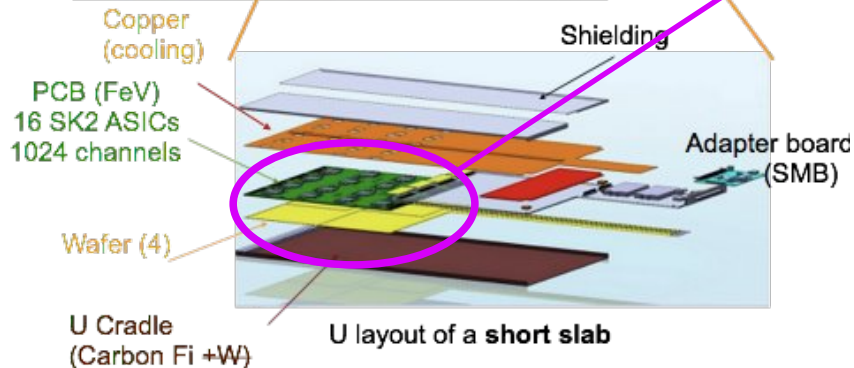


Large Scale Building : CALICE ECALS

ILD & SiW-ECAL barrel



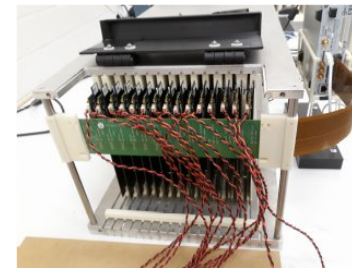
'Base unit'



ILD ECAL

Prototyped*

~10,000 SLAB's	~0.1
100,000 ASU's	~20
400,000 Wafers	~350
1,600,000 ASIC's	~1000
100,000,000 channels	~20000
	*incl.



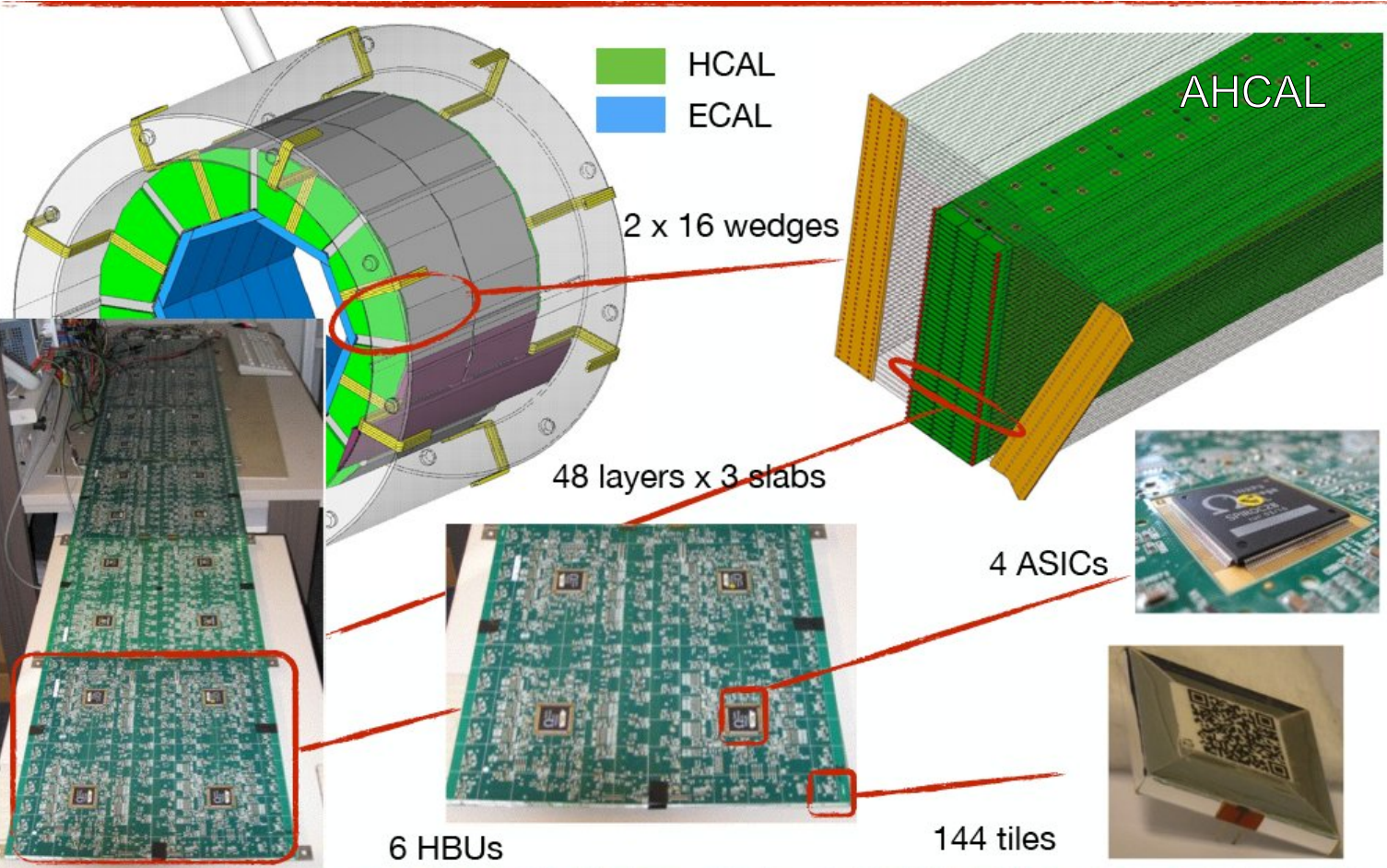
SiW-ECAL
0,5×0,5 cm²
×15 couches +W



ScW-ECAL
0,5×4,5 cm²
×30 layers + SS

[See Adrian's presentation](#)

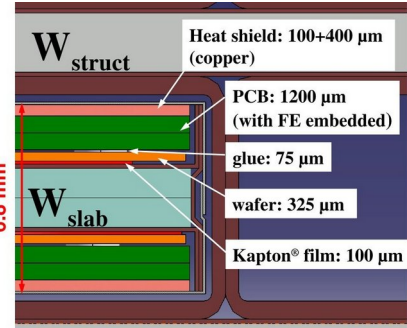
Large Scale Building : CALICE HCALS



See Katja's presentation 48

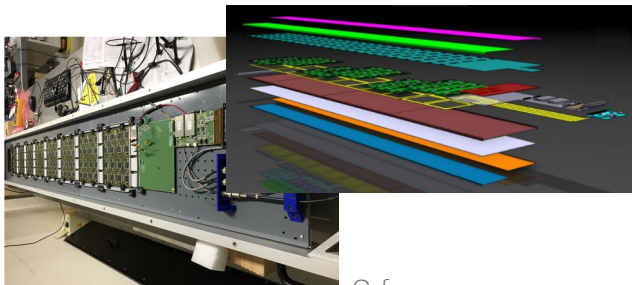
CALICE Thin, long cassettes → all prototyped

Silicon / Scint W-ECAL



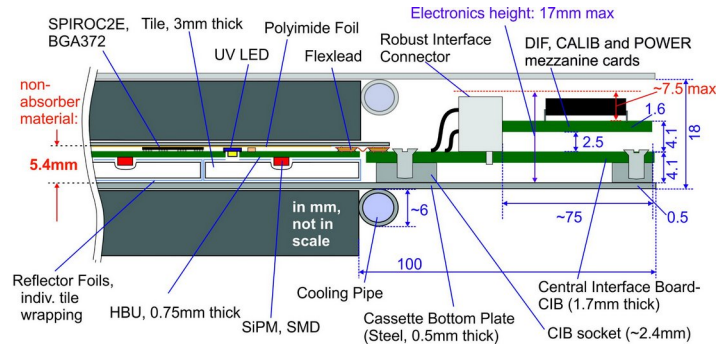
≤1.8m long

– Passive cooling



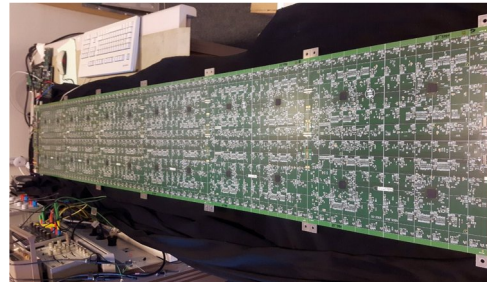
vincent.bouary@in2p3.fr

Scint Analog HCAL (also used for HGCAL)

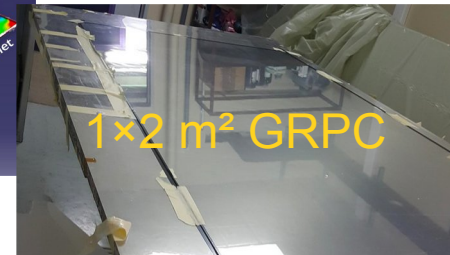
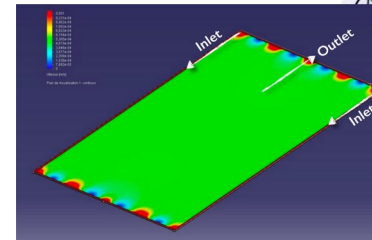
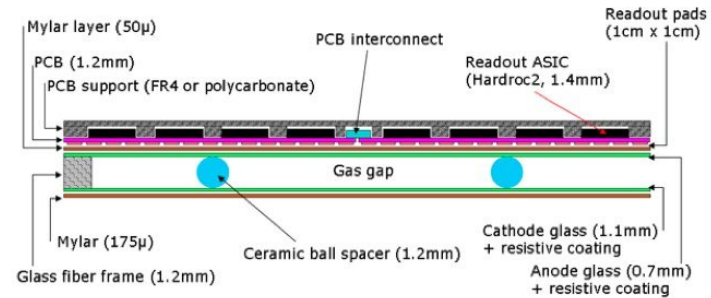


≤ 3m long

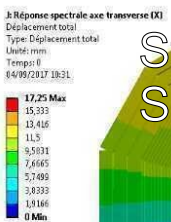
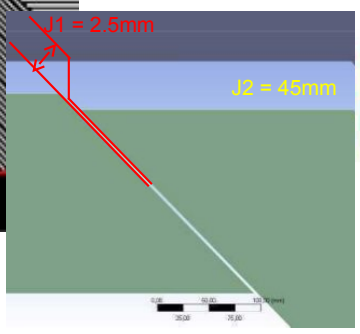
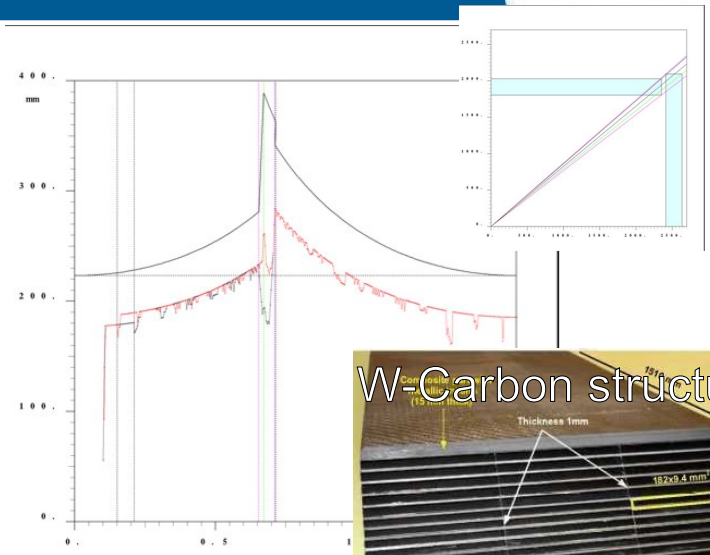
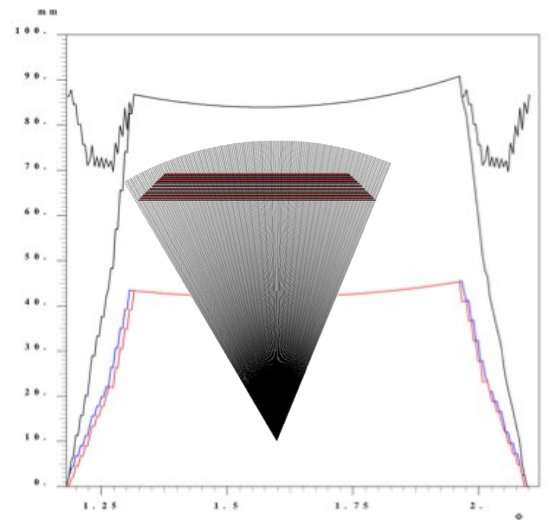
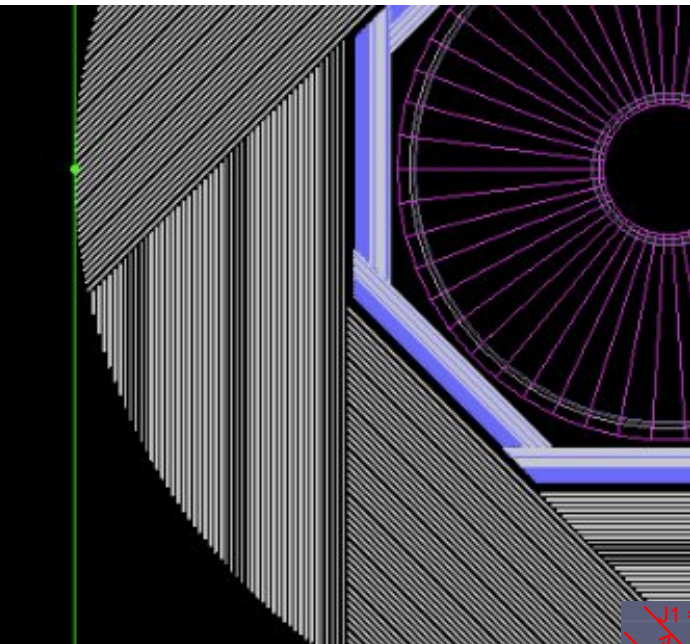
No cooling or gas flow



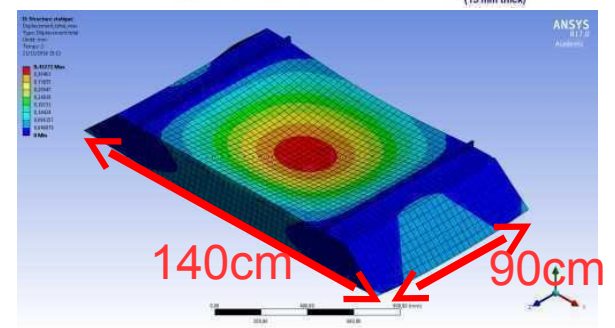
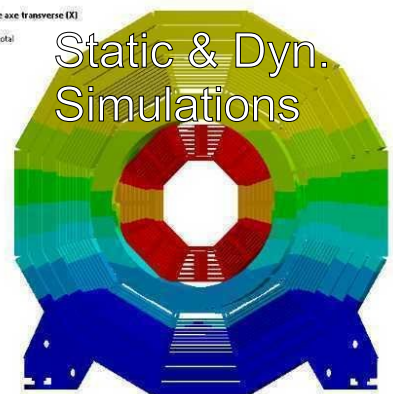
(Semi)Digital Gaseous HCAL



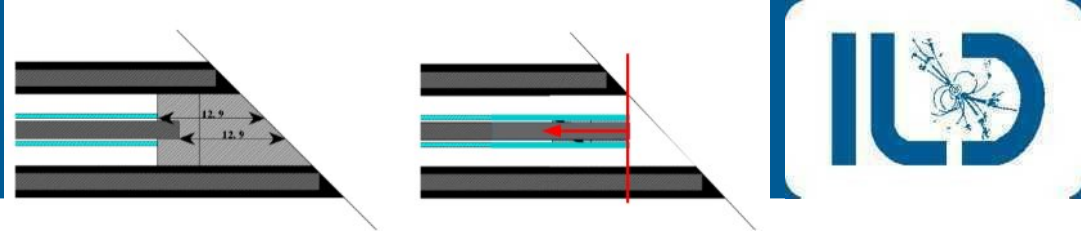
A crack-less ECAL geometry



Static & Dyn.
Simulations

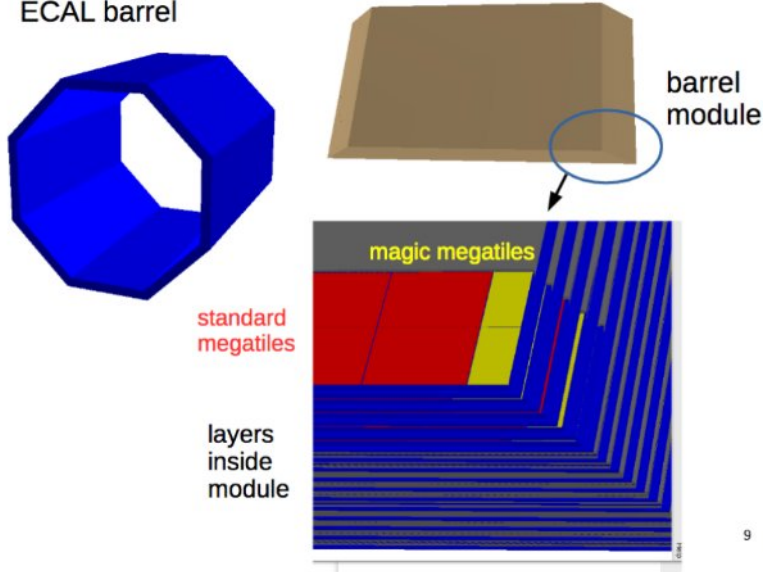


Simulation



ECAL driver used in ILD models has been largely re-written (Mokka → DD4HEP)

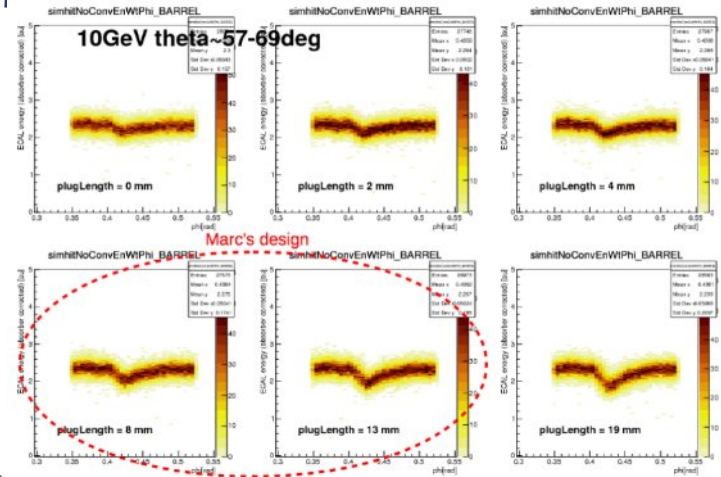
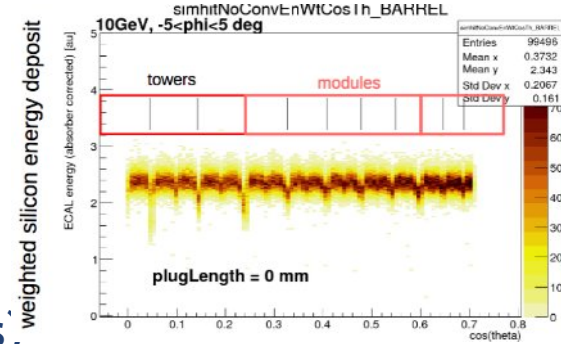
- more modular code:
 - less duplication Barrel & Endcap
 - more configurable...
- ECAL barrel



Effect of cracks [RAW= no correction at all!]

- Drop ~ 15%

Effect of plug (missing in previous simulations)



SiD SiW-ECAL

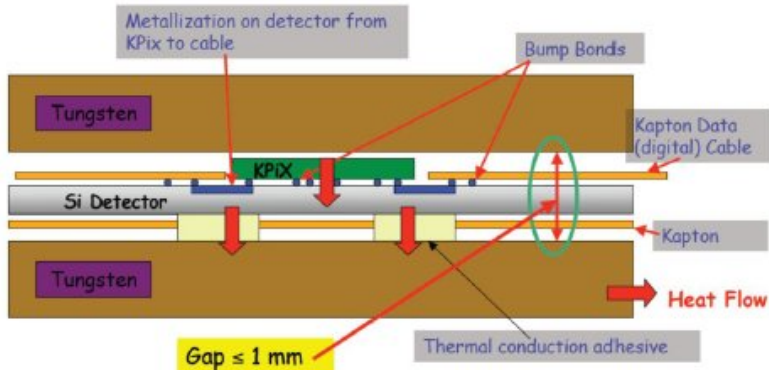
HCALS ~similar to ILD's:
DHCAL, AHCAL

20 + 10 layers

1.25 mm gap between W layers

- Minimize R_M (~ 13 mm effective)
- Keep calorimeter compact

Tungsten plates \Rightarrow thermal bridge to cooling



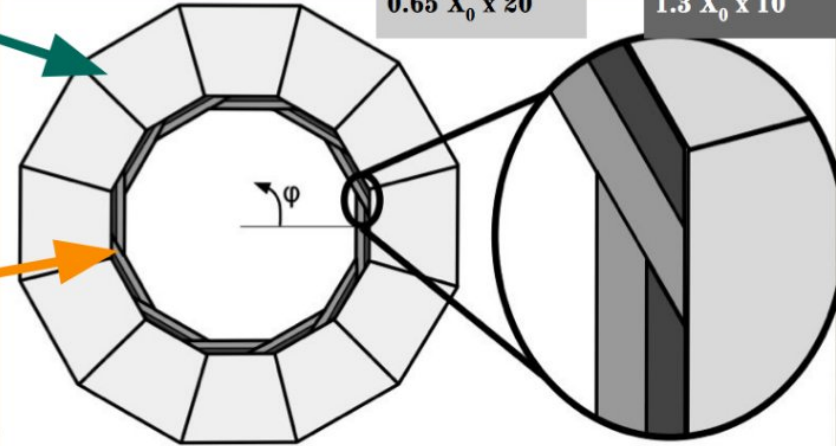
Calorimeter Geometry

HCal

Scintillator sampling calorimeter
Steel/polystyrene

ECal

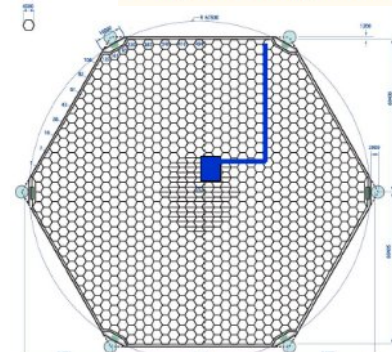
Solid state sampling calorimeter
Tungsten alloy/silicon

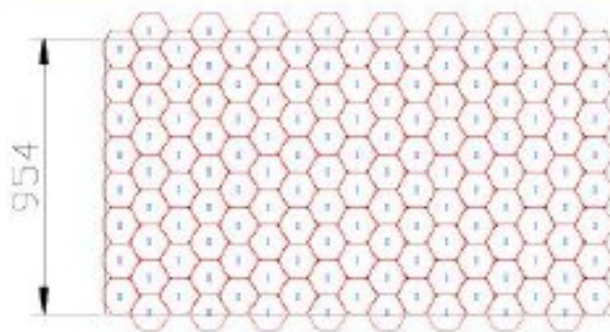
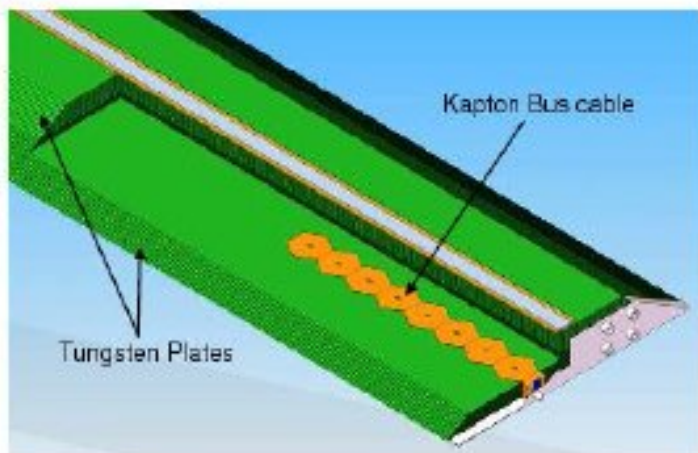
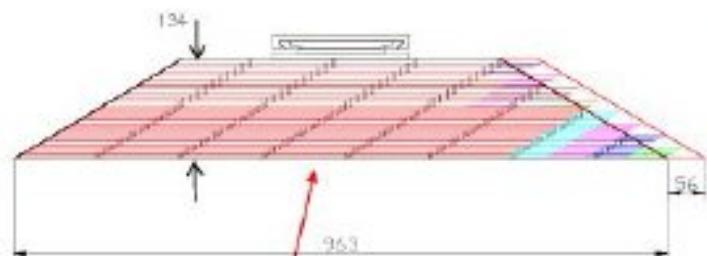


Hexagonal Wafers (optim material)

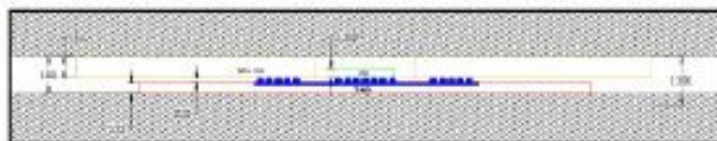
1 Kpix Chips (1024 ch) per Wafer

- Bump Bounder on Sensor.





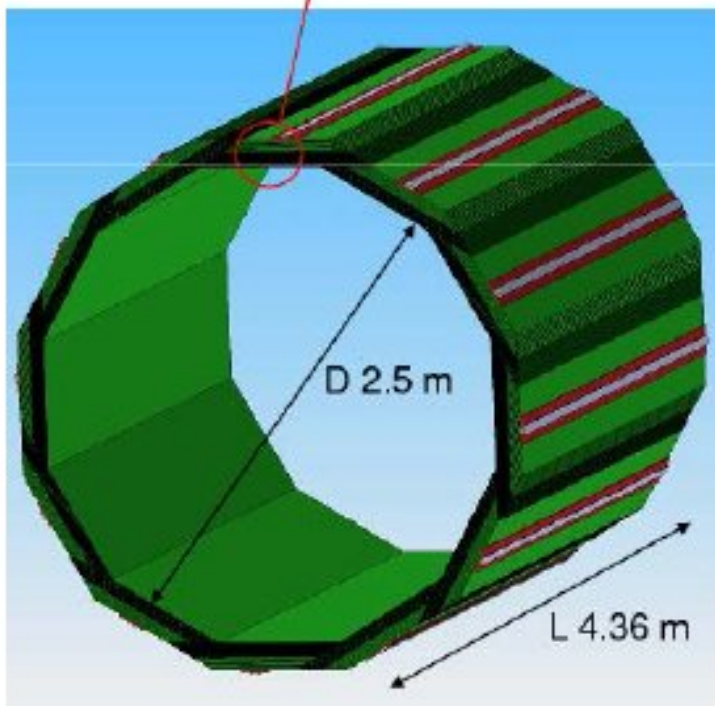
Hexagon sensors arrangement



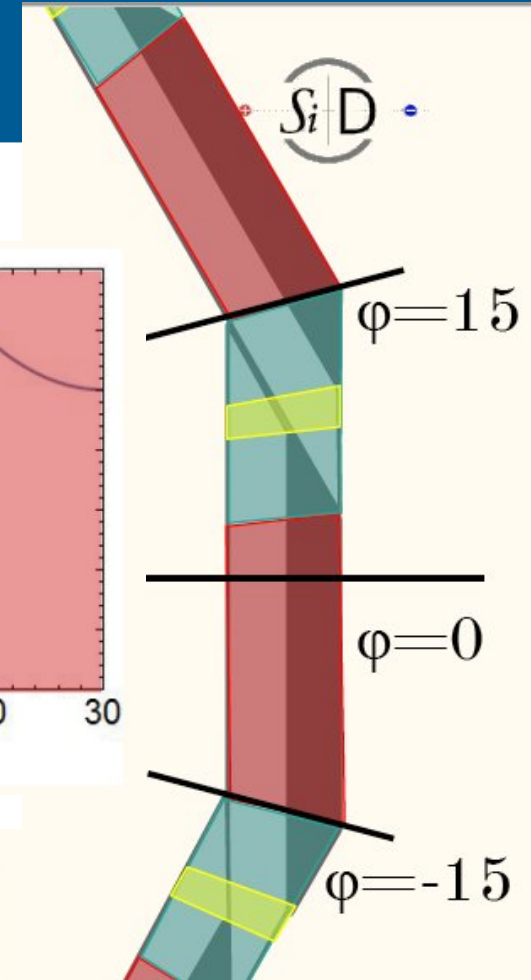
detector module between tungsten plates

Baseline configuration:

- transverse: 12 mm² pixels
- longitudinal: (20 x 5/7 X₀) + (10 x 10/7 X₀) ⇒ 17%/sqrt(E)
- 1 mm readout gaps ⇒ 13 mm effective Moliere radius



Geometry & Calibration studies

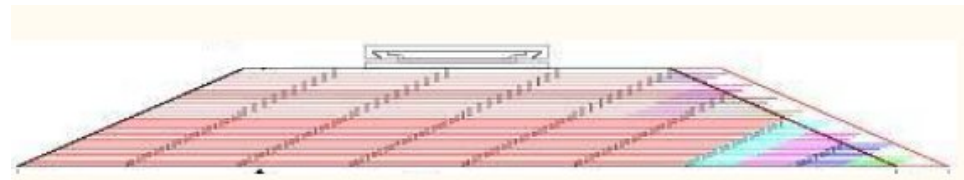
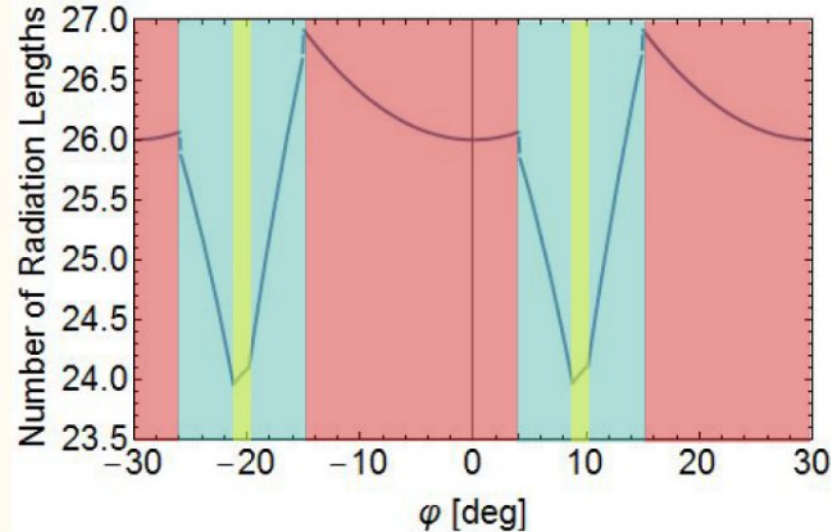


Periodic structure - $\varphi = 30^\circ$ increments

- Entire module,
- overlap region,
- thin overlap region

30% of detector coverage has overlapped modules

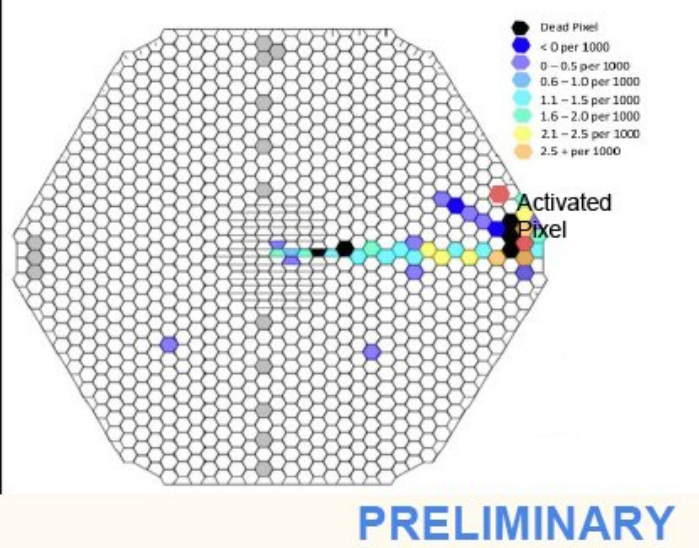
+ leakage corrections



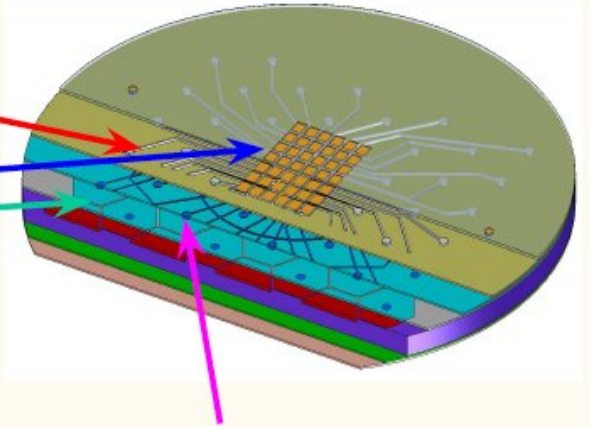
Prototype testing

Laser injection in single pad

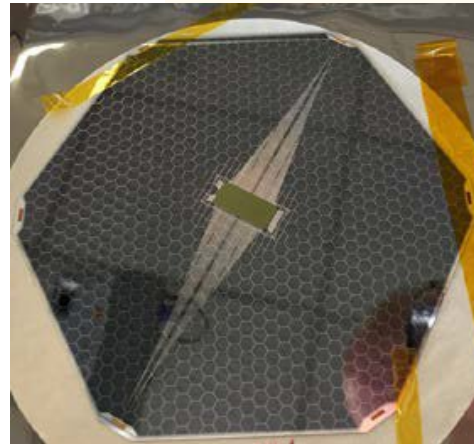
Probe Tested Laser



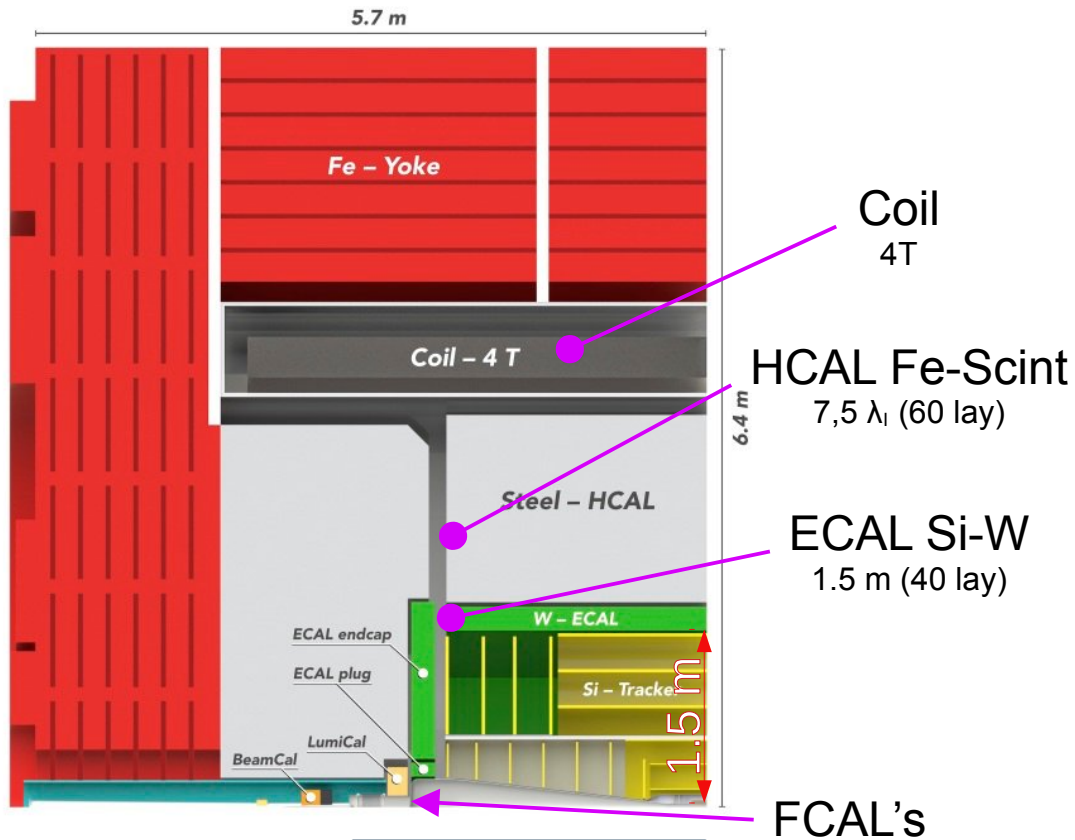
In present design, **metal 2 traces** from pixels to pad array run over other pixels: parasitic capacitances cause crosstalk.



New scheme has “same” metal 2 traces, but a fixed potential metal 1 trace shields the signal traces from the pixels.



CLICdet



CLICdp-Note-2018-005

ILD and SiD-based

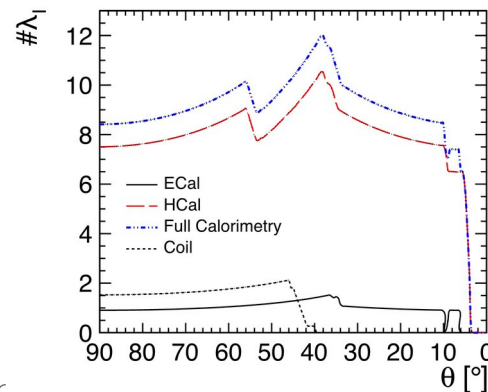
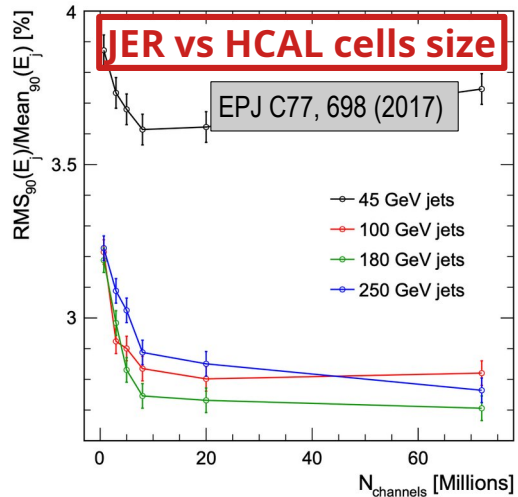
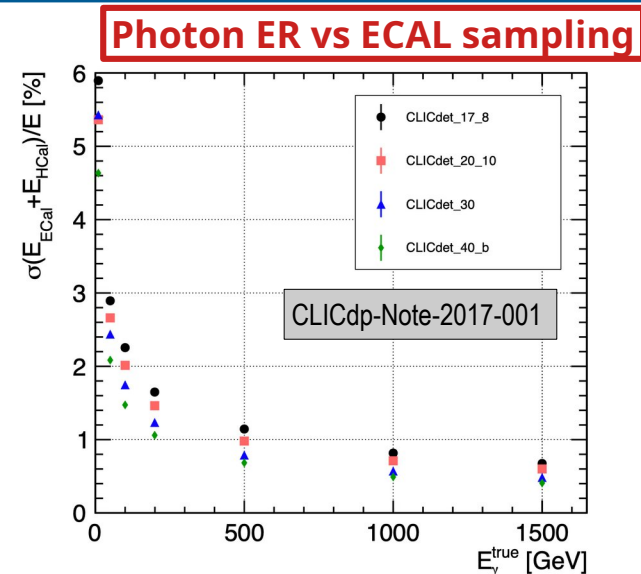
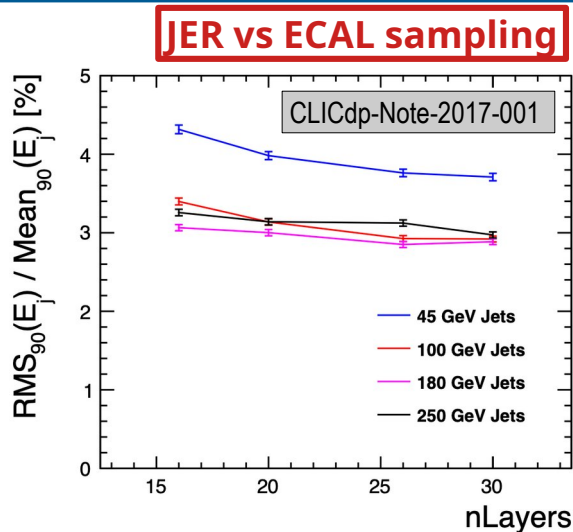
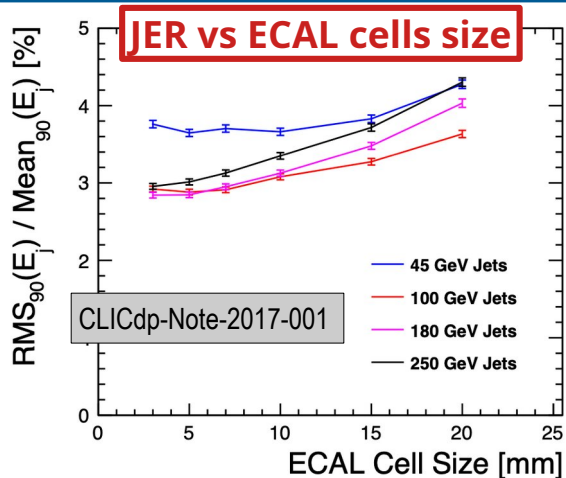
CLIC:

- Energy range : 380 GeV – 3 TeV
- Beam timing :
 - at CLIC: $\Delta t_{\text{bunches}} = 0.5 \text{ ns}$; $\text{frep} = 50 \text{ Hz}$
 - at ILC: $\Delta t_{\text{bunches}} = 554 \text{ ns}$; $\text{frep} = 5 \text{ Hz}$
- Power Pulsing ✓

ILD-based

- SiTracker
- ECAL : 30 → 40 Layers
- HCAL: 40 → 60 Layers
 - (W envisaged & dropped)

Some CLICdp optimisation studies



Implication of HL schemes

Higher $\mathcal{L} \Rightarrow$

- Occupation / bunch train ↗
 - More memory for events
 - But large margins

Higher repetition rates × longer bunch

- $Power = f_{rep} \times \sum P_{ASIC_part} \times \tau_{spill_part}$

• $\tau_{spill} = \tau_{Ramp-up} + \tau_{Train} + \tau_{Conv}$
 $= \mathcal{O}(\mu s) + \{ \dots \} + \mathcal{O}(100's \mu s)$

- $\tau_{Train} = \Delta T_{bunches} \times N_{bunches}$

- $\tau_{Conv} \propto (\text{occupancy} + \text{Noise} \geq \text{thr.})$

Critical also for Power budget

⇒ Full ZERO suppr. needed

HL-ILC:

- $\mathcal{L} \times 4$ (6)
- $N_{bunches} \times 2 : \tau_{Train} : 1 \rightarrow 2 \text{ ms}$
- $f_{rep} \times 2$ (3): $5 \rightarrow 15 \text{ Hz}$

Dominated by ACQ time:

$P(\sim 25\mu W/ch) \times 6$

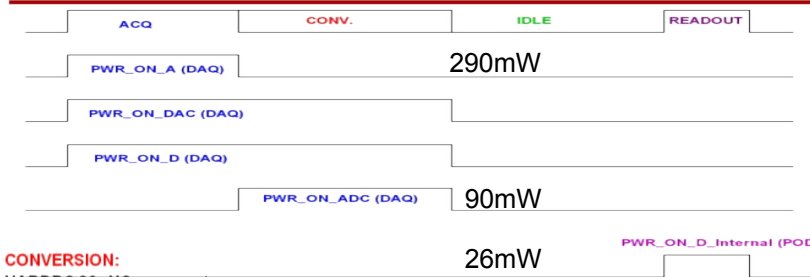
HL-CLIC:

- $\mathcal{L} \times 2$
- $N_{bunches} \rightarrow : \tau_{Train} : 176 \text{ ns}$
- $f_{rep} \times 2 : 50 \rightarrow 100 \text{ Hz}$

Dominated by Set-up &

Conversion time: $P(\sim 82\mu W/ch) \times 2$

Power pulsing lines timing



SK2 chips

64 ch full conversion

CONVERSION:

HARDROC2: NO conversion

SPIROC2: max time (Full chip)= 16 SCAx 2 (HG or LG/Time) x 103 μs =3.2ms

SKIROC2: max time (Full chip)= 15 SCA x 2 (HG or LG/Time) x 103 μs = **3 ms**

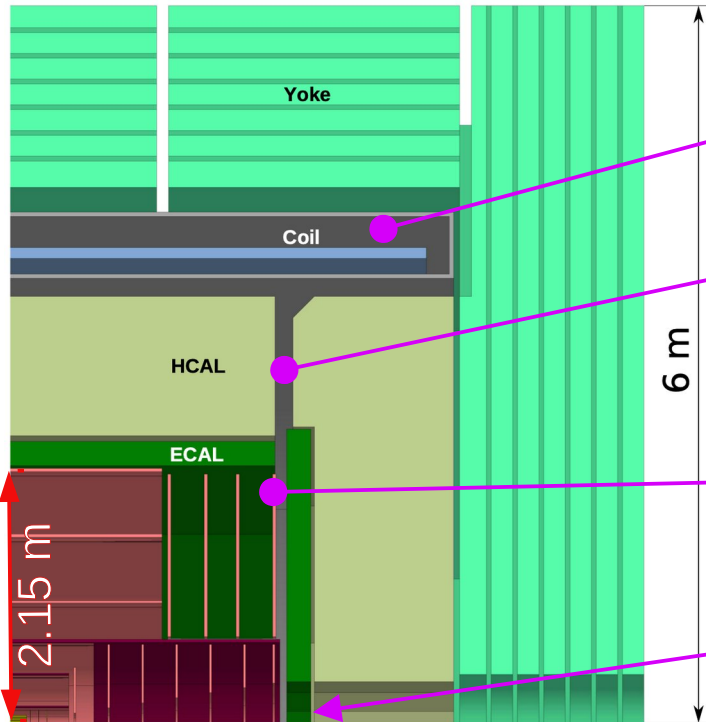
READOUT:

HARDROC2: 127 (memory depth) x [64 channelsx 2 trigger bits + 24 BCID bits + 8 Header bits]=20 320 bits => 200 nsx20k=4 ms/ Full Chip (WORST case)

SPIROC2: 16 SCAx2 (HG or LG/Time) x 36 ch x 16 ADC bits + 16 SCAx16 BCID bits + 16 Header bits= 18 704 bits => **3.8 ms/Full Chip (Worst case)**

SKIROC2: 15 SCAx2 (HG or LG/Time) x 64 ch x 16 ADC bits + 15 SCAx16 BCID bits + 16 Header bits= 30 976 bits => **6 ms/Full Fhip (Worst case)**

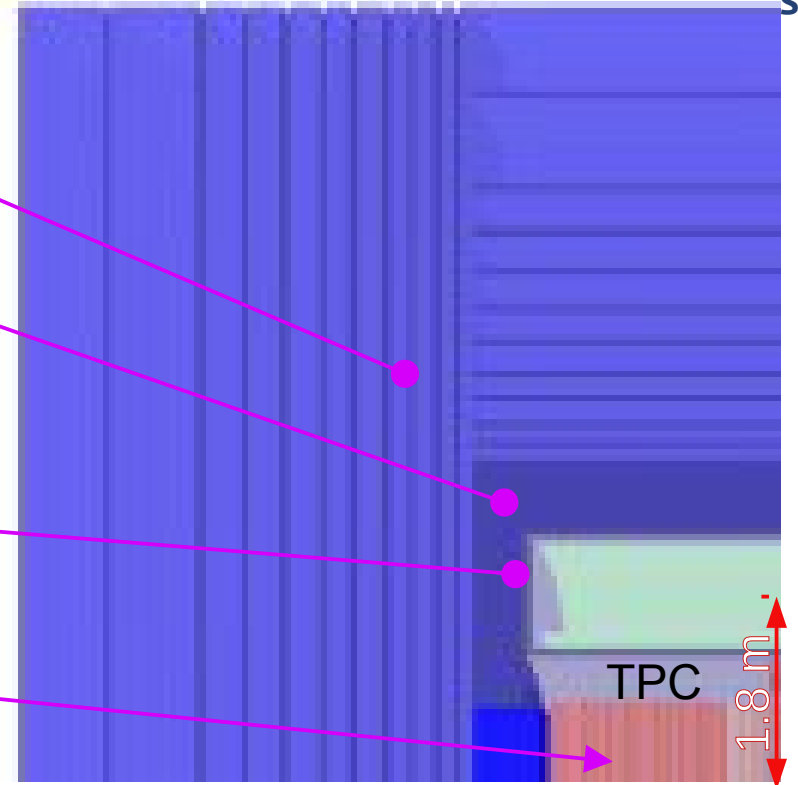
CLD



LCD-Note-2019-001

CLICdet based

baseline



Coil

4T vs 3/2T

HCAL Fe

Scint vs GRPC

7,6 λ_i vs 4.9 λ_i
60 lay vs 40 lay.

ECAL Si-W

1.5m vs 1.8m
(40 lay.) vs 30 lay

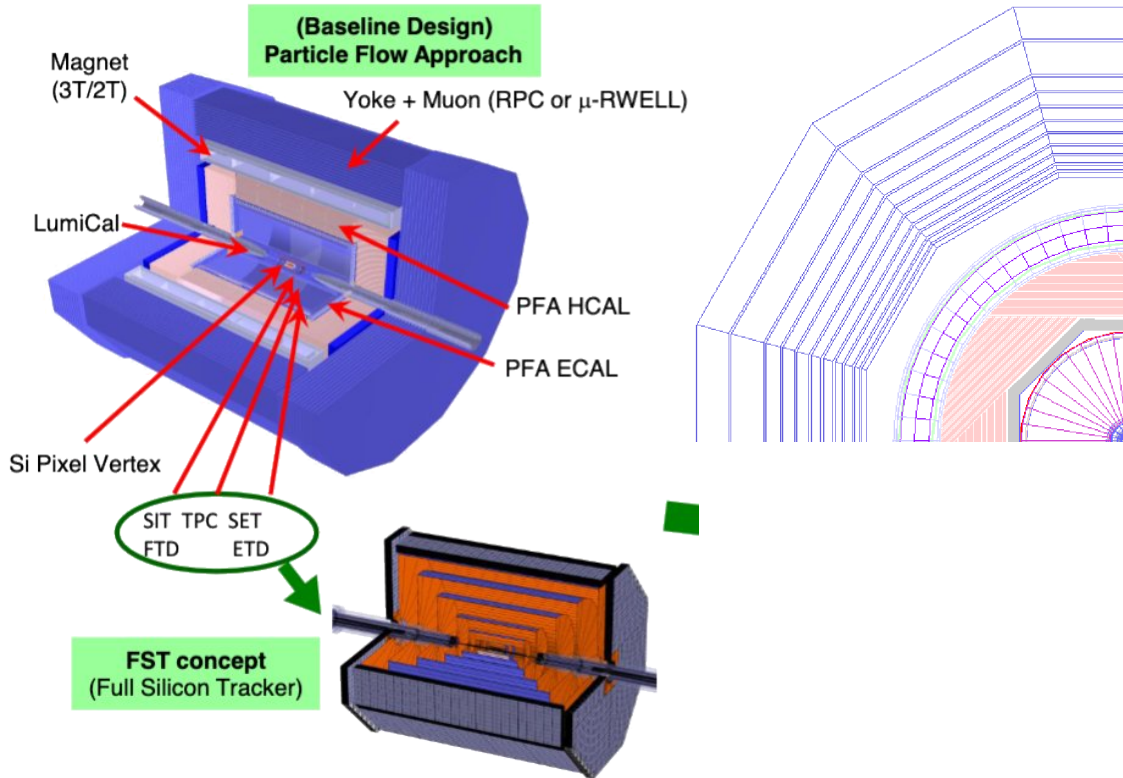
no FCAL's (yet)

ILD-based

CEPC CDR (2018)

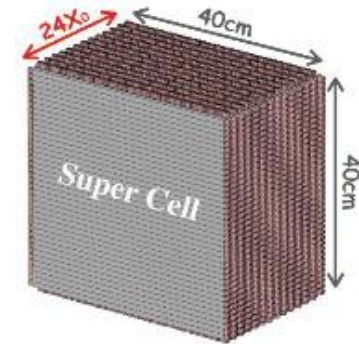
G. Li @ ECFA HF : 1st topical meeting on simulations, Feb. 2022

CEPC Baseline Options



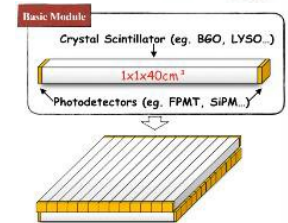
ECAL:

- SiW-ECAL:
- Strip scintillators
- Crystals



HCAL:

- SDHCAL
- Scintillators (AHCAL)



Linear vs Circular Collider's Conditions

Linear (ILC, HL-ILC...)

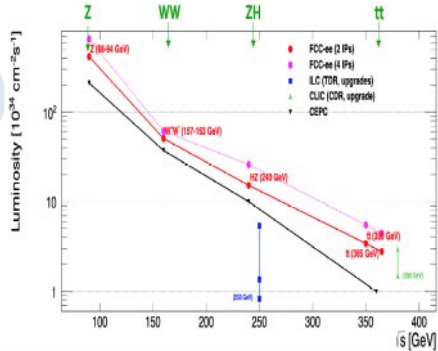
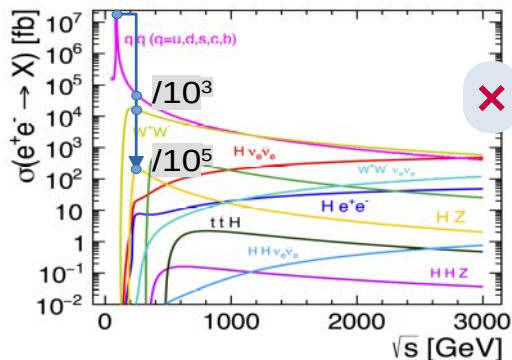
- 250 GeV (ZH), 365 GeV (tt), 500 GeV (ZH) + [1000 GeV], $\mathcal{L} \sim \text{cst.}$
- Power pulsing : 5 [10–15]Hz × 1 [2] ms

Moere diverse et stringer conditions:

- $90\text{GeV} \times 10^7 \text{ fb} \times 5 \cdot 10^{36} \text{ cm}^{-2} \text{ s}^{-1}$ (qq × 20,000 ILC @ 250)
- 150 GeV (WW) + 250 GeV (ZH)+ 365 GeV (tt)
 $\sim 10^4 \text{ fb} \times 5 \cdot 10^{35} \text{ cm}^{-2} \text{ s}^{-1}$ (qq × 5–10 ILC @ 250)

From Pulsed to Contious operation

- ASIC, Power/Cooling, DAQ, Granularity, Precisions (E, t), New ideas...



HL-ILC:

- $\mathcal{L} \times 4$ (6)
- $N_{\text{bunches}} \times 2 : \tau_{\text{Train}} : 1 \rightarrow 2 \text{ ms}$
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HL-CLIC:

- $\mathcal{L} \times 2$
- $N_{\text{bunches}} \rightarrow : \tau_{\text{Train}} : 176 \text{ ns}$
- $f_{\text{rep}} \times 2 : 50 \rightarrow 100 \text{ Hz}$

Dominated by Set-up &

Conversion time: $P(\sim 82\mu\text{W}/\text{ch}) \times 2$

FCC-ee parameters		Z	W*W*	ZH	ttbar
\sqrt{s}	GeV	91.2	160	240	350-365
Luminosity / IP	$10^{34} \text{ cm}^{-2} \text{ s}^{-1}$	230	28	8.5	1.7
Bunch spacing	ns	19.6	163	994	3000
"Physics" cross section	pb	35,000	10	0.2	0.5
Total cross section (Z)	pb	40,000	30	10	8
Event rate	Hz	92,000	8.4	1	0.1
"Pile up" parameter [μ]	10^{-6}	1,800	1	1	1

Experimentally, Z pole most challenging

- Extremely large statistics
- Physics event rates up to 100 kHz
- Bunch spacing at 20 ns
 - "Continuous" beams, no bunch trains, no power pulsing
- No pileup, no underlying event ...
 - ...well, pileup of 2×10^{-3} at Z pole

<https://indico.cern.ch/event/1064327/contributions/4893208/>
 Mogens Dam @ FCC Week, 10/06/2022

Detector Parameters: scaling rules

- Cell lateral size
 - Shower separation (EM $\sim 2\times$ cell size)
 - Cell time resolution (1 cm/c \sim 30 ps)
 - Time performance for showers
 - » ParticleID, easier reconstruction
- Longitudinal segmentation
 - sampling fraction
 - E resolution (ECAL $\sim 15\%/VE$)
 - shower separation/start
- ECAL inner radius; Barrel Z_{Start}
- ECAL–HCAL distance
- Barrel–Endcap distance
- Dead-zones sizes (from Mechanics, Cooling)

Number of cells $\nearrow \Rightarrow$ Cost \nearrow (1/size²)
Cell density $\nearrow \Rightarrow$ Power consumption \nearrow
Time resolution $\searrow \Rightarrow$ Power \nearrow

***threshold, passive vs active
cooling
dead-zones \nearrow***

**NEED TO BE FULLY RE-EVALUATED
for EW region
 \rightarrow Cooling change ?
 \rightarrow New electronics !**

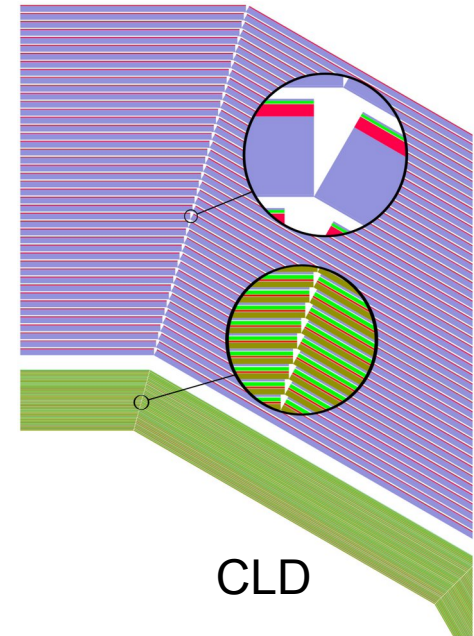
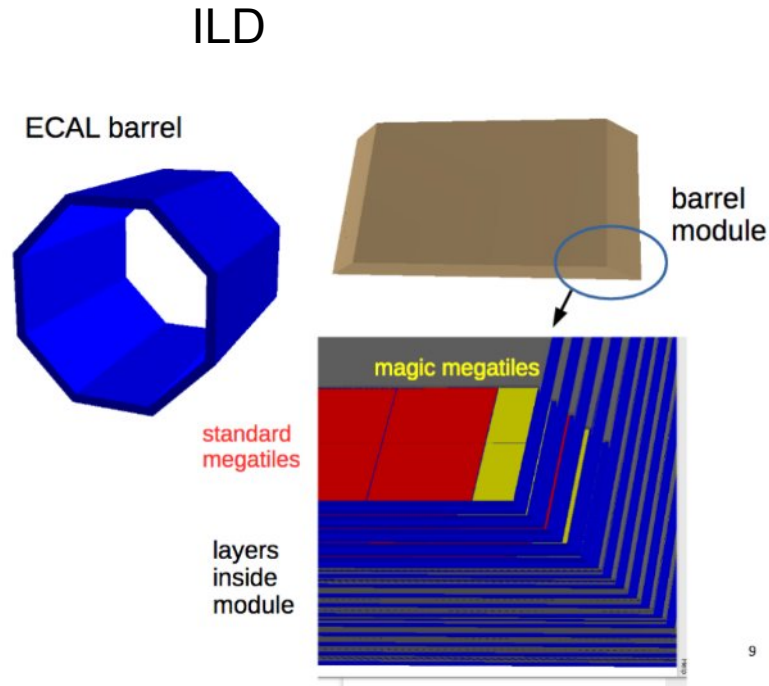
Inner Radius $\nearrow \Rightarrow$ Tracking performance \nearrow
Cost \nearrow^2 (\Rightarrow Magnet, Iron)
Gaps $\nearrow \Rightarrow$ PFlow performances \searrow

Review of physical implication (from TeV): see [Linear collider detector requirements and CLD, F. Simon @ FCC-Now \(nov 2020\)](#)
Physics Requirement studies @ 250 GeV: see [Higgs measurements and others, M. Ruan @ CEPC WS, \(nov 2018\)](#)

Software

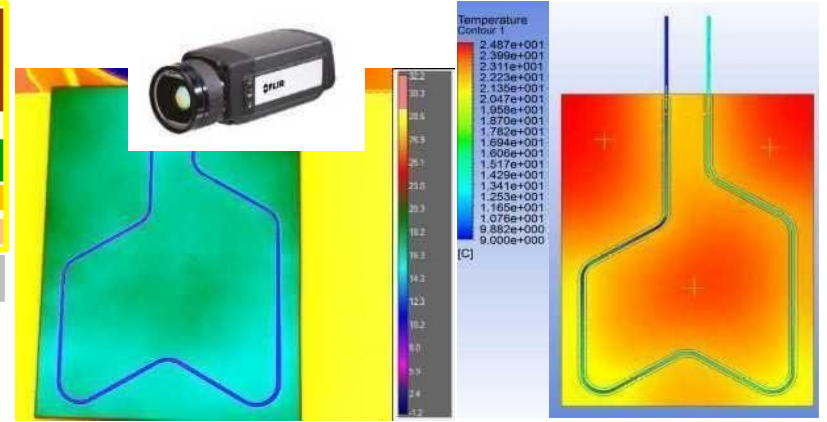
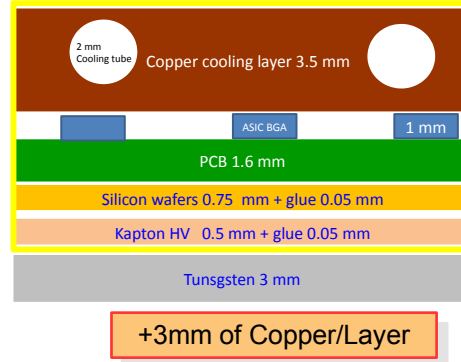
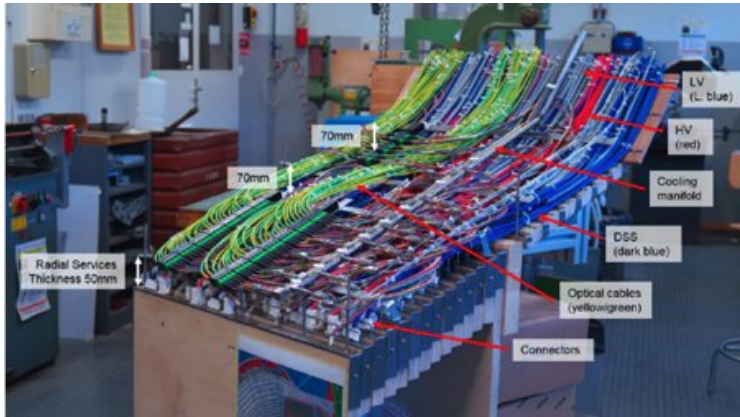
Full Simulations:

- All models in DD4SIM (\supset beam tests)
 - Sharing of models easier
- **ILD Model: dual technology**
 - No PCB's
 - Scint layer AND Silicon layer
 - GRPC and Scintillators
- **Simpler models in CLICdet, CLD, CEPC baseline**

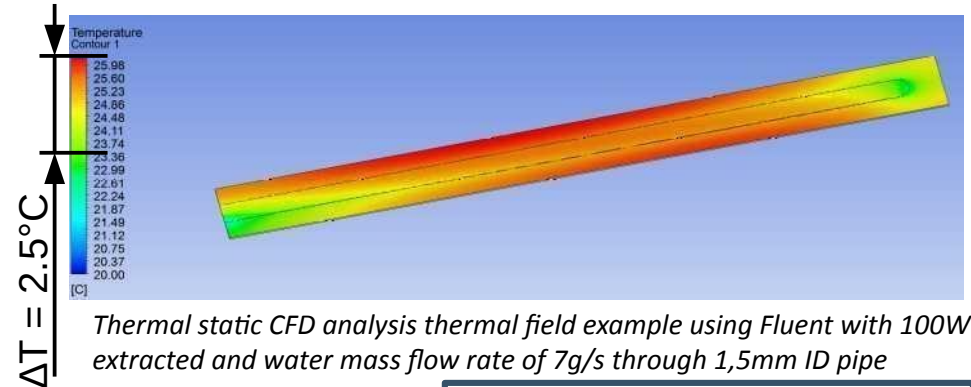
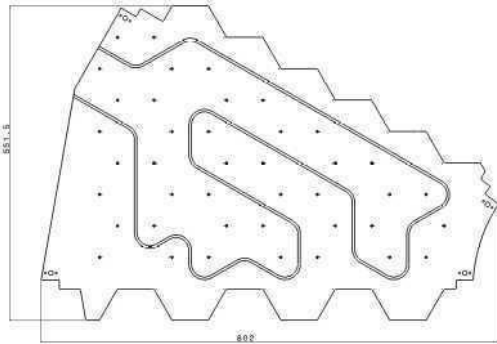


9

Services: integration & cooling



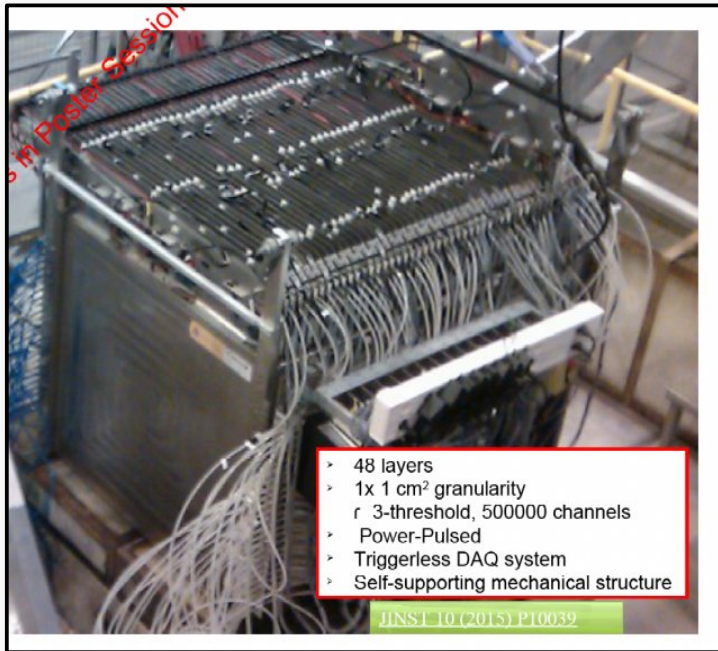
- Pipe insertion process introduces some efficiency loss due to the thermal contact resistance.
- The benefit remains significant with regard to a passive cooling



Thermal static CFD analysis thermal field example using Fluent with 100W extracted and water mass flow rate of 7g/s through 1,5mm ID pipe

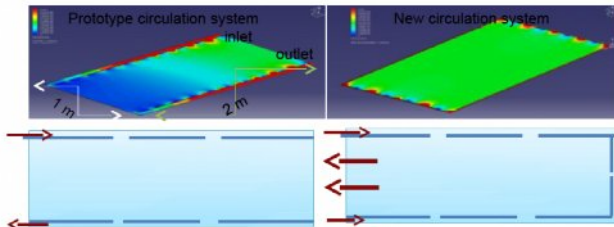
= 2x cont. operation of a SLAB

CALICE SDHCAL



- Detectors as large as 3x1m² need to be built
- Electronic readout should be the most robust with minimal intervention during operation.
- Mechanical structure with minimal dead zone
- Include time information SDHCAL -> T-SDHCAL

Large RPC detectors



Large mechanical structure

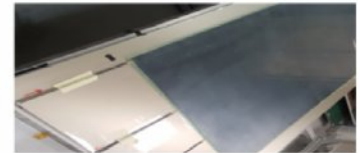
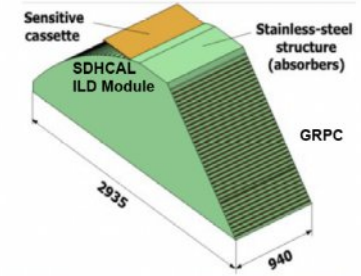
Flatness

Using roller leveling



Reduced dead zone

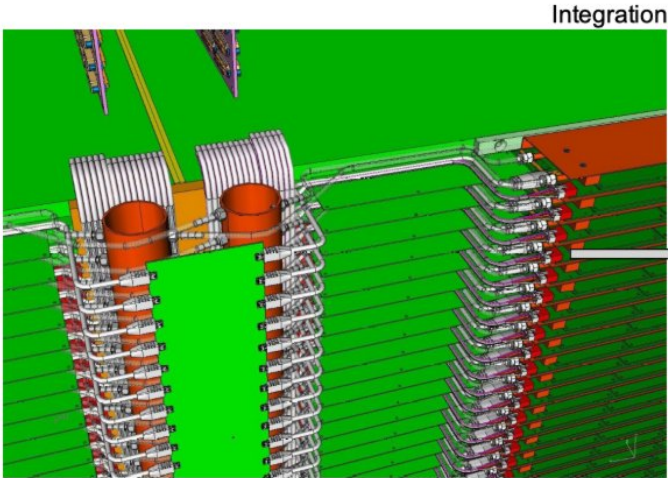
Using electron beam welding



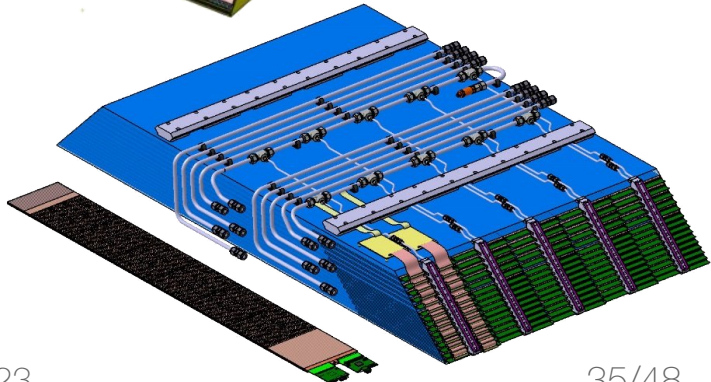
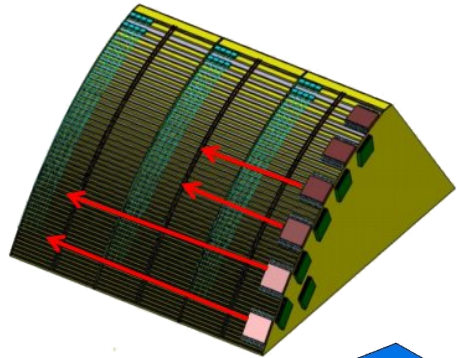
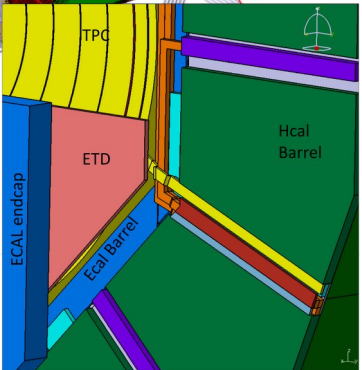
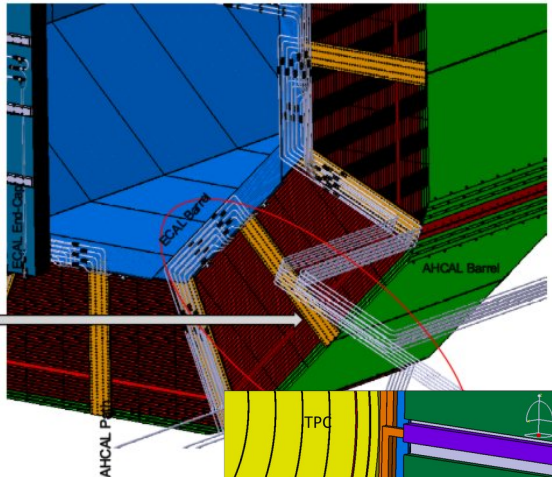
Services: integration & cooling

Task 2-2 AHCAL/ECAL services integration

- Detailed design of the AHCAL and ECAL services



Integration



Leakless Water cooling system

Thermique/Intégration

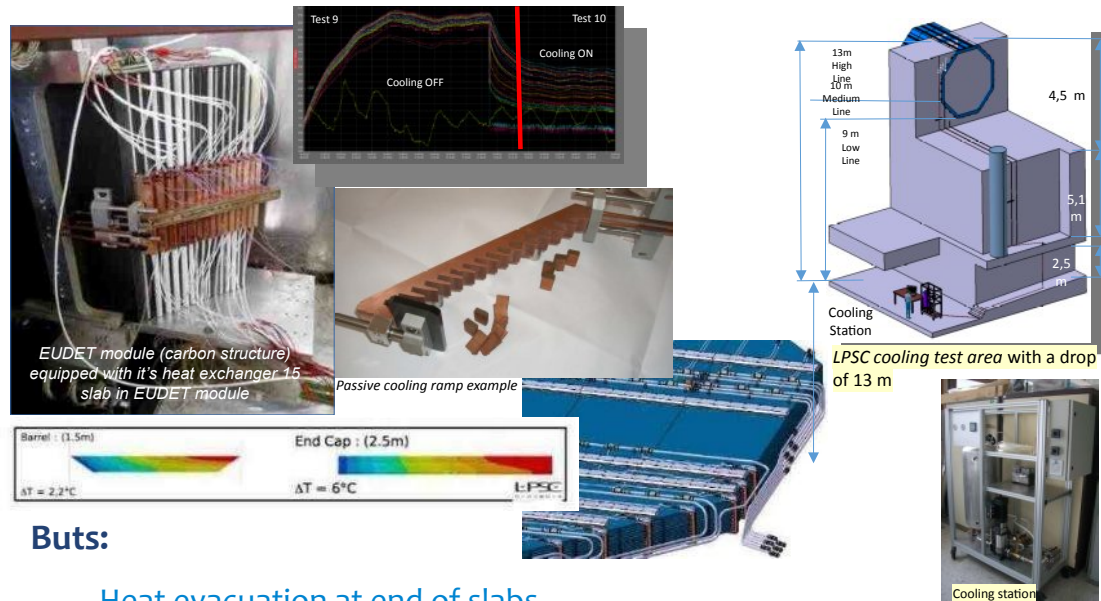
Modèle : sur module pilote

Maquette 1: 1:1

- simple circuit

Maquette 2: 3:4

- heat model in C-W structure



Buts:

- Heat evacuation at end of slabs
- Caloduc compatible with ECAL–HCAL spacing (3 cm)
- Leakless (depression)

To Do:

- Test sur on a full ECAL module

For FCC-ee:

- 1) Dimensioning for continuous working, if possible, without active cooling
- 2) if not, include a active cooling CO₂ (in Cu or W)

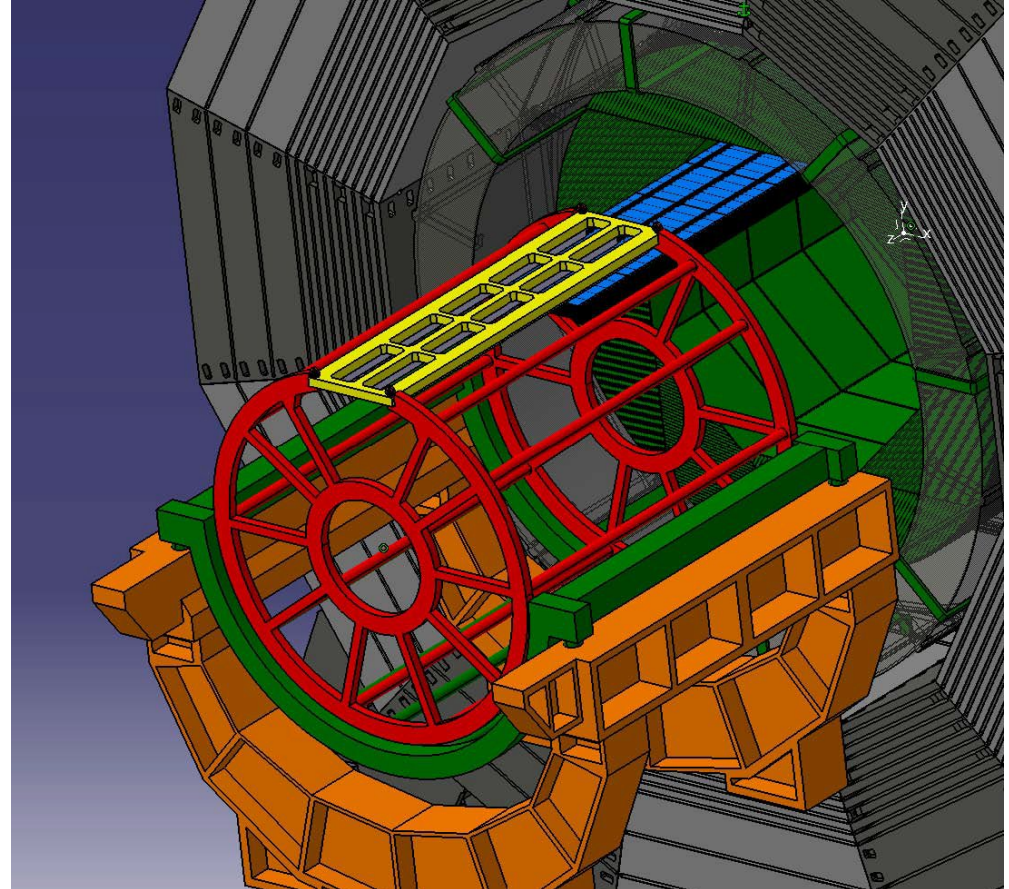
Building tools and procedure

Documented for ILD

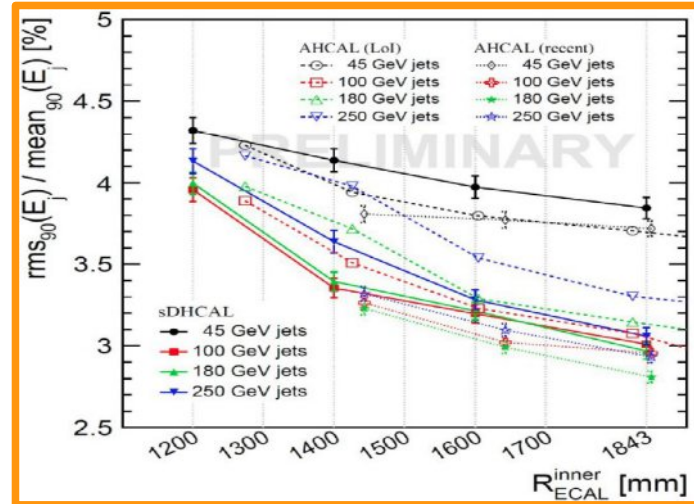
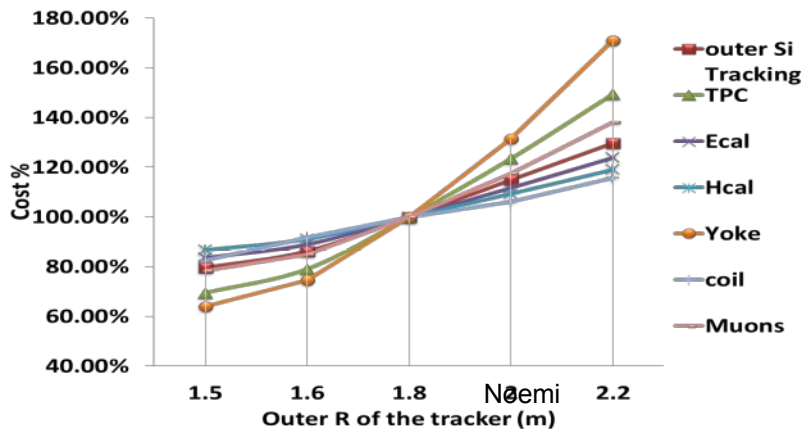
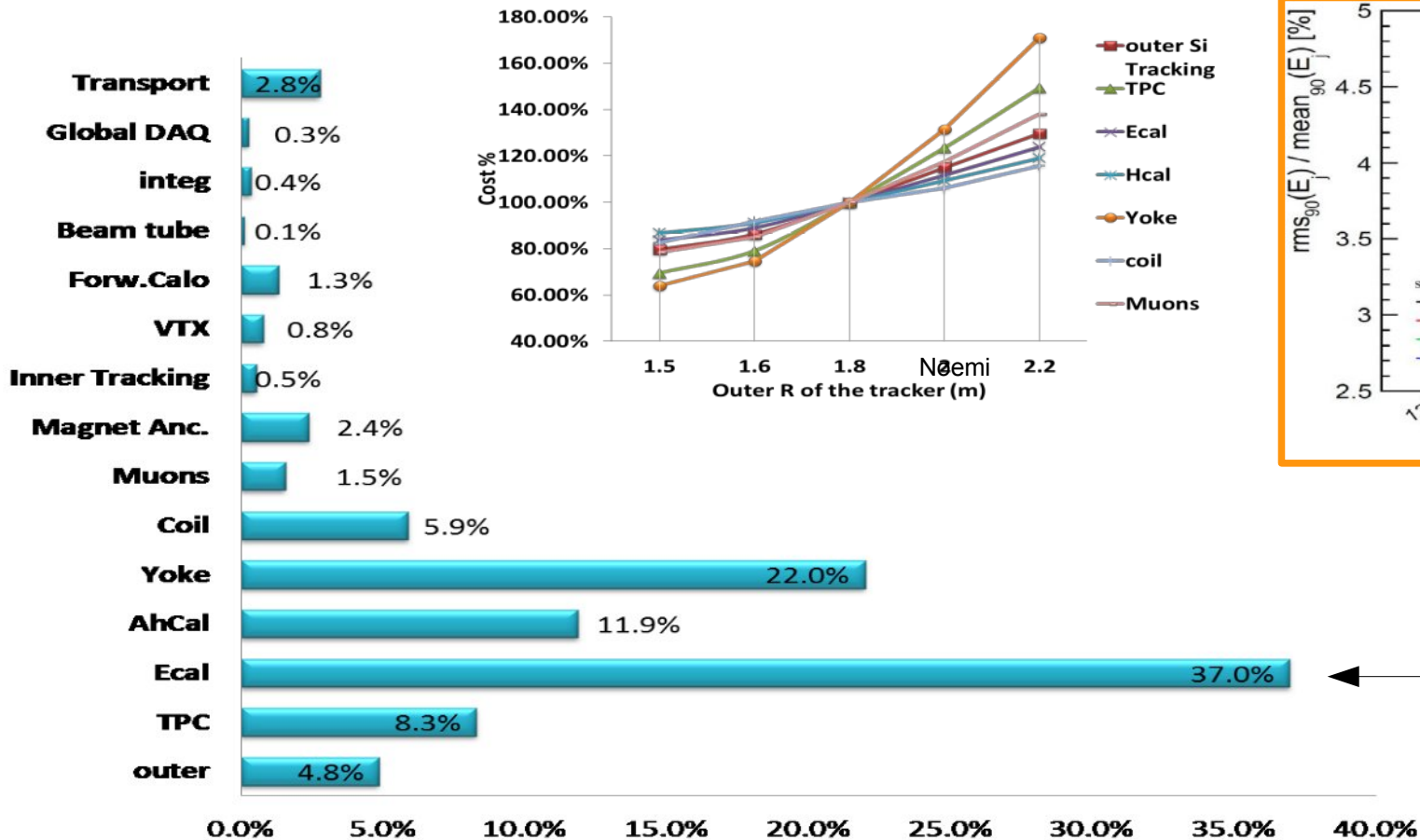
- including space and manpower estimations



Handling and positioning tool for integration & tests



Cost Structure of ILD



← Full Silicon option

FCAL Collaboration: LumiCal & BeamCAL

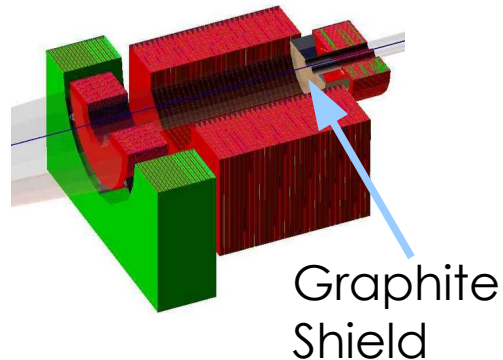
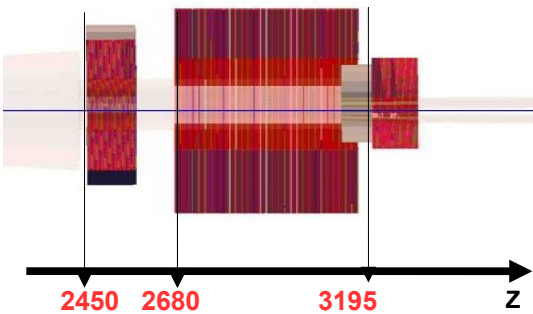
Needs to be adapted
for FCC (Mechanical precision)

LumiCal :

- Precise integrated luminosity measurements (Bhabha events)
- Extend calorimetric coverage to small polar angles. Important for physics analysis

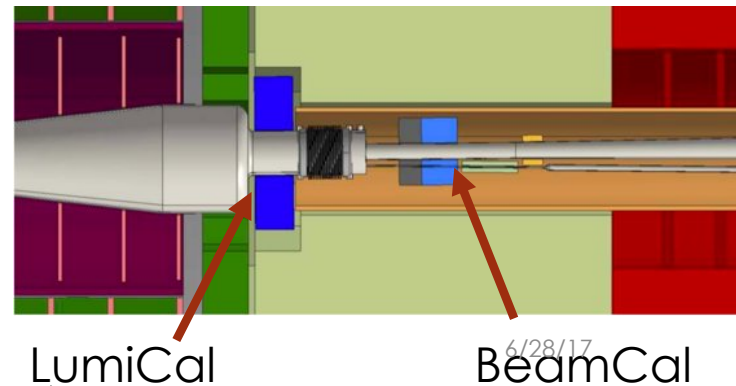
LHCal :

- Extend the hadronic calorimeter coverage
- 29 layers of 16mm thickness. Absorber : tungsten or iron



BeamCal :

- Measure instant Luminosity. Feedback for beamtuning
 - providing supplementary beam diagnostics information extracted from the pattern of incoherent-pair energy depositions
- tagging of high energy electrons to suppress backgrounds to potential BSM process
 - shielding of the accelerator components from the beam-induced background
- Sampling calorimeter based on tungsten plates
 - 30 layers for ILC, 40 layers for CLIC
- Due to large dose, rad hard sensors (GaAs, Diamond, Sapphire)

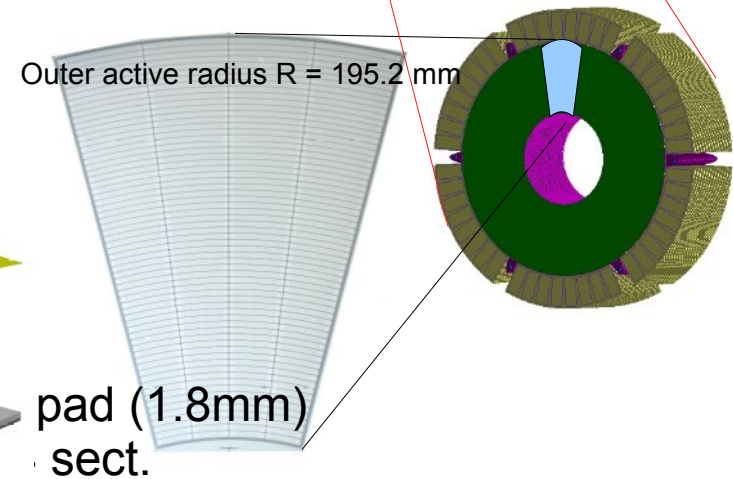
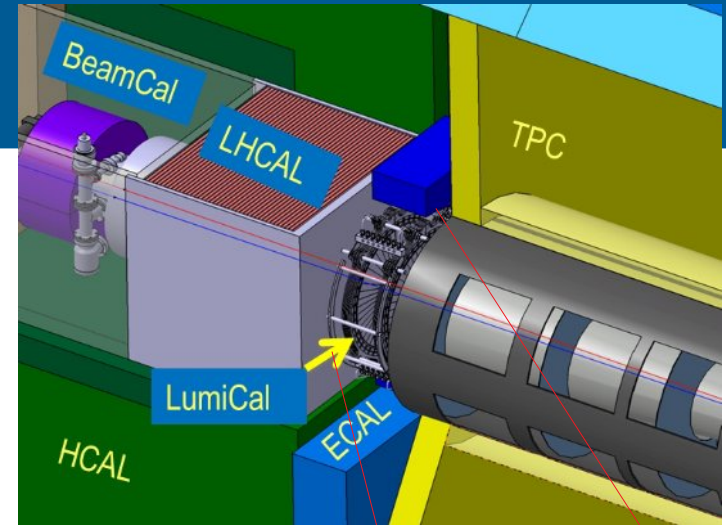
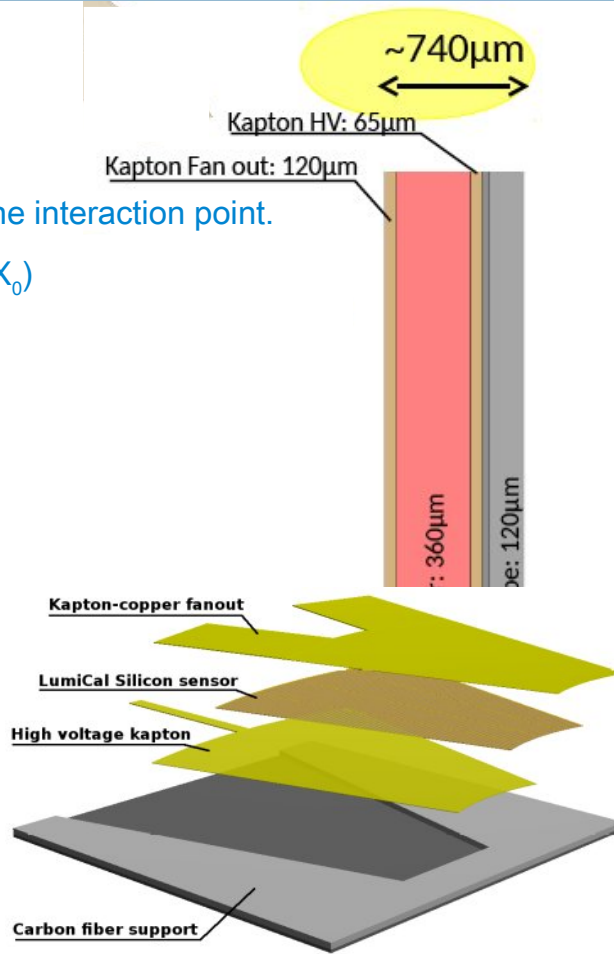
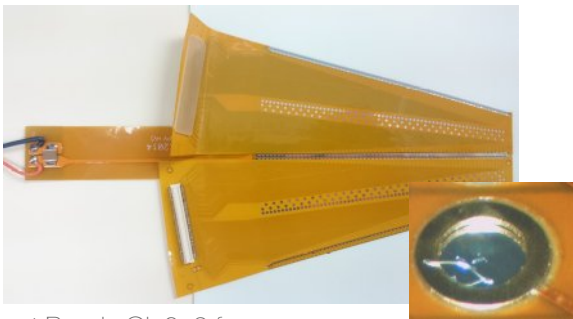


FCAL collaboration: LumiCal

Bhabha measurement

Sampling ECAL

- symmetrically on both sides at $\sim 2.5\text{m}$ from the interaction point.
- 30 layers of 3.5 mm thick tungsten plates ($1X_0$)
- 320 μm Si (p+ implants in n-type bulk)
 - DC coupling to readout
 - through Kapton foils glued on wafer
- Ultra-thin design: $R_M = 12\text{mm}$ expected
 - Bhabha + $\gamma\gamma$ background



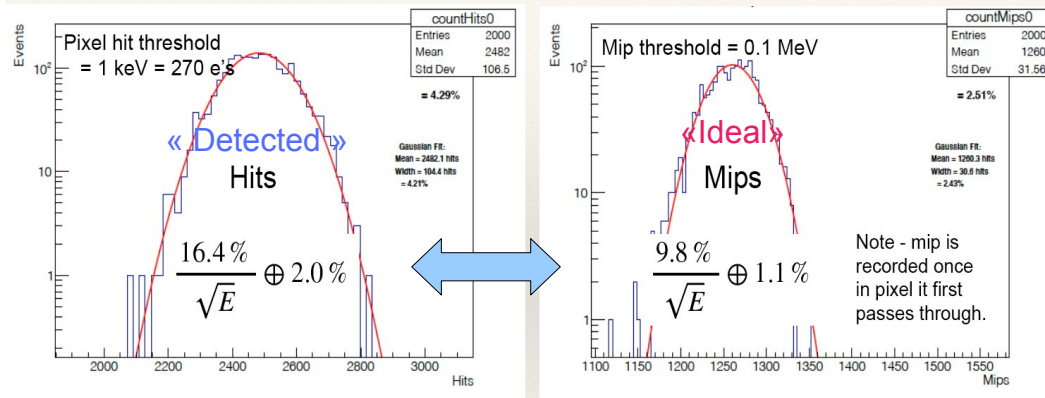
[NEW] Optimisation of a Digital Readout

Digital readout:

- Count “hits” = number of readout cells above a threshold
 - Best Threshold ? $\frac{1}{2} \sim \frac{1}{4}$ of a MIP = Minimum Ionising Particle, perp. to the sensor $\equiv \sim$ minimum signal for a particle
- Known : Very good behaviour with 100–25 mm² cells at low E (few GeV)
- ~New: excellent behaviour with very small cells (using MAPS):

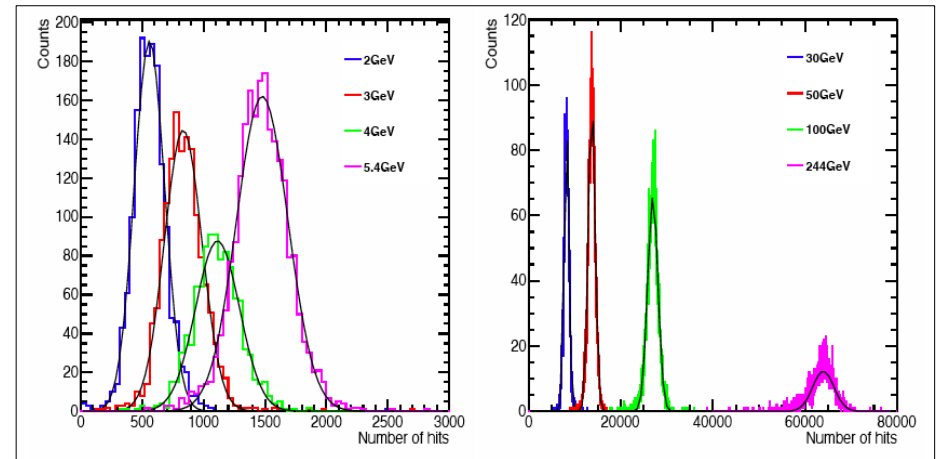
SiD DECAL (MAPS 25×100 μm²) simulation

Pixel counts (hits & mips) - 20 GeV γ (B=5T)



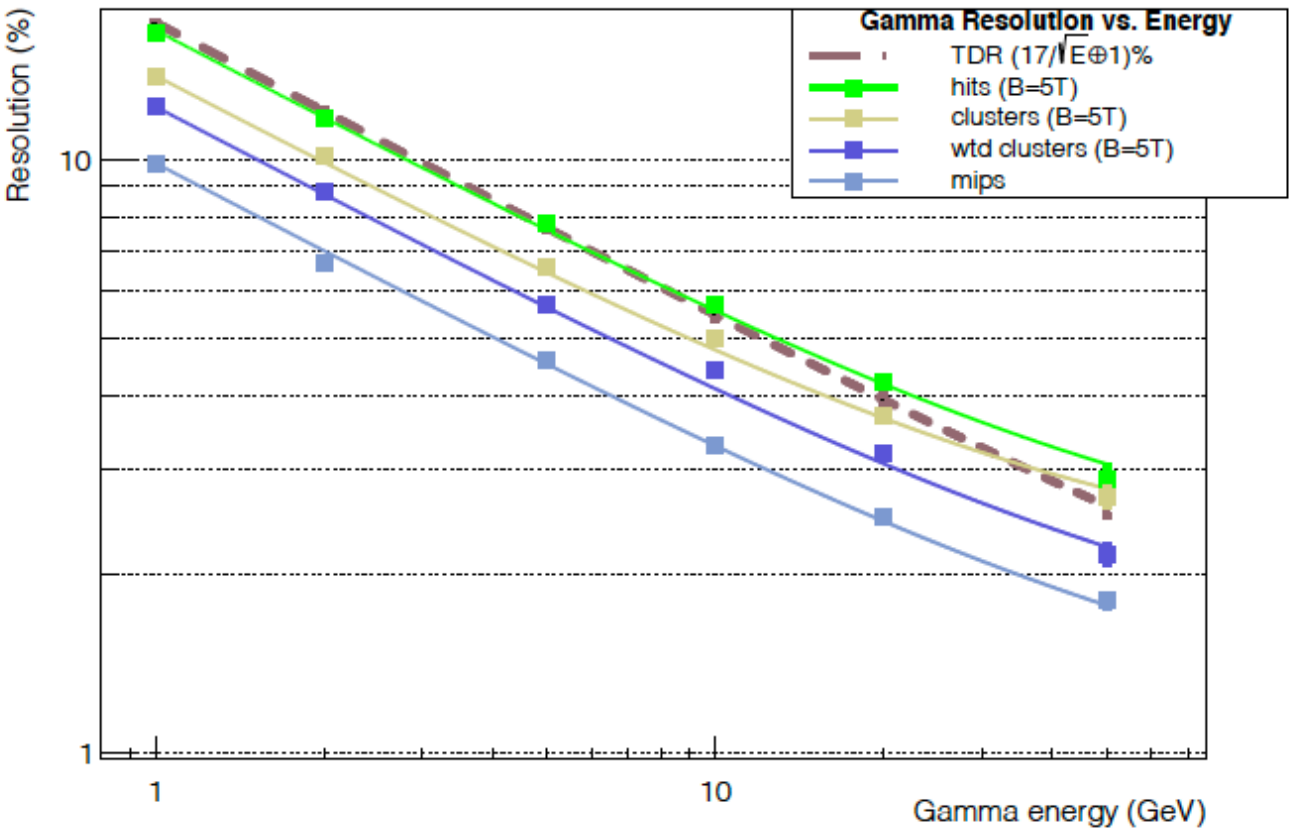
Real prototype :

FOCAL prototype (\leftrightarrow ALICE) [see APICAL2 as well]
MIMOSA23 MAPS, 640×640 arrays of (30 μm)² pixels



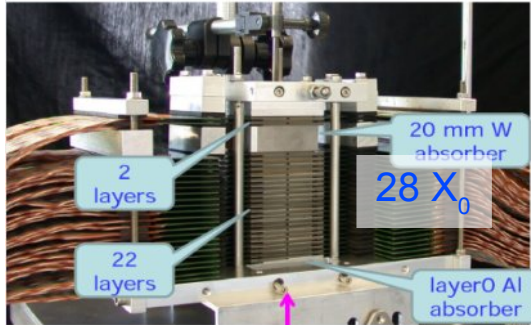
SiD Simulations

Gamma Resolution vs. Energy (B=5T)



MAPS & DECAL

FOCAL DECAL prototype

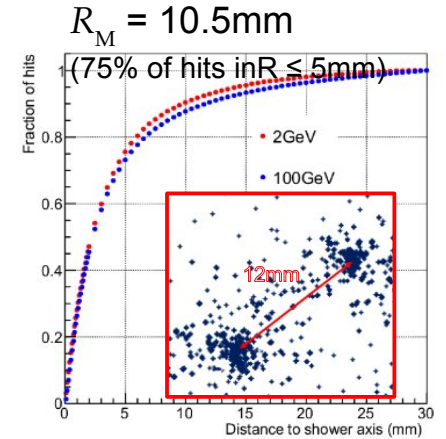


beam direction

FOCAL = 2 layers of MAPS

but How to build a full detector ?

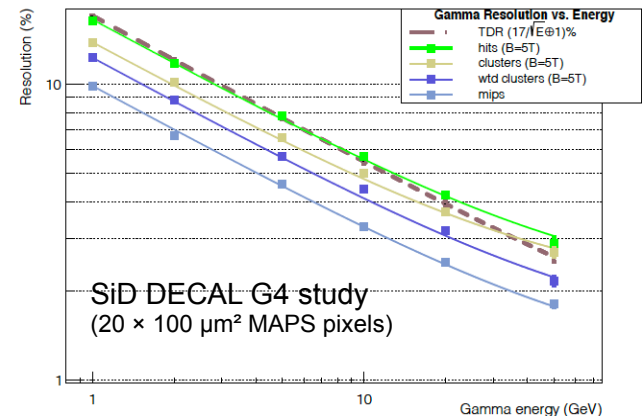
- Services: Power + Cooling ?
- Gains by going fully digital ?
- For what physical gain ?
- Improved separation ✓
- Improved resolution ?



4 MIMOSA-26 / Layer CMOS sensors (IPHC)

- $6 \times 6\text{ cm}^2$
- $30 \times 30\ \mu\text{m}^2$ pixels
- 39 M pixels = full readout

Gamma Resolution vs. Energy (B=5T)



Detector concepts for PF

Calorimeters ILD, SiD, CLIC-Dp, CEPC-Baseline

- Many similarities
 - Magnet outside
 - Compact design, min dead space,
 - small gap ECAL–HCAL
- Small differences:
 - Inner diameter
 - Granularity: cell-size, number of layers
 - Sensor’s technology (next slides)

Differences in level of details in implementations

- Simulation, costs
- Integration of services:
 - power, cooling, readout

Geometry:

- ECAL hangs to
HCAL hangs to
Coils
- for ILD: 2 geometry explored for the HCAL
 - TESLA : barrel made of staves (sectors)
 - electronics between barrel and endcaps
 - «a la Videau» / H1: barrel made of rigid wheels
 - services outside (cooling , power, readout interface)

Base elements of HG PFlow calorimeters:

- “Standard” stitchable elements with embedded FE (ASIC’s) driven at a single end.
 - gases, power, readout, cooling

High Granularity Calorimetry Ins & Outs

Pluses

- Small constant term
- Particle Tracking (with mip sensitivity)
 - “online” Calibration
- new Particle ID tools
 - Shower shapes, Fractal Dim, Tracks in calo, ...
- SW compensations
 - Global (density) \leftrightarrow EM fraction
 - Start of shower \rightarrow leakage corr.
 - in-calo tracking
 - "Not yet fully exploited"
 - new estim.
 - loss leakage,

Minuses

- Complex Calibration (100M+ channels)
- System: Power & Cooling, Integration

Scaling laws

- cell size = d , N_{layers}
- Sensor & Electronics cost, power $\sim 1/d^2 \times N_{\text{layers}}$
- Raw timing precision $\sim d/2 / (2/3c)$ (25 ps for 1 cm)
- FE elect. power $\sim \dots$

Conclusions

Linear Colliders

- Mature technologies & designs
- Marginal (?) gains from optimisation ?
 - Gain from timing → PID, PFA, Event Cleaning

Circular Colliders Calorimeters

- Large effort still needed
 - (e.g. CEPC baseline ≠ FCC-ee)
 - ILD collaboration joined the effort (late 2022)
- Many technological options explored
 - Will require in-depth performance comparisons

Common tools:

- Simulation (dd4sim, key4hep)
- Reconstruction SW (key4hep)
 - ↔ beam test data
- ASICs? DRD6 new chips
- Services & Scaling
 - TDAQ
 - Cooling
 - Global power/thermal simulations
 - Mechanical simulations (▷ coil)

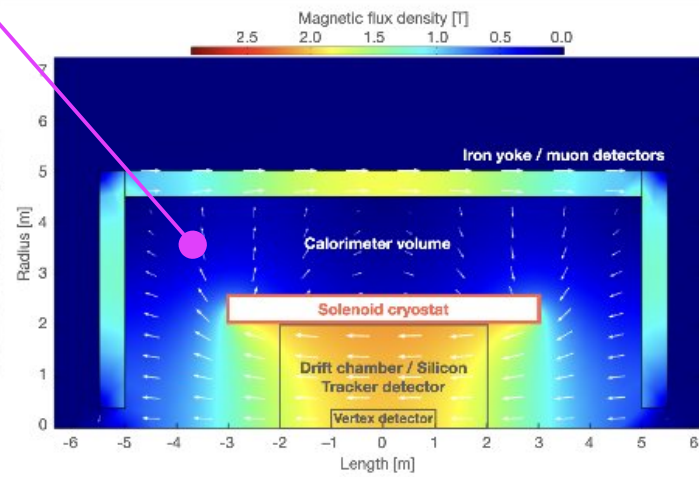
	ILD (IDR_L/IDR_S)	SiD	CLICdet	CLD	IDEA	CEPC baseline
Vertex technology	Silicon	Silicon	Silicon	Silicon	Silicon	Silicon
Vertex inner radius	1.6 cm	1.4 cm	3.1 cm	1.75 cm	1.7 cm	1.6 cm
Tracker technology	TPC + Silicon	Silicon	Silicon	Silicon	Drift chamber + Si	TPC + Silicon
Tracker outer radius	1.77 m / 1.43 m	1.22 m	1.5 m	2.1 m	2.0 m	1.8 m
Calorimeter	PFA	PFA	PFA	PFA	Dual readout	PFA
(ECAL) inner radius	1.8 m / 1.46 m	1.27 m	1.5 m	2.15 m	2.5 m	1.8 m
ECAL technology	Silicon	Silicon	Silicon	Silicon	-	Silicon
ECAL absorber	W	W	W	W	-	W
ECAL thickness	24 X_0 (30 layers)	26 X_0 (30 layers)	22 X_0 (40 layers)	22 X_0 (40 layers)	-	24 X_0 (30 layers)
HCAL technology	Scintillator	Scintillator	Scintillator	Scintillator	-	RPC
HCAL absorber	Fe	Fe	Fe	Fe	-	Fe
HCAL thickness	5.9 λ_1 (48 layers)	4.5 λ_1	7.5 λ_1 (60 layers)	5.5 λ_1 (44 layers)	8 λ_1 (2 m)	4.9 λ_1 (40 layers)
(HCAL) outer radius	3.34 m / 3.0 m	2.5 m	3.25 m	3.57 m	≤ 4.5 m	3.3 m
Solenoid field	3.5 T / 4 T	5 T	4 T	2 T	2 T	3 T
Solenoid length	7.9 m	6.1 m	8.3 m	7.4 m	6.0 m	8.0 m
Sol. inner radius	3.42 m / 3.08 m	2.6 m	3.5 m	3.7 m	2.1 m	3.4 m

Discussed :
Here Brass

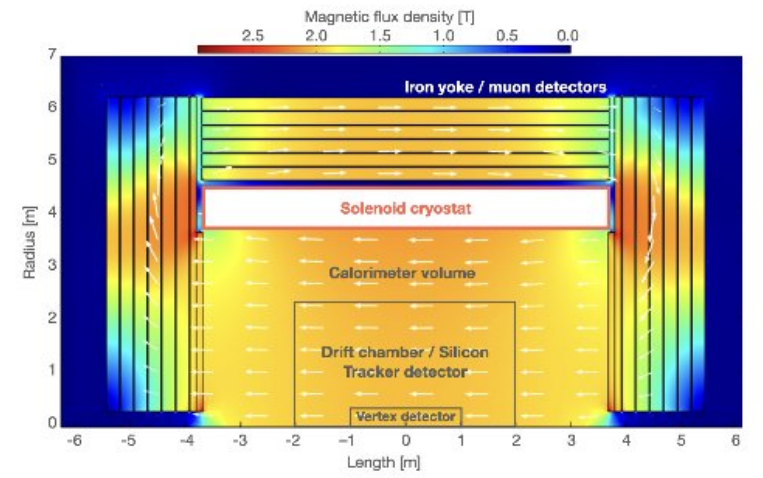
Uniform with
S. Steel

2T thin solenoid w/ calorimetry outside
R&D to develop high-strength Al-stabilized NbTi
superconducting cable and light vacuum vessel*
 $< 0.5-1X_0$ and $\approx 0.1 \lambda$

HCAL can use iron absorber to act
as field return yoke



CMS-like solenoid w/ calorimetry inside
B up to 4T above Z-peak energy



- Studies in calorimetry outside concepts with parameterized X/X_0 and λ for performance and to assess if $B > 2T$ possible
- Realistic field maps modeling in simulation to study systematics on p_T measurement