

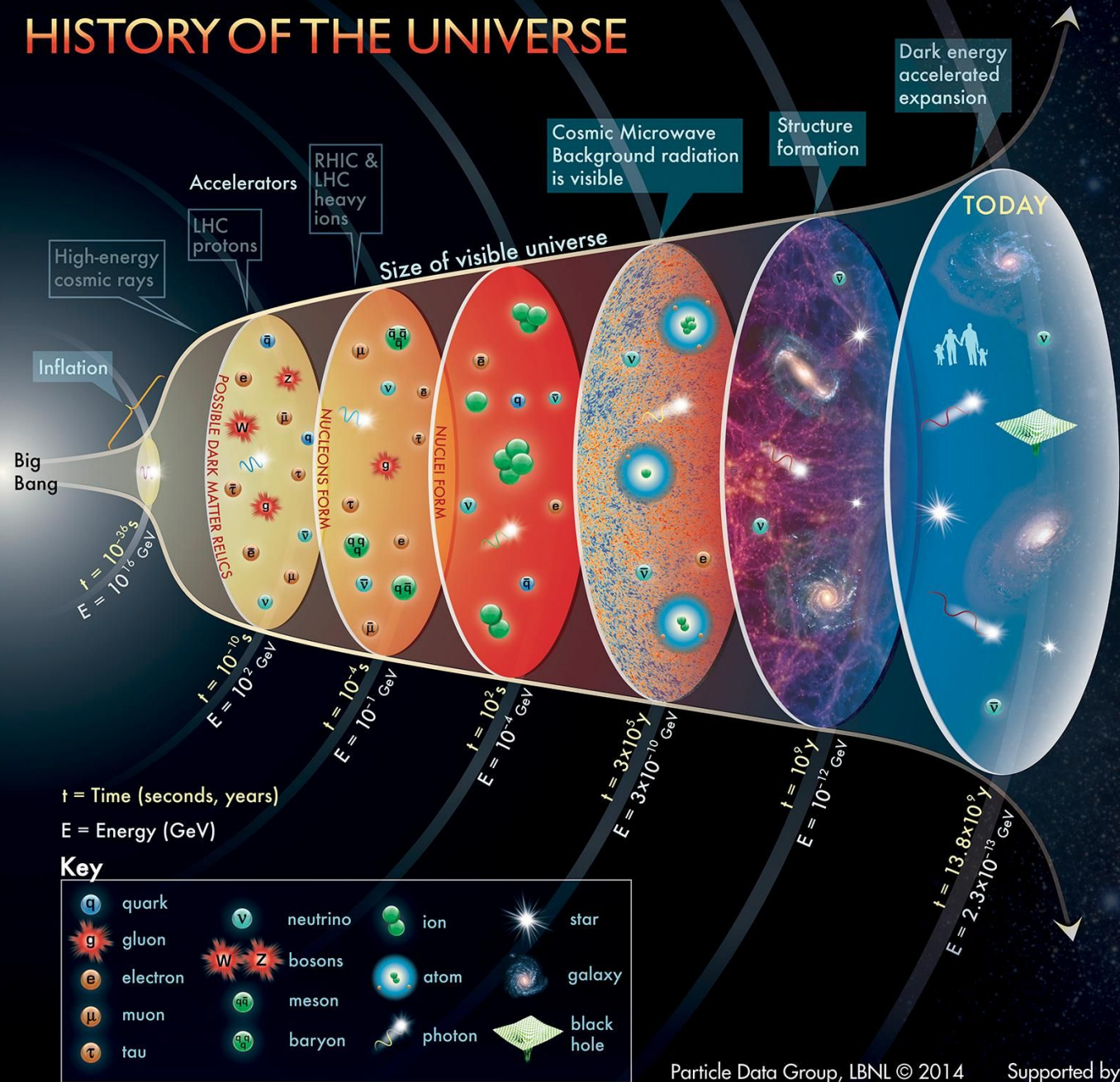


**Vilniaus
universitetas**



**Accelerators, detectors,
and computing
will pave the way for new discoveries**

HISTORY OF THE UNIVERSE



Vilniaus universitetas

- understanding fundamental laws of nature at the smallest scales
- reproduce conditions similar to early after the big bang in the laboratory
- higher energy \leftrightarrow closer in time to big bang



Mt. Blanc

Genève

Lac Léman

LHCb

ATLAS

CERN Meyrin

CERN Prévessin

SPS 7 km

4.26 km

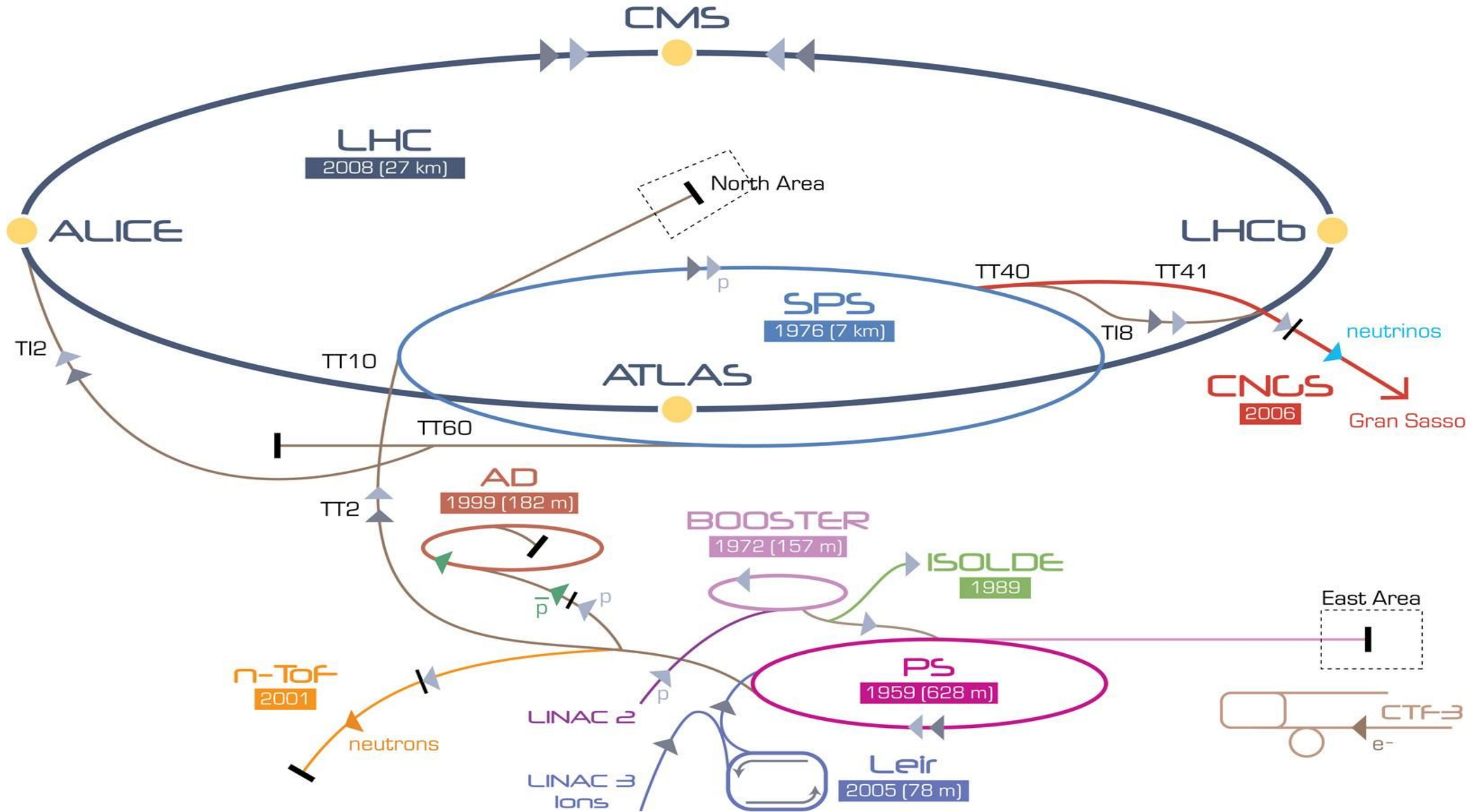
SUISSE
FRANCE

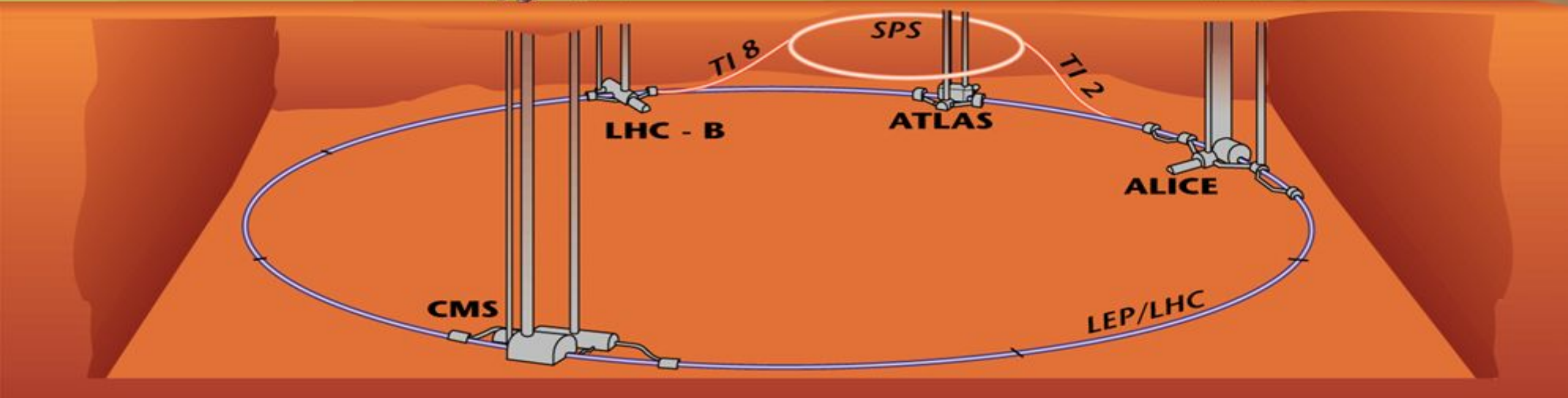
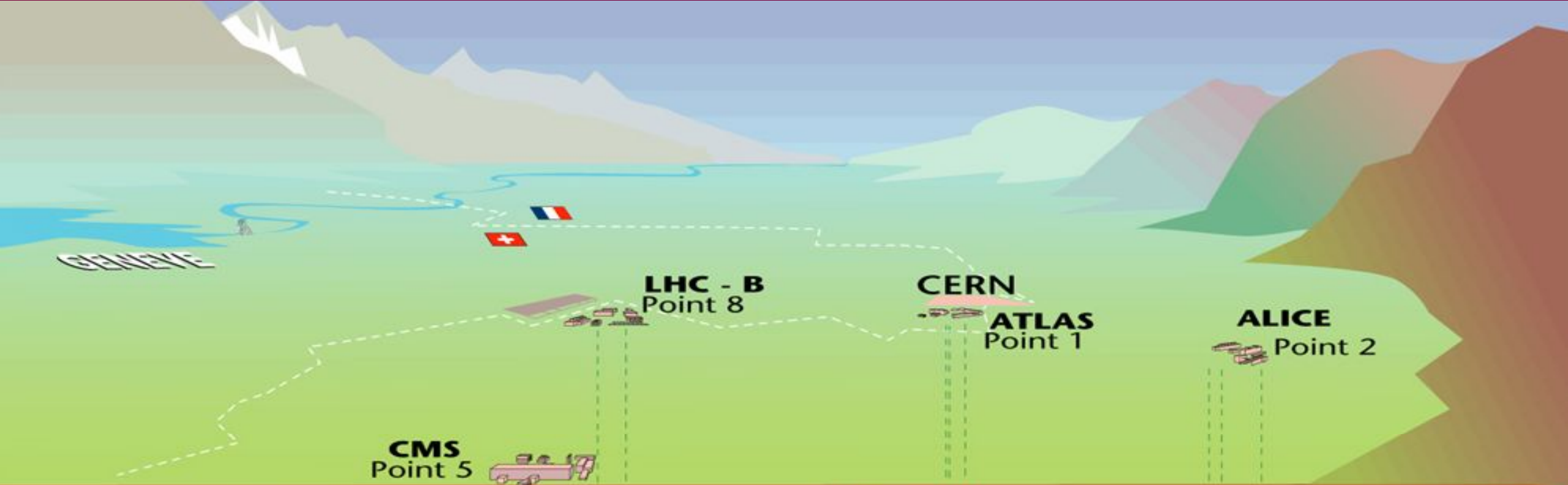
CMS

ALICE

LHC 27 km

Image credit: Maximilien Brice (CERN) CC BY-SA 3.0





CMS DETECTOR

Total weight : 14,000 tonnes
Overall diameter : 15.0 m
Overall length : 28.7 m
Magnetic field : 3.8 T

STEEL RETURN YOKE
12,500 tonnes

SILICON TRACKERS
Pixel (100x150 μm) $\sim 16\text{m}^2 \sim 66\text{M}$ channels
Microstrips (80x180 μm) $\sim 200\text{m}^2 \sim 9.6\text{M}$ channels

SUPERCONDUCTING SOLENOID
Niobium titanium coil carrying $\sim 18,000\text{A}$

MUON CHAMBERS
Barrel: 250 Drift Tube, 480 Resistive Plate Chambers
Endcaps: 468 Cathode Strip, 432 Resistive Plate Chambers

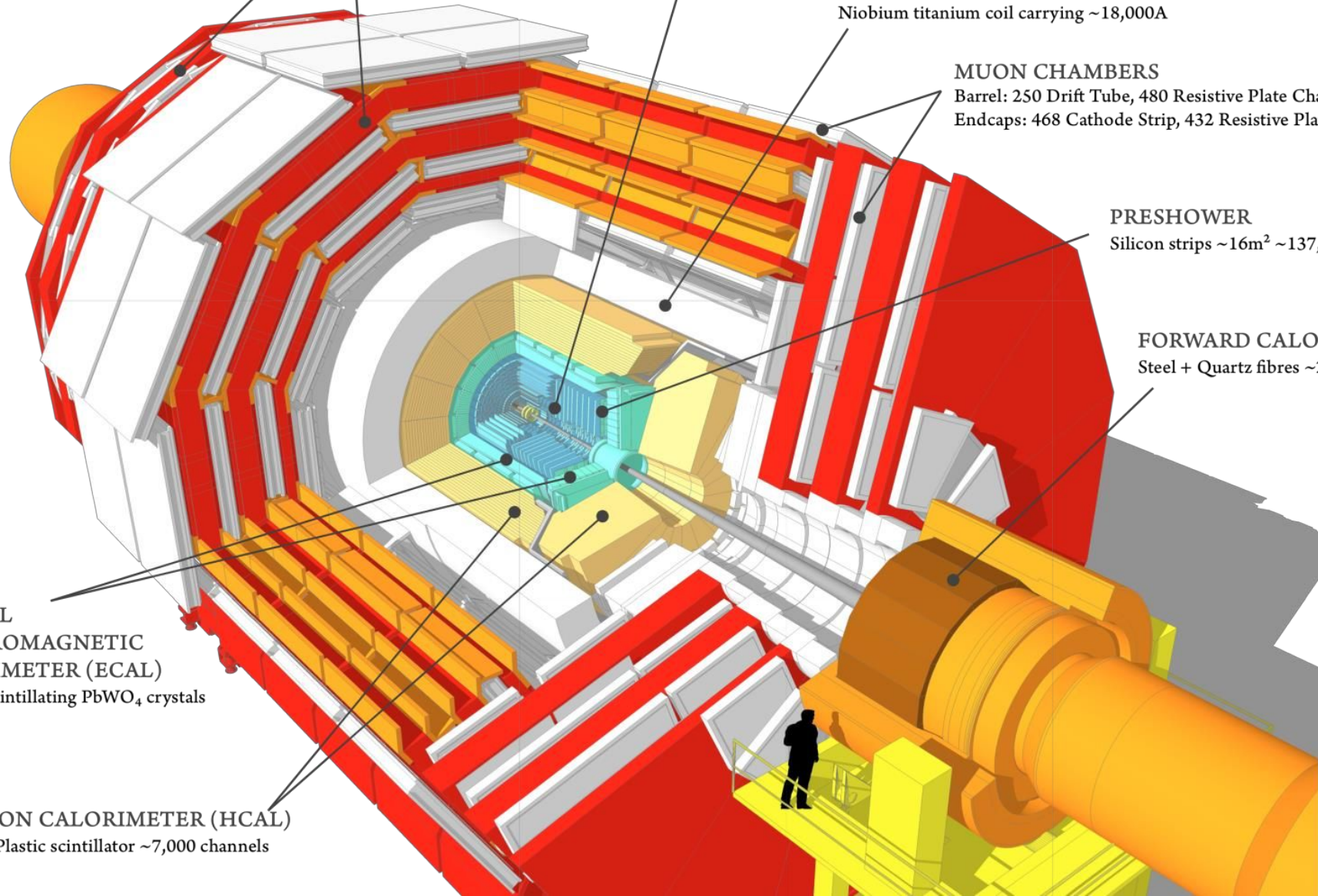
PRESHOWER
Silicon strips $\sim 16\text{m}^2 \sim 137,000$ channels

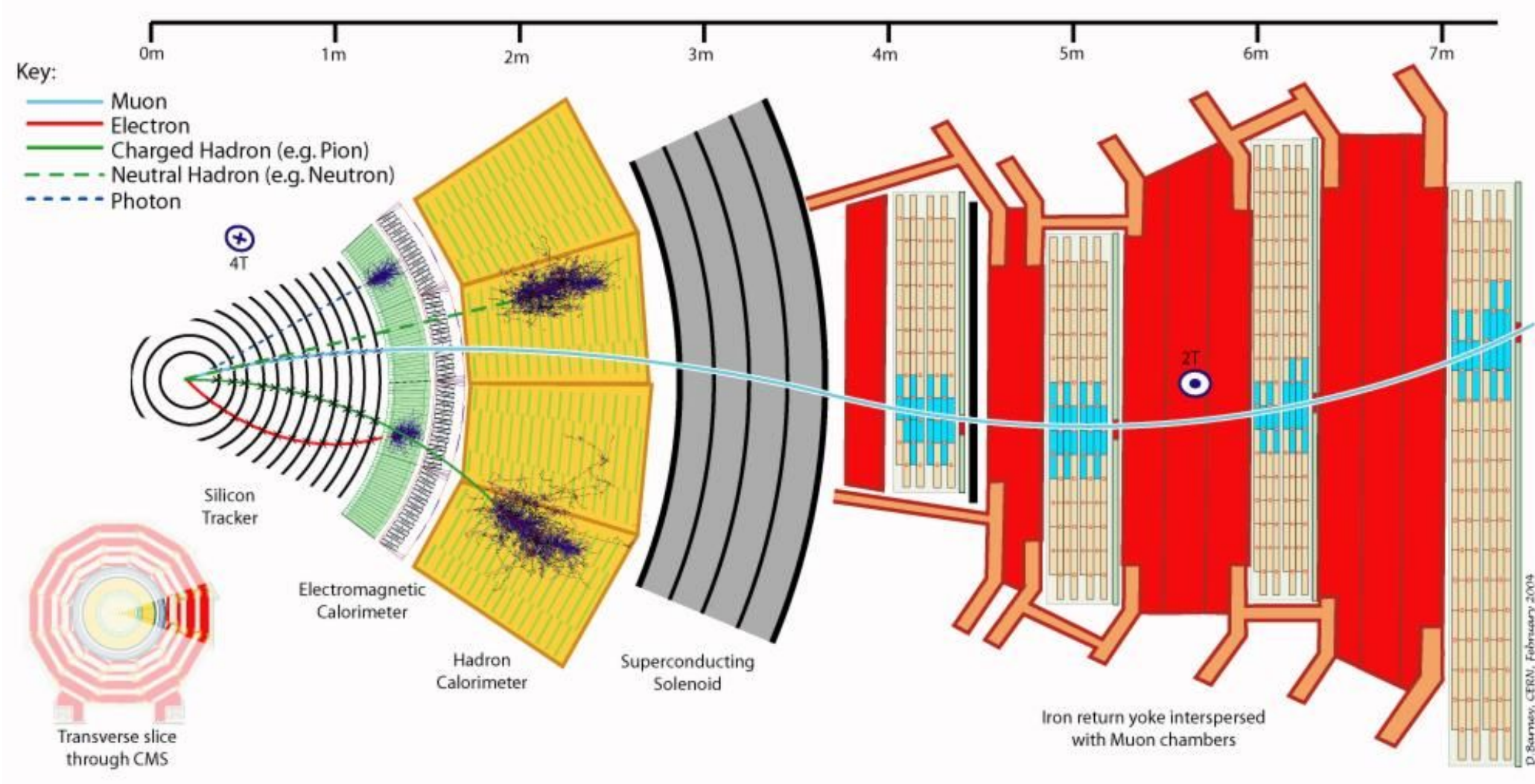
FORWARD CALORIMETER
Steel + Quartz fibres $\sim 2,000$ Channels

CRYSTAL
ELECTROMAGNETIC
CALORIMETER (ECAL)
 $\sim 76,000$ scintillating PbWO_4 crystals

HADRON CALORIMETER (HCAL)
Brass + Plastic scintillator $\sim 7,000$ channels

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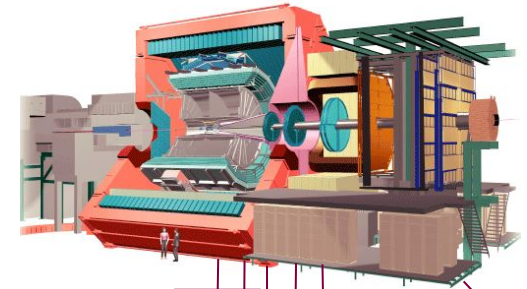
Data Readout System for a Large HEP Experiment

- ~600M collisions / sec, ~1:1M is interesting
- LHC detector scale
 - The number of channels:
 - for LHC experiments $O(10^7)$ channels
 - a (digitized) channel can be between 1 and 14 bits
 - The rate:
 - for LHC experiments everything happens at 40.08 MHz, the LHC bunch crossing frequency
 - This corresponds to 24.9500998 ns or ~25 ns
- HEP experiments consist of many different sub-detectors:
 - tracking, calorimetry, particle-ID, muon-detectors

Data Readout System for a Large HEP Experiment

- A selection mechanism (“**trigger**”)
- Electronic readout of the sensors of the detectors (“**front-end electronics**”)
- A system to keep all those things in sync (“**clock**”)
- A system to collect the selected data (“**DAQ**”)
- A Control System to configure, control and monitor the entire DAQ (“**DCS**”)
- A system to monitor data quality and integrity (“**DQM**”)

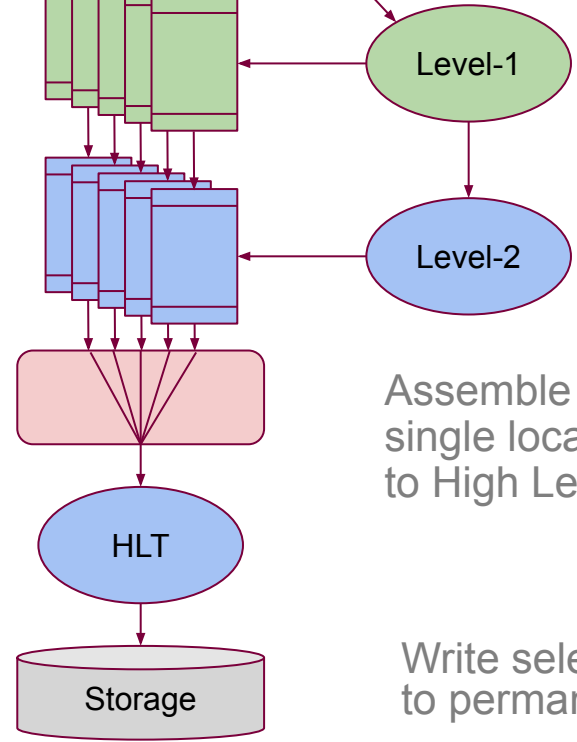
Data Acquisition “Standard Model”



Level-1 pipelines (buffers)

Temporary buffering of event fragments in readout buffers

- custom hardware
- PC
- network switch



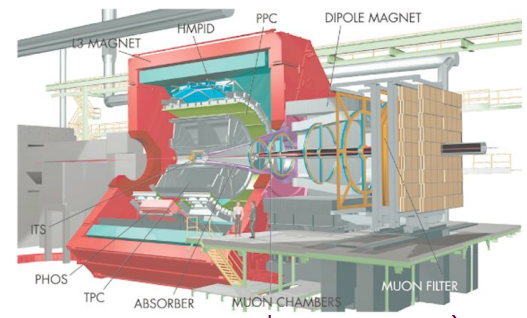
Level-1 trigger




Higher level trigger with partial event data

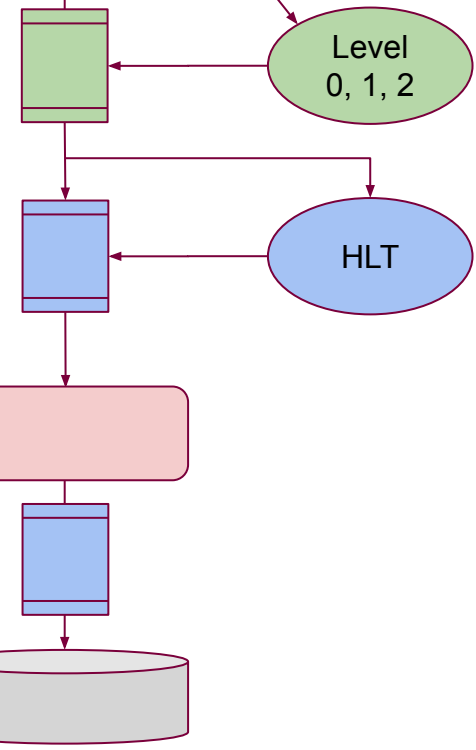
Assemble events in single location and provide to High Level Trigger (HLT)

Write selected events to permanent storage

Data Acquisition at ALICE



-  custom hardware
-  PC
-  network switch



Front-End pipelines

500 Hz

Higher level trigger

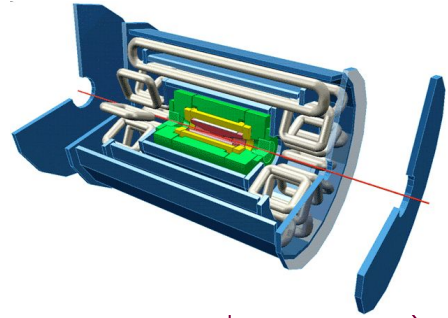
100 Hz


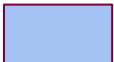

Event Builder

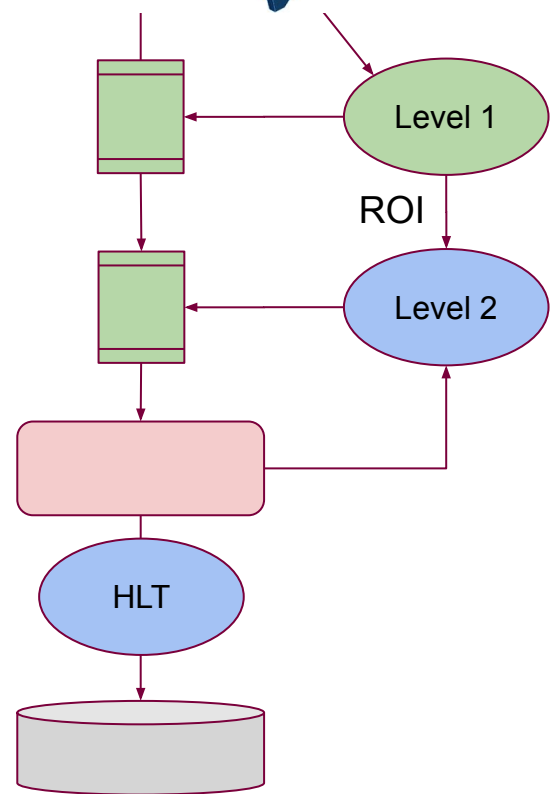
100 Hz

Event buffer

Data Acquisition at ATLAS

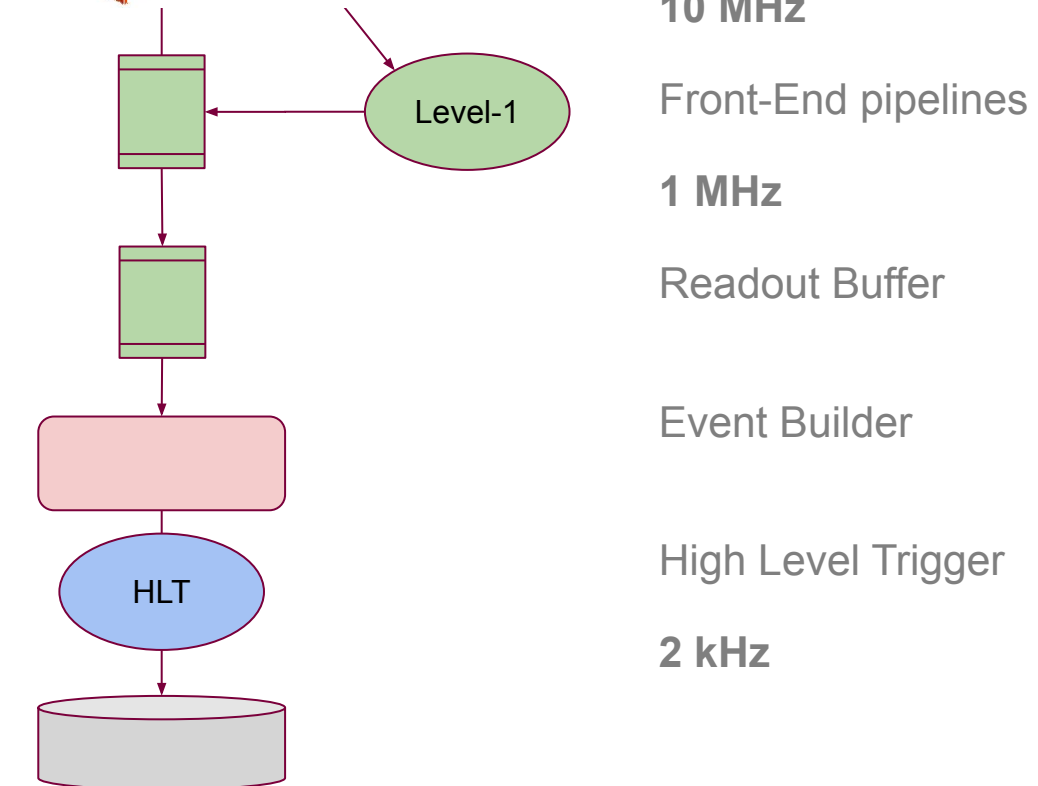
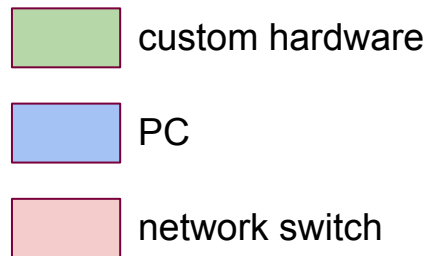
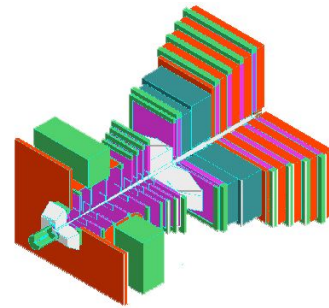


-  custom hardware
-  PC
-  network switch
- ROI Regions Of Interest

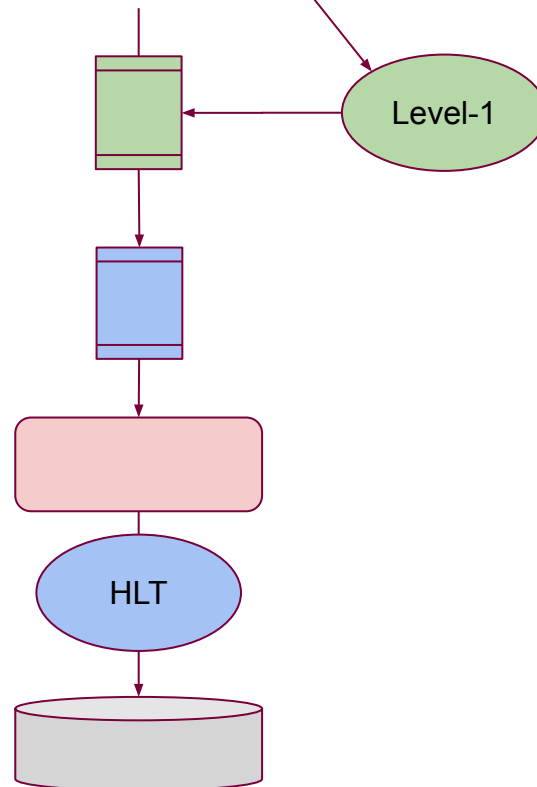
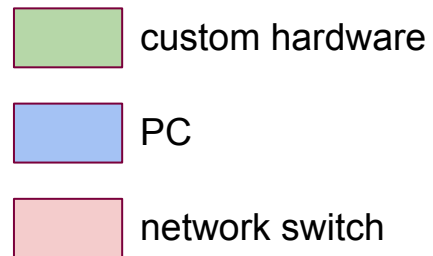
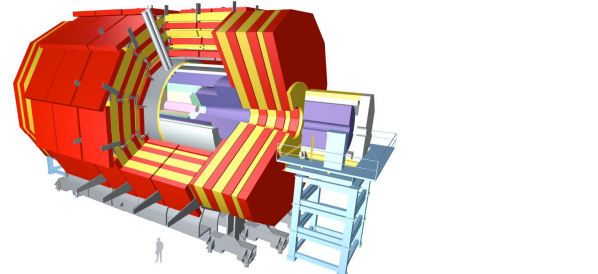


- 40 MHz
- Front-End pipelines
- 100 kHz
- Readout buffer
- 3kHz (L2)
- Event Builder
- High Level Trigger
- 200 Hz

Data Acquisition at LHCb



Data Acquisition at CMS



40 MHz

Front-End pipelines

100 kHz

Readout Buffer

Event Builder

High Level Trigger

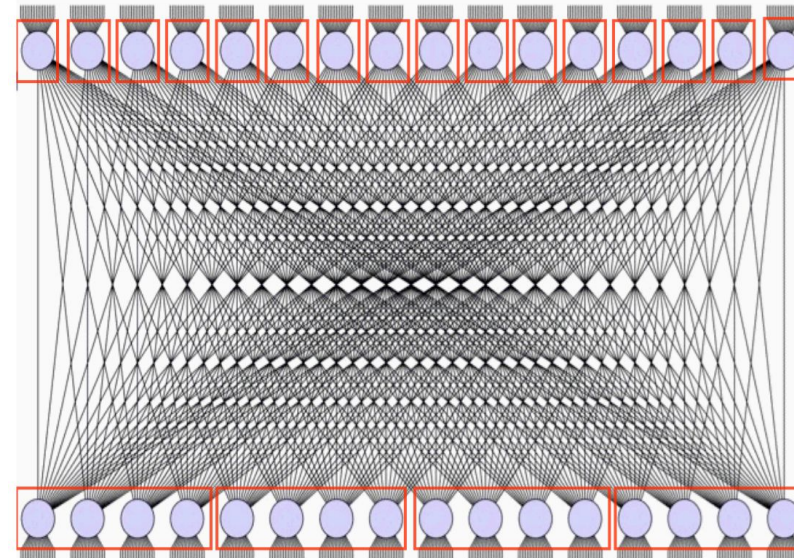
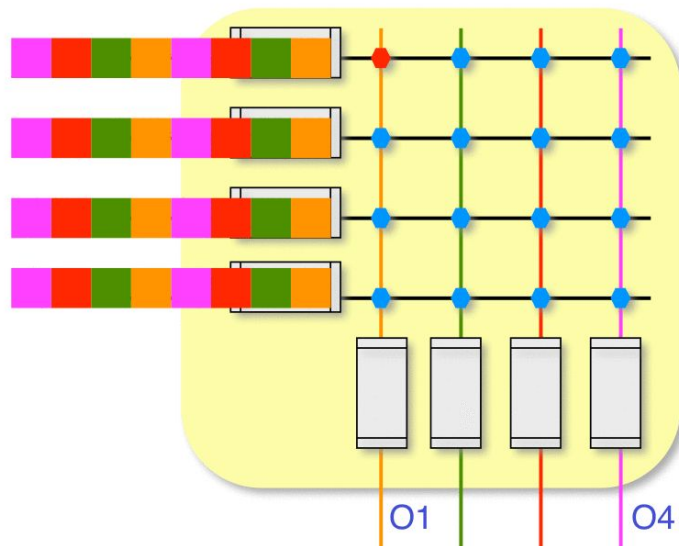
100 Hz

Level-0,1,2,* Trigger

- No (affordable) DAQ system could read out $O(10^7)$ channels at 40 MHz → 400 TBit/s to read out – even assuming binary channels!
- Most of these millions of events per second are totally uninteresting: one Higgs event every 0.02 seconds / Low level triggers must somehow select the more interesting events
- Design principles
 - Millions of channels → try to work as much as possible with “local” information / Keeps number of interconnections low
 - Must be fast: look for “simple” signatures / Keep the good ones, kill the bad ones / Robust, can be implemented in hardware (fast) / to keep buffer sizes under control / every 25 nanoseconds (ns) a new event: have to decide within a few microseconds (μ s): trigger-latency

Event Builder

- After L1, event data are digitized, pre-processed and tagged with a unique, monotonically increasing number
- The event data are distributed over many read-out boards (“sources”)
- For the next stage of selection, or even simply to write it to tape we have to get the pieces of the event together: **event building**
- L1 rate: 100kHz, event size: 1 Mbyte, No. readout systems: 512

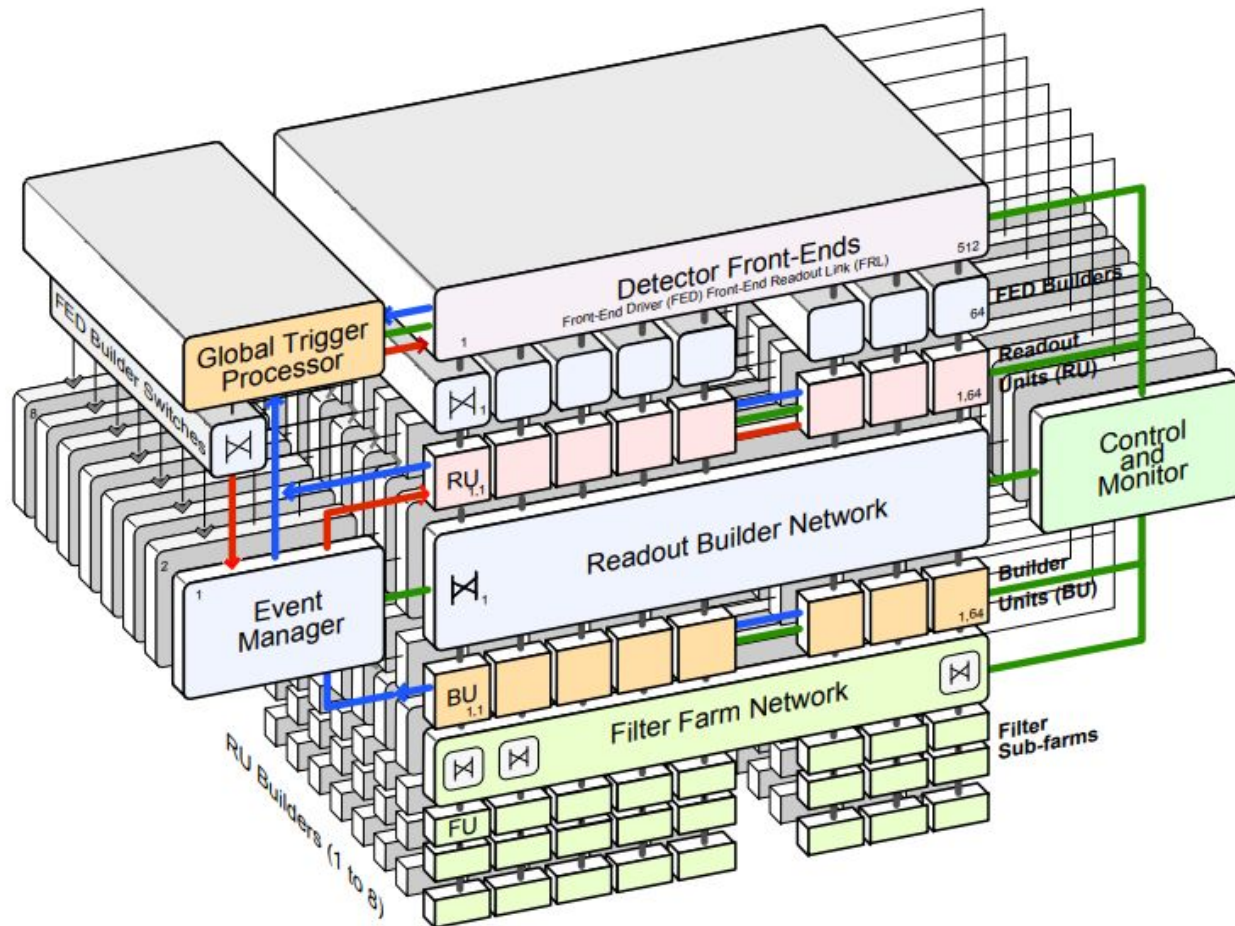


High-Level Trigger

- assembly of full collision data on second layer of PCs
- must reduce the rate of selected collisions from 100 kHz to ~ 1 kHz
- ~ 15'000 cores → 150 ms decision time on average
- software of 3.8M C++ and 1.2M python lines of source code
- partial reconstruction of collision data
 - finding clusters of high energy deposit
 - 3D track fitting (Kalman filtering) from 3D and 2D points
 - matching of tracks to clusters



CMS DAQ



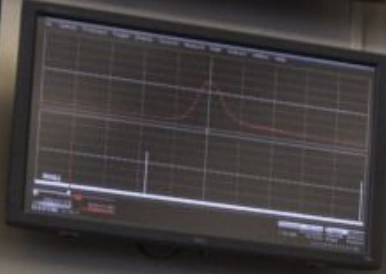
- Collision rate 40 MHz
- Level-1 Maximum trigger rate 100 kHz
- Average event size ≈ 1 Mbyte
- Event Flow Control $\approx 10^6$ Mssg/s
- No. of In-Out units: 512
- Readout bandwidth ≈ 1 Terabit/s
- Event filter computing power $\approx 10^6$ SI95
- Data production \approx Tbyte/day
- No. of PC motherboards \approx Thousands



CONTACT BRM ONCALL
18-5583
CONTACT LUMI ONCALL
18-5637

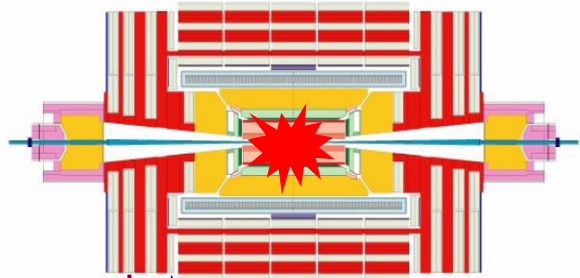
Ouvrir : presser "RC" puis taper le code
Fermer : presser "RC" puis "C"

Compteur de la centrale

A monitor displaying a data table with multiple columns and rows, likely containing operational parameters or status information.A monitor displaying a data table with multiple columns and rows, likely containing operational parameters or status information.A monitor displaying a data table with multiple columns and rows, likely containing operational parameters or status information.

A monitor displaying text-based information, possibly a log or a list of parameters, with the title "LHC Machine Coordination Planning".

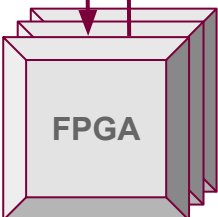
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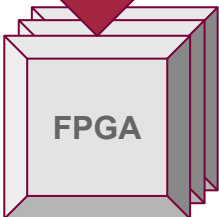
trigger data
40 MHz

readout requests

2 Mbyte x 100 kHz
custom protocols



Level 1
trigger
electronics

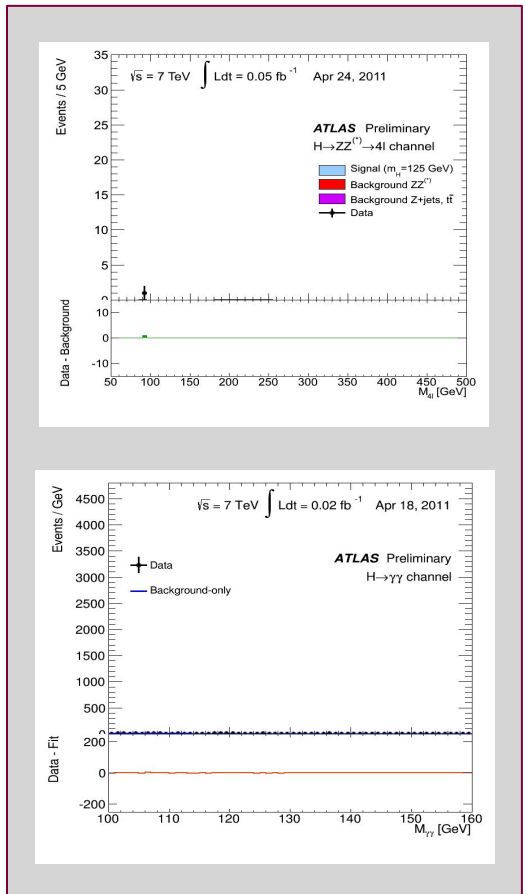


576 x 10 Gbit/s
Ethernet

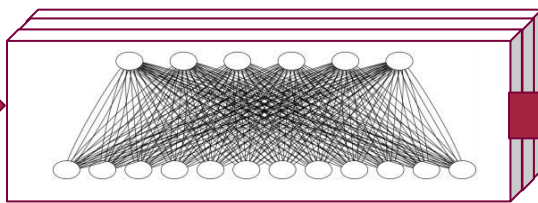


40 GBit/s
Ethernet

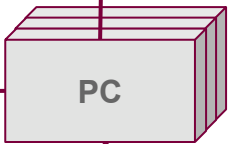
1st stage data
aggregation



Infiniband FDR
6 spine / 12 leaf
folded Clos



Vilnius
universitetas

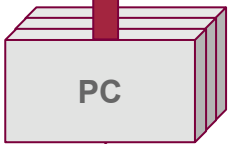


offline
reconstruction,
data analysis



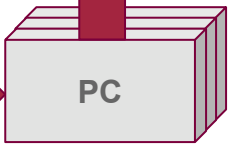
Storage

2 Mbyte x 1 kHz



High level trigger
(software)
reduction \sim 1:100

Fully assembled
collision data



2nd stage data
aggregation

Data Centre

Most of the CERN IT equipment is hosted in the Meyrin Data Centre. However, a second network hub has been inaugurated in 2017 and is located in Prévessin.

About 470 000 processor cores and 11 000 servers run 24/7

90% of resources run a private OpenStack cloud which hosts around 14 000 virtual machines

380 PB of data on tapes, in 2020 has served 2.5 exabyte of physics data, increase of 25 PB/year



Worldwide LHC Computing Grid

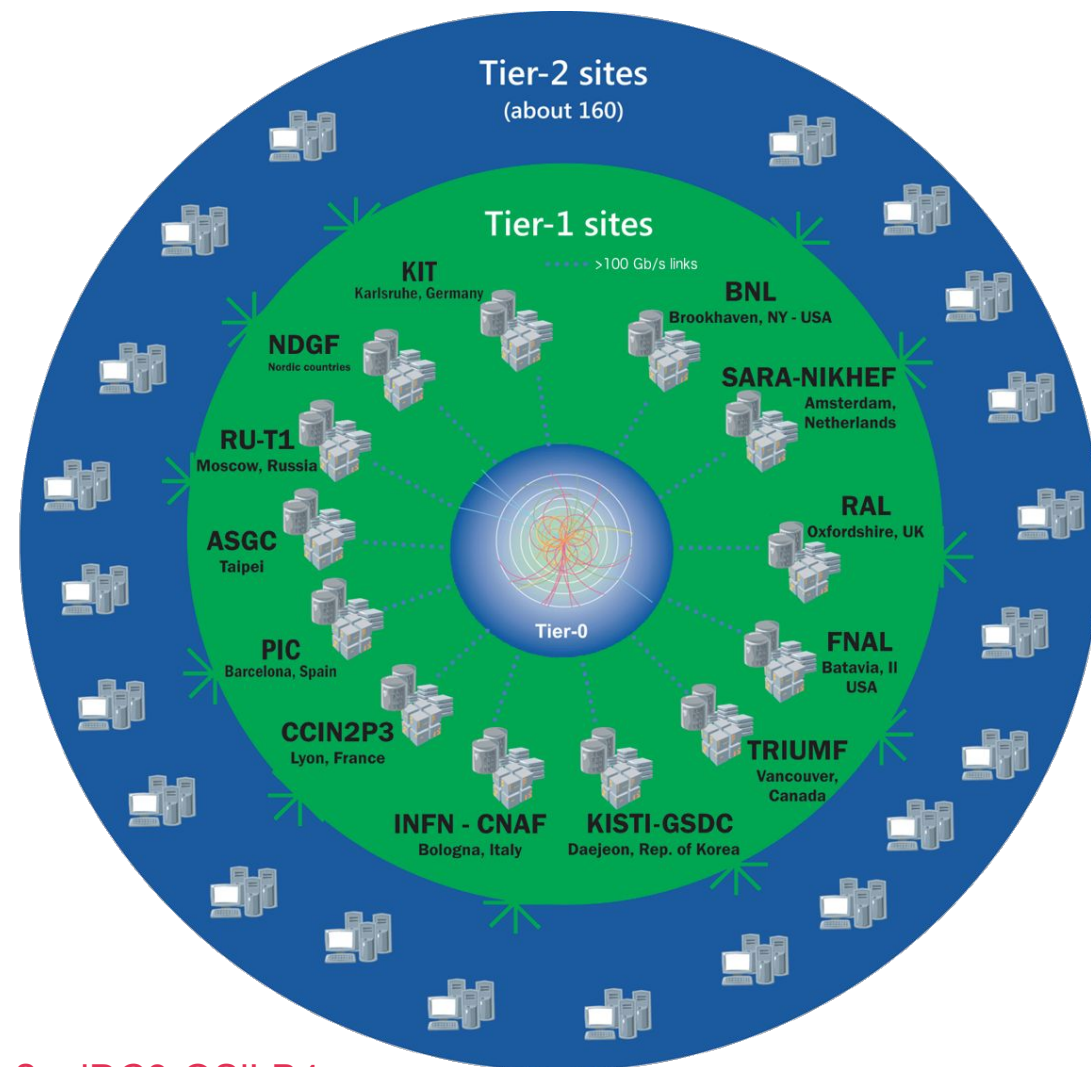
WLCG project is a global collaboration of around 170 computing centres in more than 40 countries

Tier-0: CERN Data Centre, around 20% of the total compute capacity, safe-keeping of the raw data (first copy), first pass reconstruction, distribution of raw data

Tier 1: 13 large computer centres / safe-keeping of a proportional share of raw and reconstructed data, large-scale reprocessing / distribution of data to Tier 2s

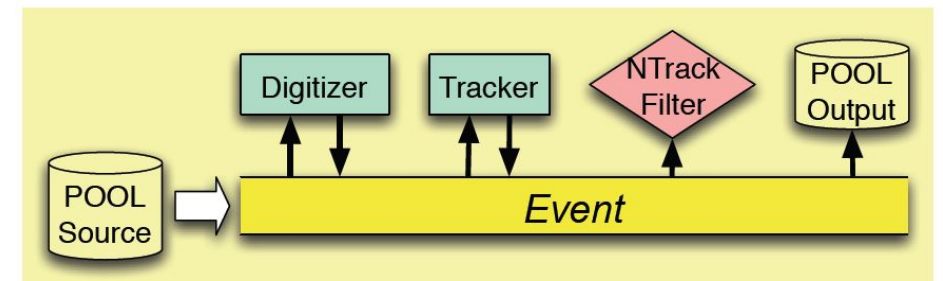
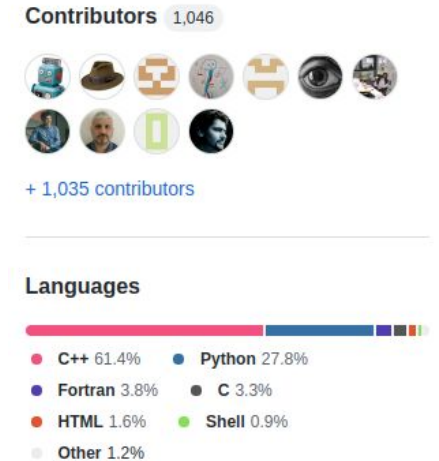
Tier 2: around 160 universities and other scientific institutes, handle analysis requirements and share of simulated event production and reconstruction

Tier 3: individual scientists will access these facilities through local computing resources



CMSSW

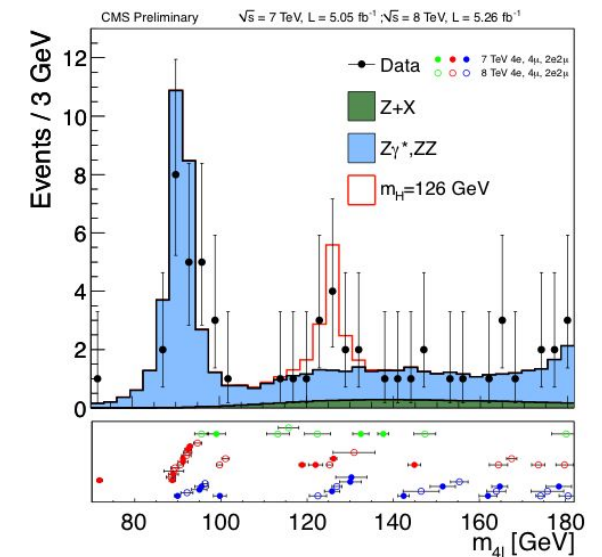
- The CMS Software (CMSSW) is a collection of software that the CMS experiment uses in order to acquire, produce, process and even analyze its data
- The program is written in C++ but its configuration is manipulated using the Python language.
- CMSSW is built around a Framework, an Event Data Model (EDM), and Services needed by the simulation, calibration and alignment, and reconstruction modules that process event data so that physicists can perform analysis
- <http://cms-sw.github.io/>



ROOT

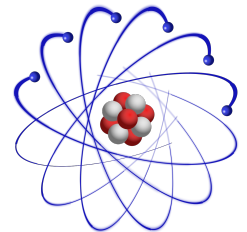


- An open-source data analysis framework used by high energy physics and others
 - Save and access your data (and any C++ object) in a compressed binary form in a ROOT file
 - Mine data by using powerful mathematical and statistical tools are provided to operate on your data
 - Results can be displayed with histograms, scatter plots, fitting functions and others
 - Use the Cling C++ interpreter for your interactive sessions and to write macros, or you can compile your program to run at full speed
 - ROOT provides a set of bindings in order to seamlessly integrate with existing languages such as Python and R
- <https://root.cern/>



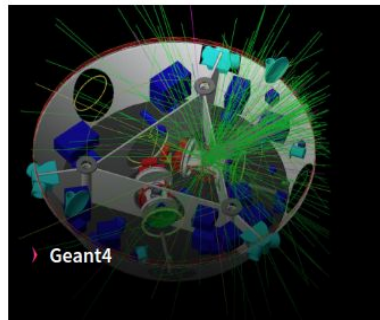
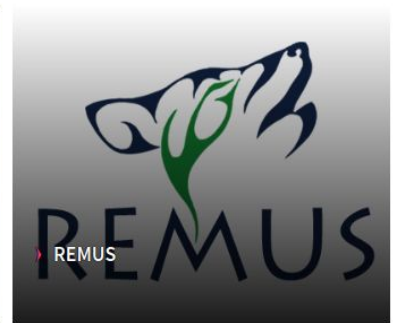
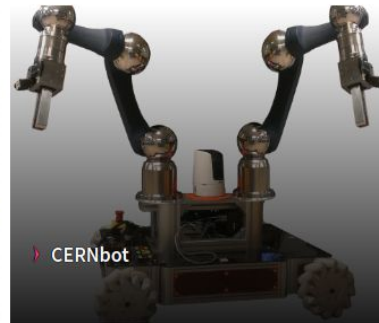
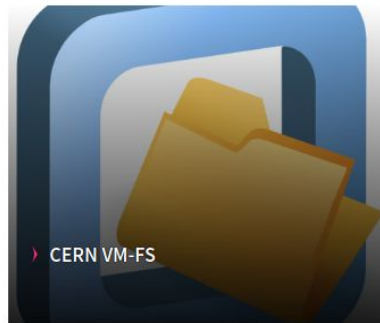
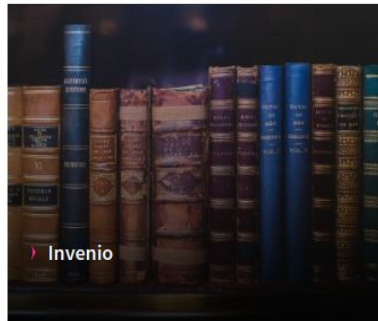
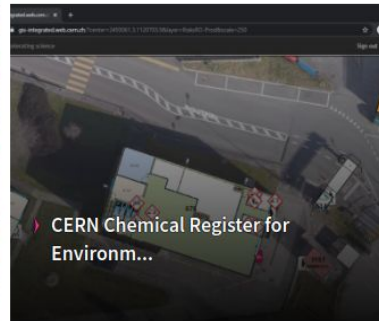
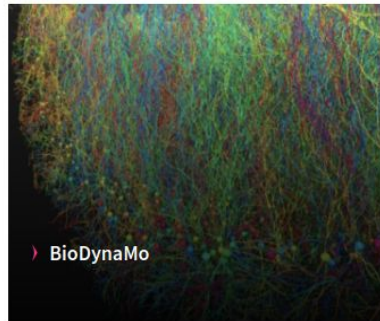
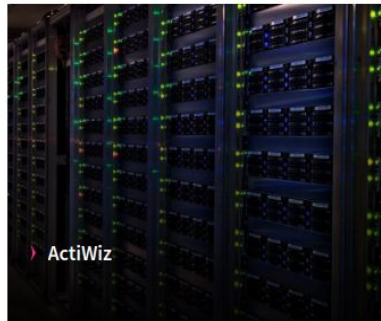
Linux at CERN

- CERN used to be a Red Hat Enterprise Linux customer. But, back in 2004, they worked with Fermilab to build their own Linux distribution called Scientific Linux.
- Eventually they realized that, because they were not modifying the kernel, there was no point in spending time spinning up their own distribution. In 2015, CERN began migrating away from Scientific Linux to CentOS. Scientific Linux is still maintained by a Fermilab, other labs and universities.
- On December 8, 2020, IBM's Red Hat announced the discontinuation of CentOS. CERN turned to alternative - AlmaLinux. Scientific Linux 7, at Fermilab, and CERN CentOS 7, at CERN, will continue to be supported for their remaining life, until June 2024.
- AlmaLinux, a somewhat popular free Linux distribution derived from Red Hat Enterprise Linux (RHEL), received a vote of confidence on Thursday from the European and American science communities.

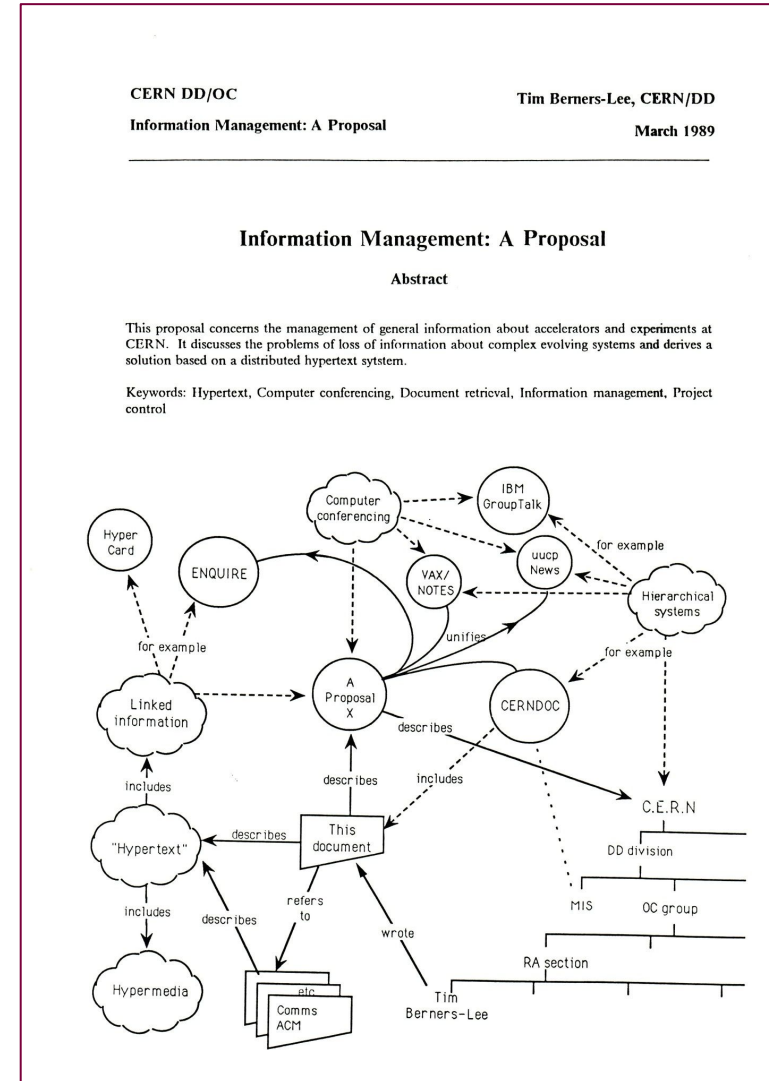
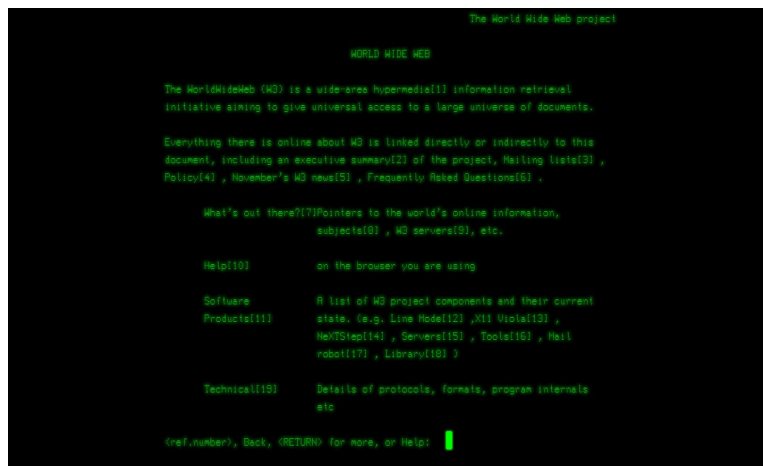


CERN ITC Technologies

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World Wide Web





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Thank you!

Valdas Rapševičius

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