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Speaker: **Alice Pagano**

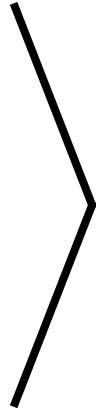
Ab-initio two-dimensional digital twin for quantum computer benchmarking



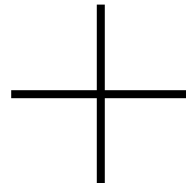
Collaboration of...



Alice Pagano
PhD Student



Padova



Ulm



$\sqrt{2}$



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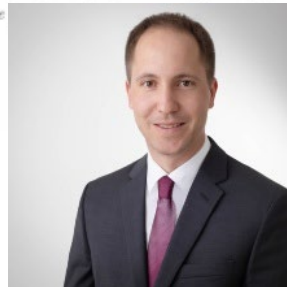
University of Stuttgart



Simone Montangero

Ab-initio two-dimensional digital twin for quantum computer benchmarking

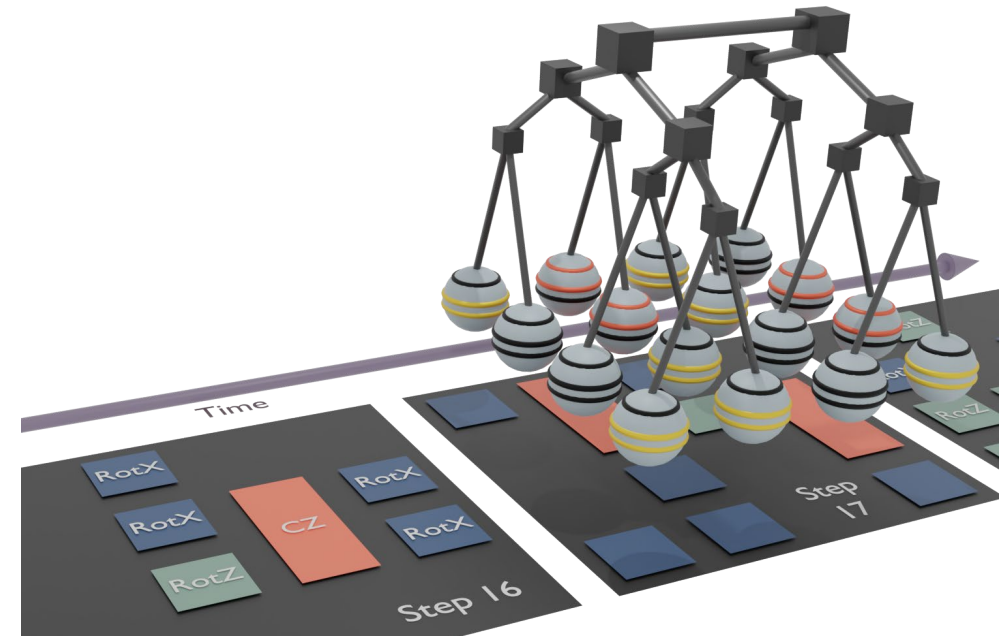
Daniel Jaschke^{1,2,3} Alice Pagano^{1,2,3} Sebastian Weber⁴ and Simone Montangero^{1,2,3}
¹Institute for Quantum Information Science, University of Stuttgart, Germany



Daniel Jaschke



Sebastian Weber



Classical simulation of QPU

 QPU = Quantum Processing Unit

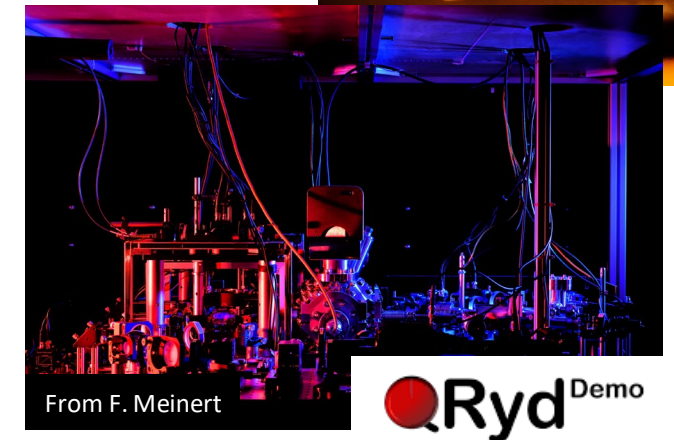
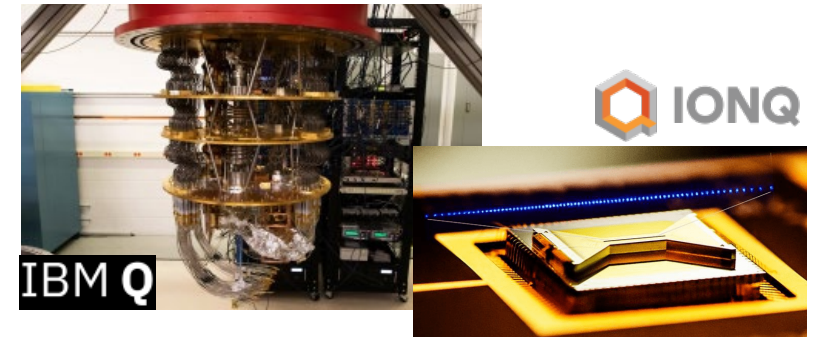
Goal

- Gain insights on quantum hardware for QPU development
- Large scale simulation to support the next decades of hardware developments

Our approach

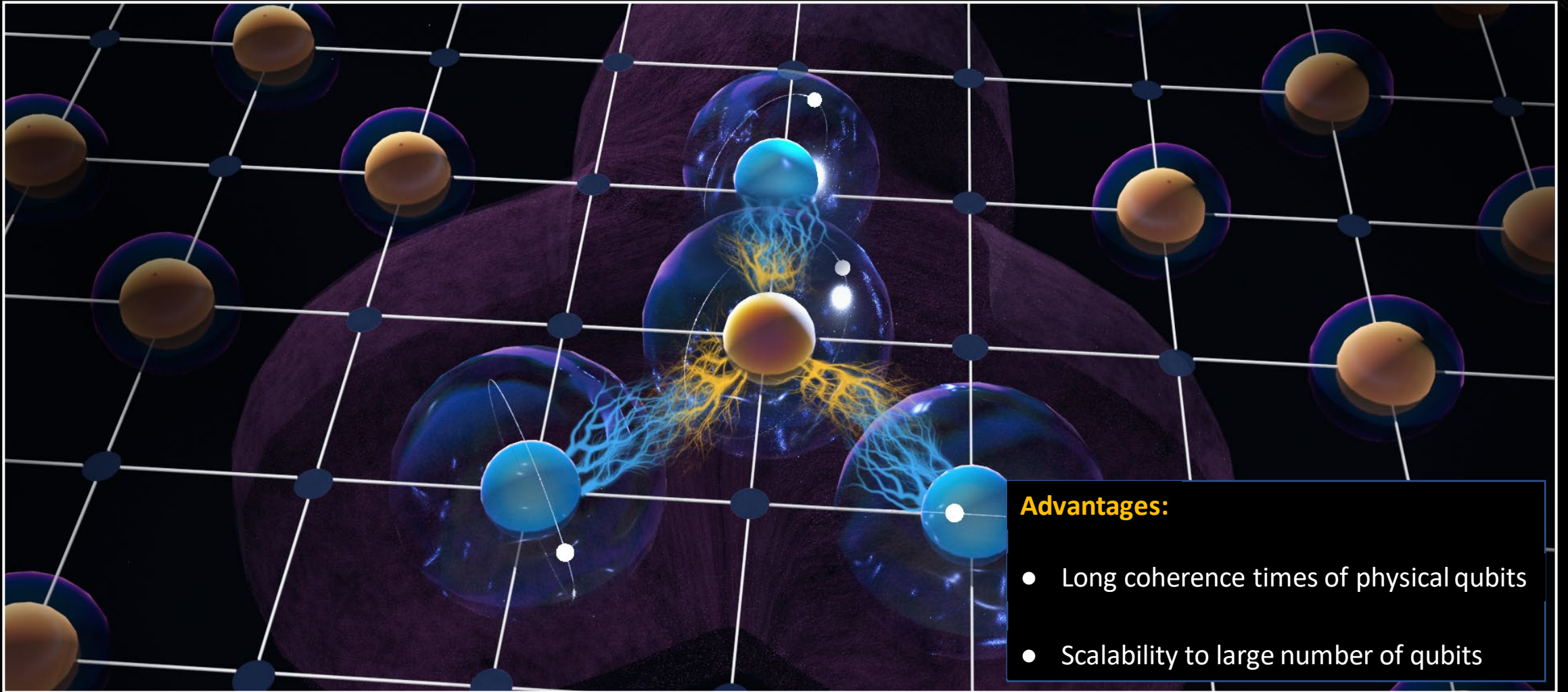
	We don't do	We do
Gate:	Matrix	Pulse
Qubits:	>100	>100
Speed:	Faster	Slower

- Our **digital twin** can simulate different platforms, e.g. Rydberg quantum computer



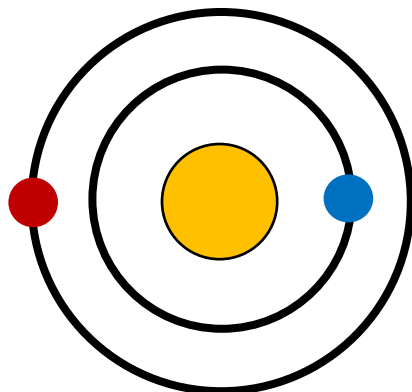
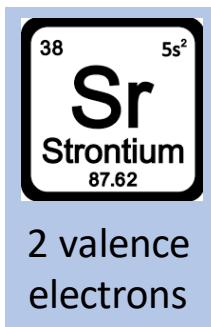
Outline

- Overview of Rydberg QPU
- Main ingredients of the digital twin
- Analysis of crosstalk between CZ gates
- Summary

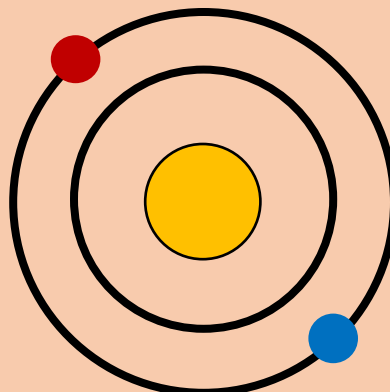


Rydberg QPU overview

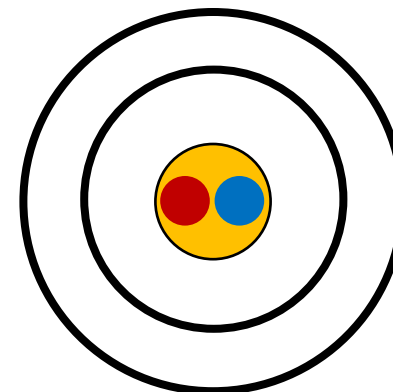
Qubits in Strontium atom



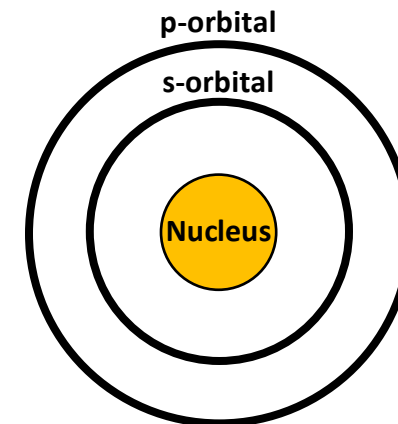
Optical Qubit



Fine-Structure Qubit



Nuclear Qubit



Coherence time

Seconds

Tens of milliseconds

Minutes

Pros

Well understood

Fast single-qubit gates

Well-protected from environment

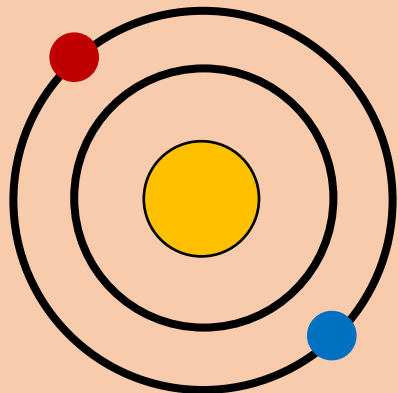
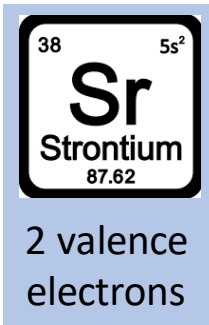
Cons

Slow single-qubit gates

Dephasing due to finite tensor polarizability

Cryogenic setup

Energetic levels of Sr-88



Fine-Structure Qubit

Coherence time

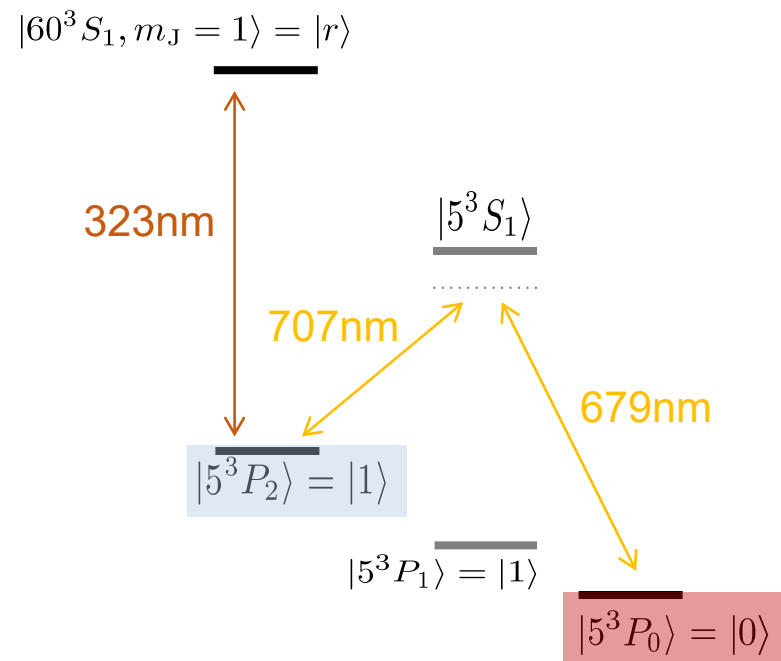
Tens of milliseconds

Pros

Fast single-qubit gates

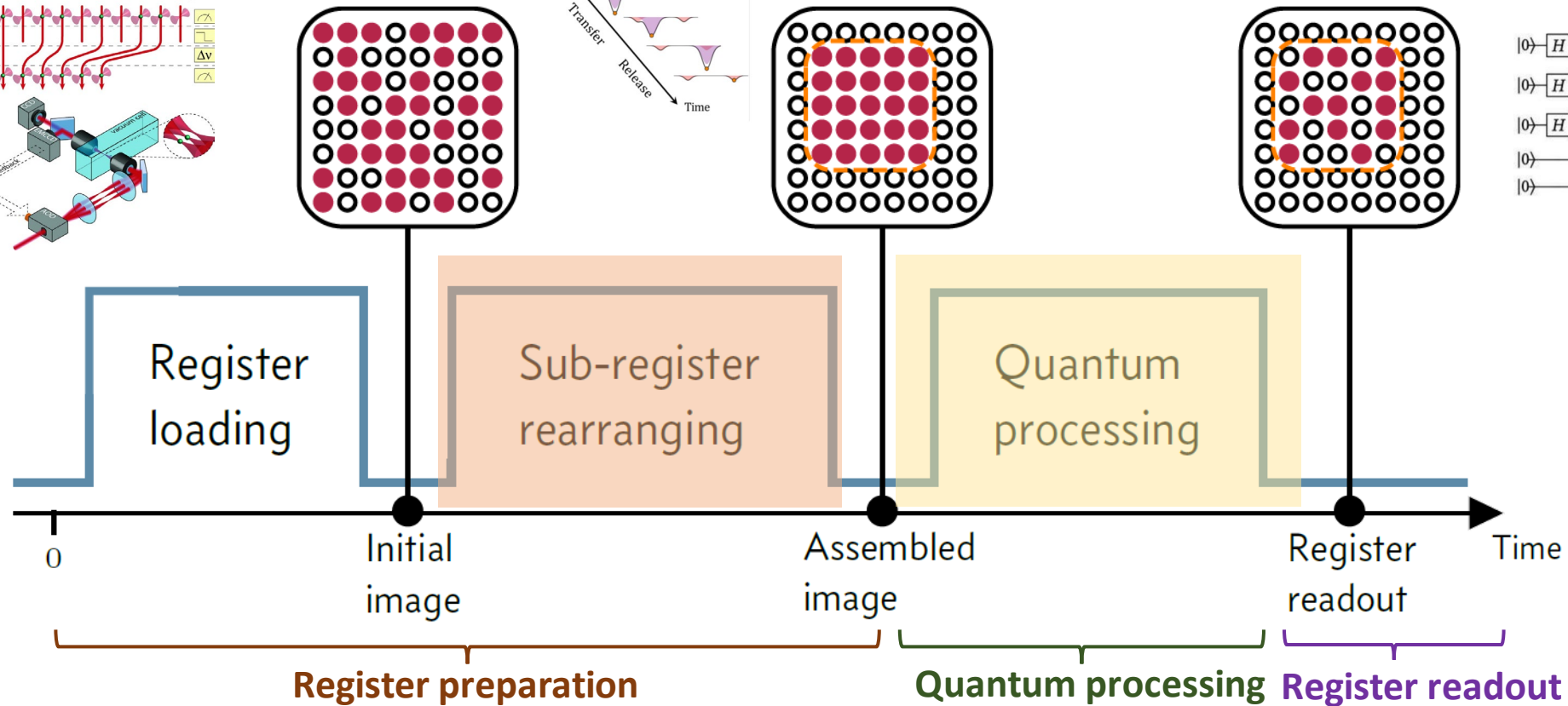
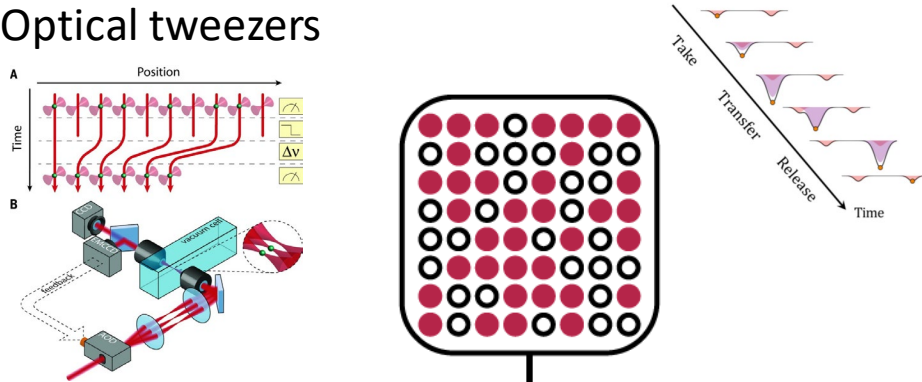
Cons

Dephasing due to finite tensor polarizability

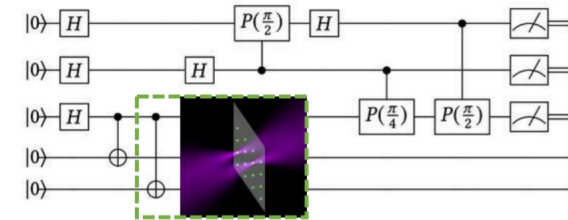


One computation cycle for Rydberg QPU

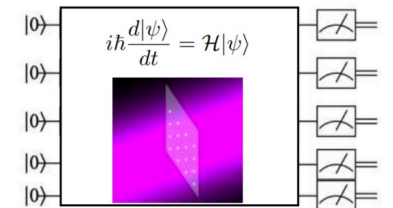
Optical tweezers



Digital processing

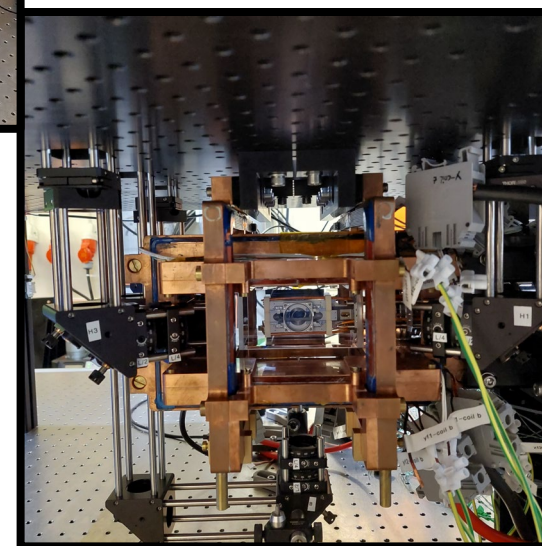
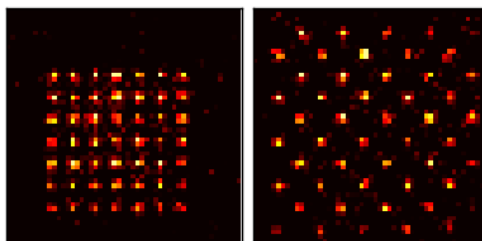
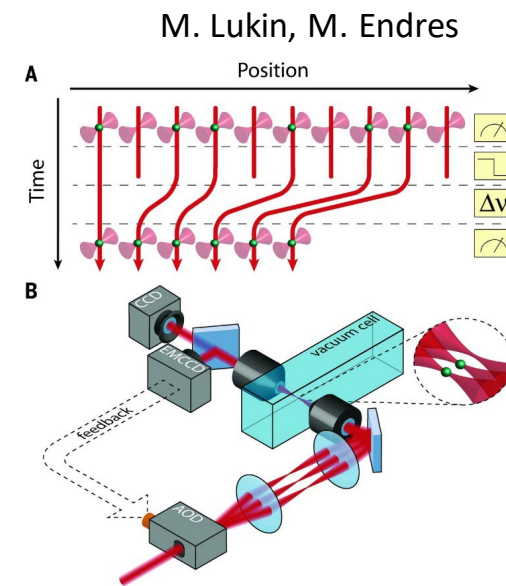
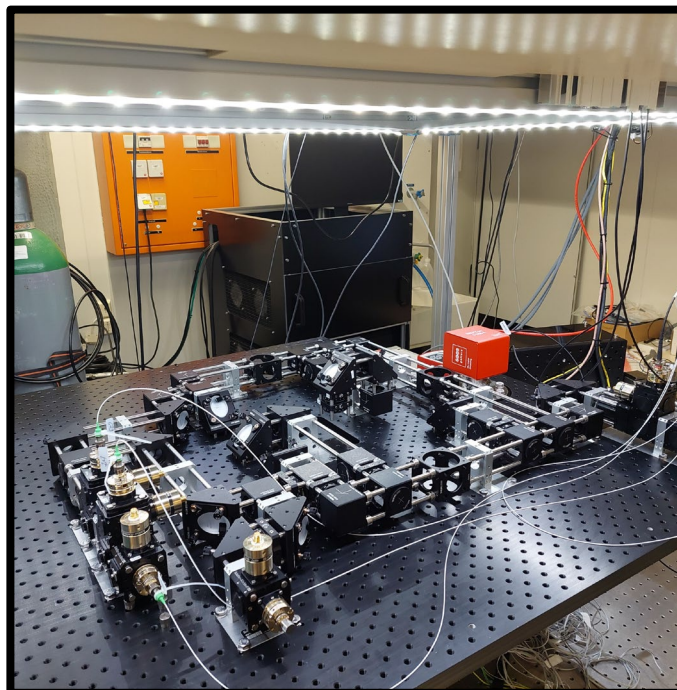
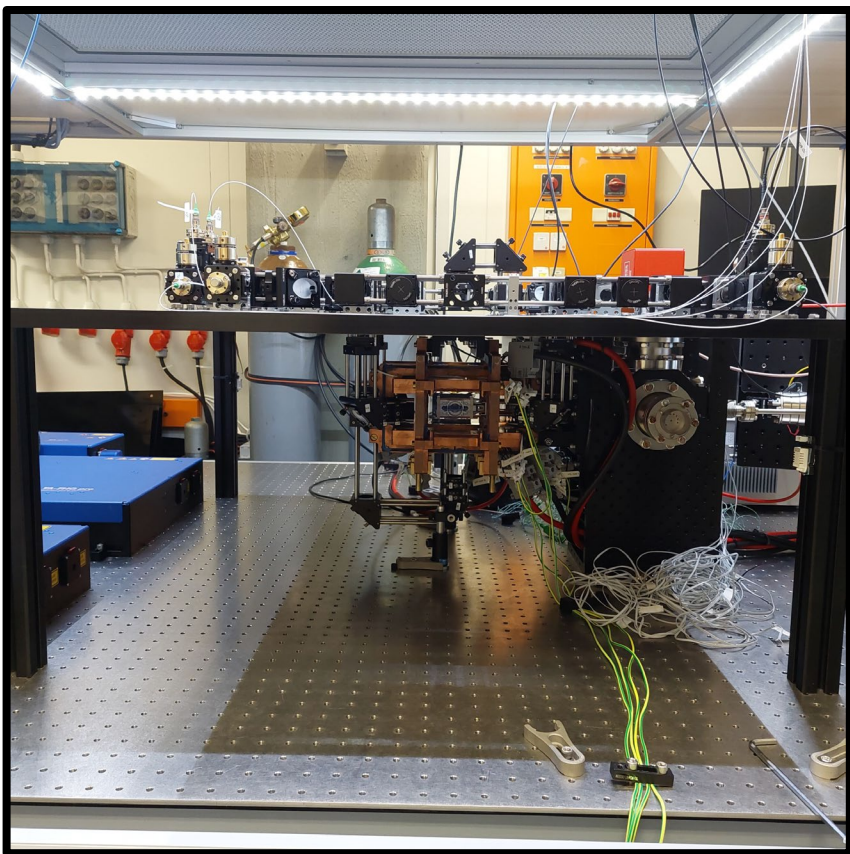


Analog processing



Using acousto-optic deflectors

Ryd Demo



Experimental setup of Rydberg QPU

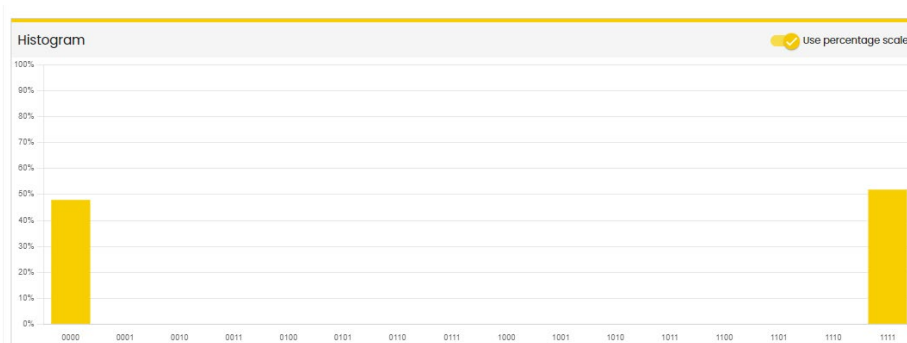
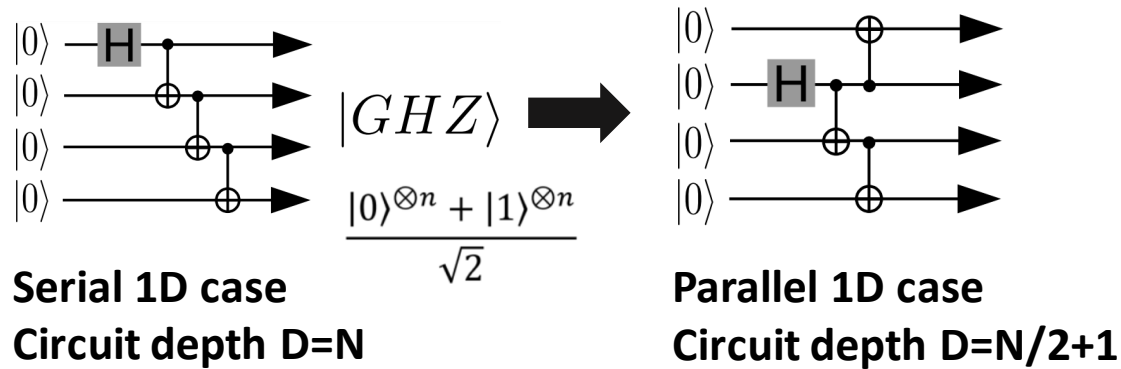


Digital twin of Rydberg QPU

A lot of ingredients...

Question

➤ Prepare global GHZ state



THE QUANTUM LÄND
RYDBERG QUANTUM COMPUTERS & SIMULATORS MADE IN STUTTART.

Quantum circuit

```

q[0] H
q[1]
q[2]
q[3]
+ - 1 2 3 4
    
```

QASM code

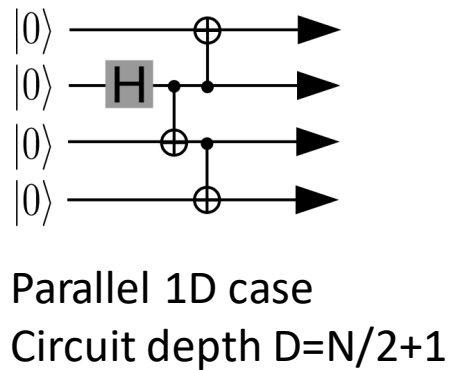
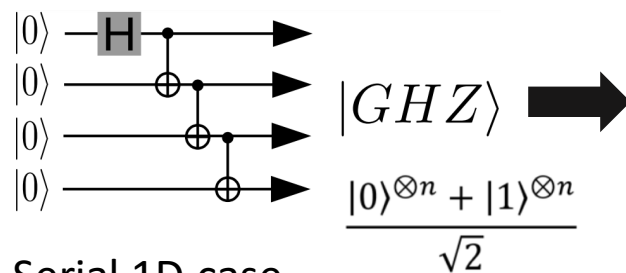
```

1 OPENQASM 3;
2 include 'customgates.inc';
3
4 bit[4] c;
5 qubit[4] q;
6
7 h q[0];
8 cx q[0], q[1];
9 cx q[1], q[2];
10 cx q[2], q[3];
11
12 c[0] = measure q[0];
13 c[1] = measure q[1];
14 c[2] = measure q[2];
15 c[3] = measure q[3];
    
```

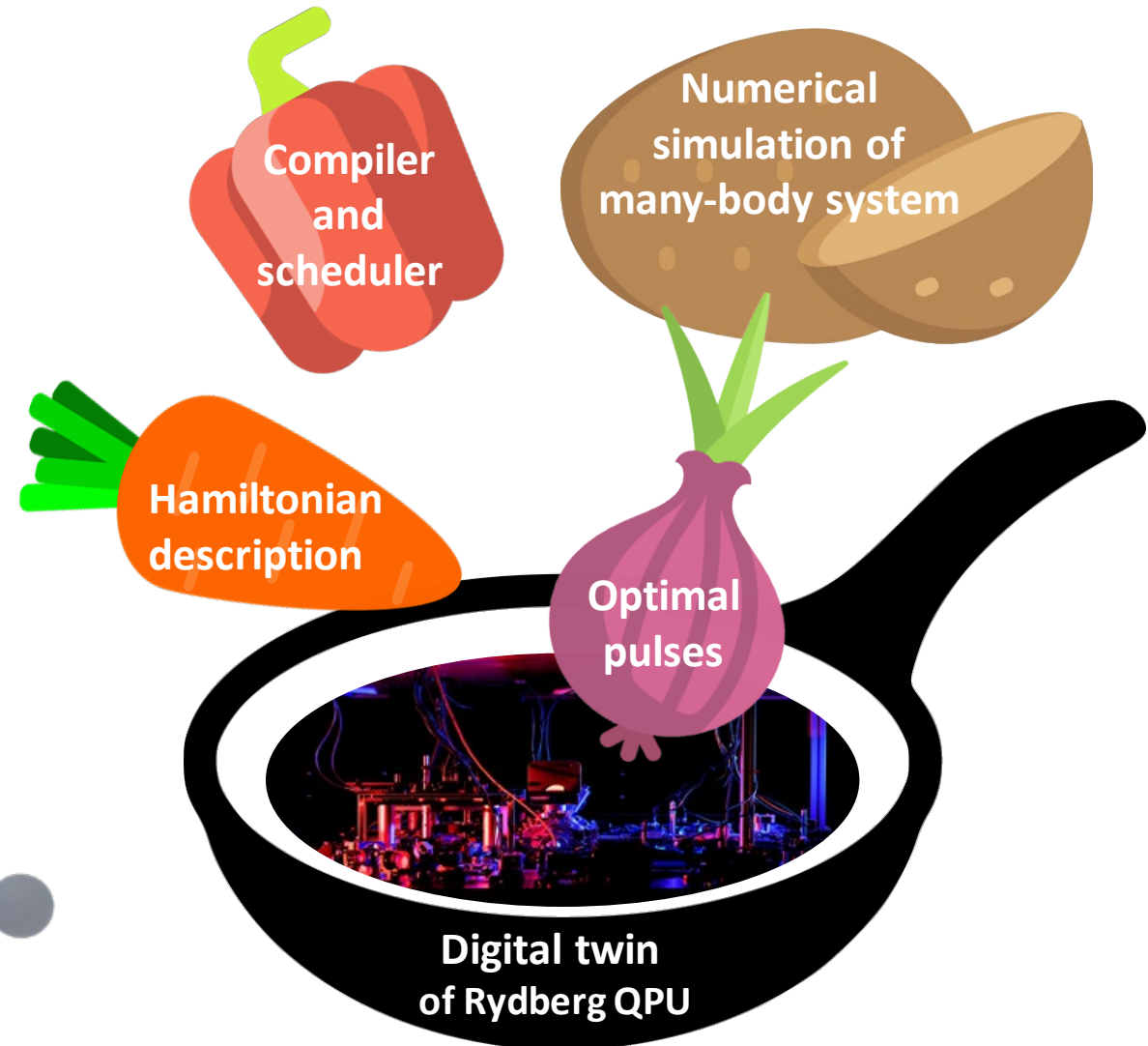
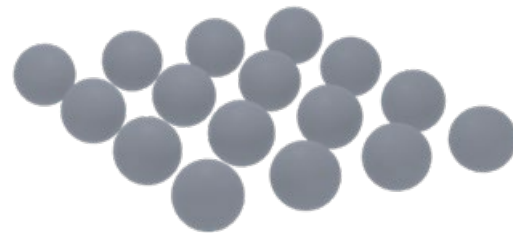

A lot of ingredients...

Question

➤ Prepare global GHZ state

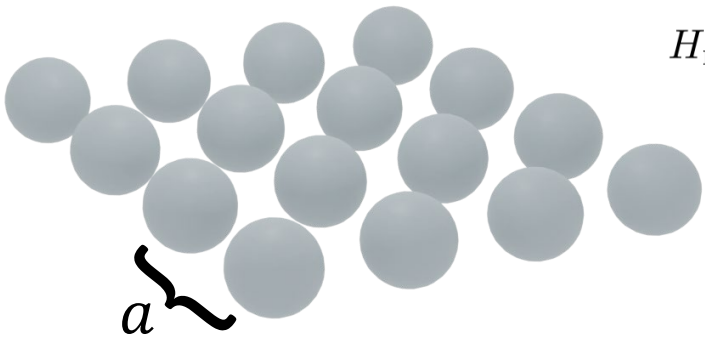


To which extent can we profit in 2d Rydberg systems from parallelization?

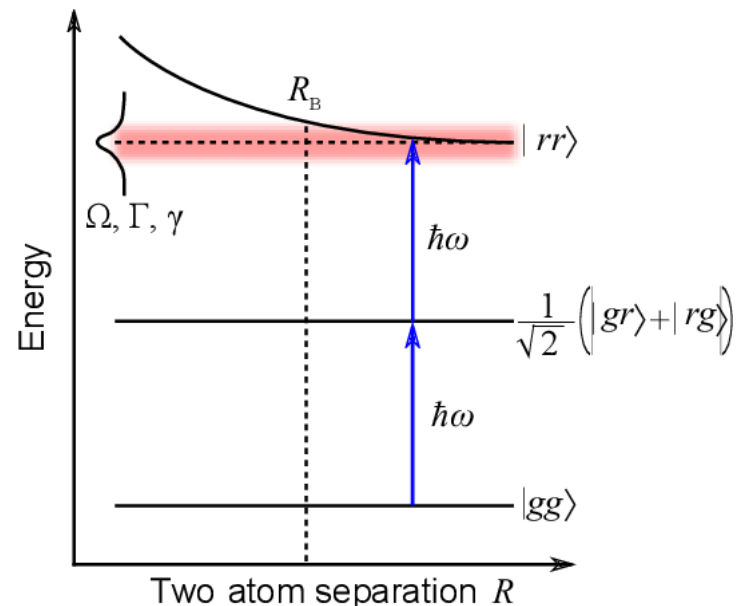


Hamiltonian of Rydberg QPU

- 2d array of Rydberg atoms trapped in optical tweezers




$$\begin{aligned}
 H_{\text{Ryd}} = & \sum_{j,k} \Omega_{j,k}^x(t) \sigma_{j,k}^x + \Omega_{j,k}^z(t) \sigma_{j,k}^z \\
 & + \sum_{j,k} \left(\Omega_{j,k}^R(t) \sigma_{j,k}^+ + h.c. \right) \\
 & + \sum_{j,k} \sum_{j',k'} V(j,k,j',k') n_{j,k} n_{j',k'}
 \end{aligned}$$



- Three-level system description: 0, 1, r

Sr88



————— $|r\rangle = |60^3S_1, m_J = 1\rangle$
 ————— $|1\rangle = |5^3P_2\rangle$
 ————— $|0\rangle = |5^3P_0\rangle$

- Strong long-range Rydberg interaction

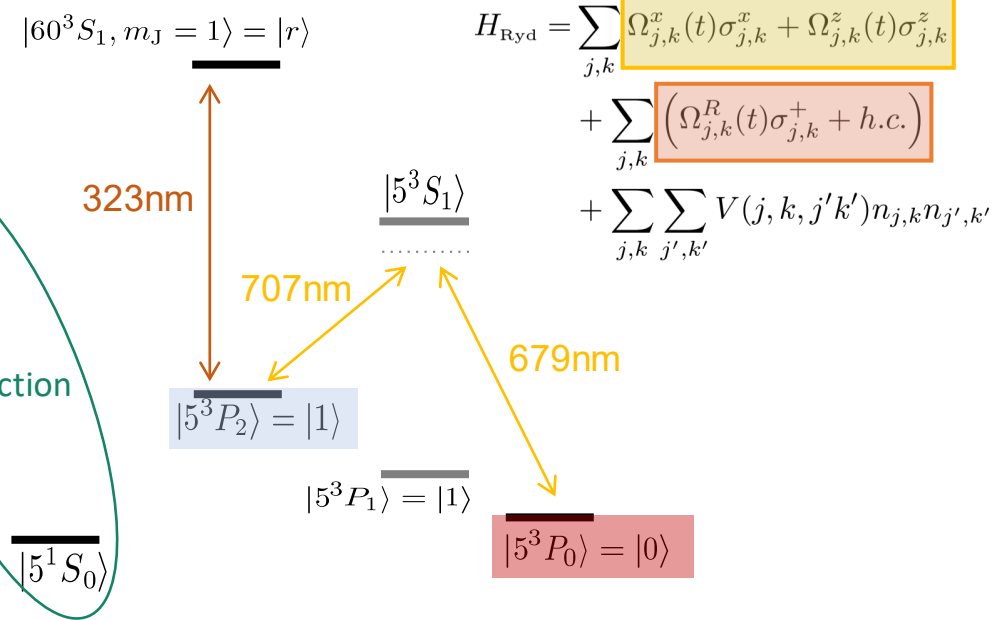
$$V(j, k, j', k') = \frac{-C_6}{d^6} \propto n^{11}$$

Rydberg blockade mechanism

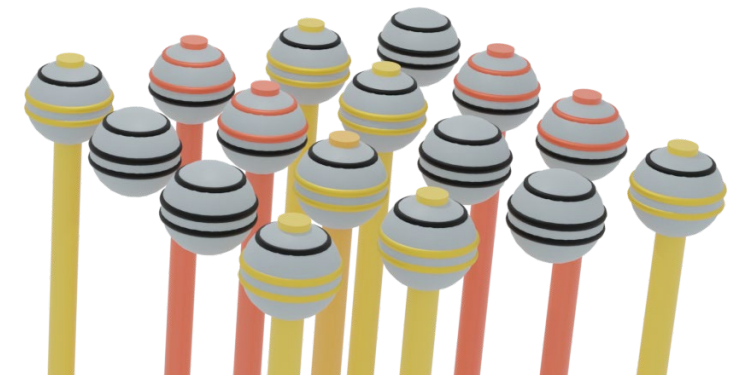
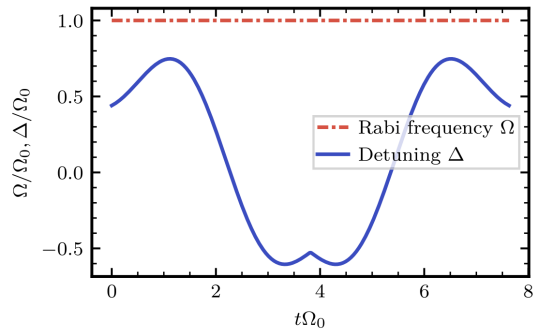
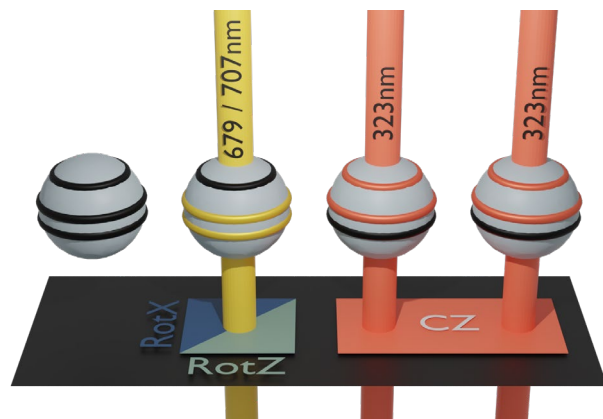


Optimal pulses and gates

- Single-qubit gates: are implemented via Raman lasers
- Two-qubit gates: use the Rydberg interaction in the r-state to implement a CZ gate
- Protocol from Pagano et al, PRR 4, 033019
10% time speedup



$$H_{\text{Ryd}} = \sum_{j,k} \Omega_{j,k}^x(t) \sigma_{j,k}^x + \Omega_{j,k}^z(t) \sigma_{j,k}^z + \sum_{j,k} \left(\Omega_{j,k}^R(t) \sigma_{j,k}^+ + h.c. \right) + \sum_{j,k} \sum_{j',k'} V(j,k,j',k') n_{j,k} n_{j',k'}$$




Algorithm: compiler

1. qoqo compiler



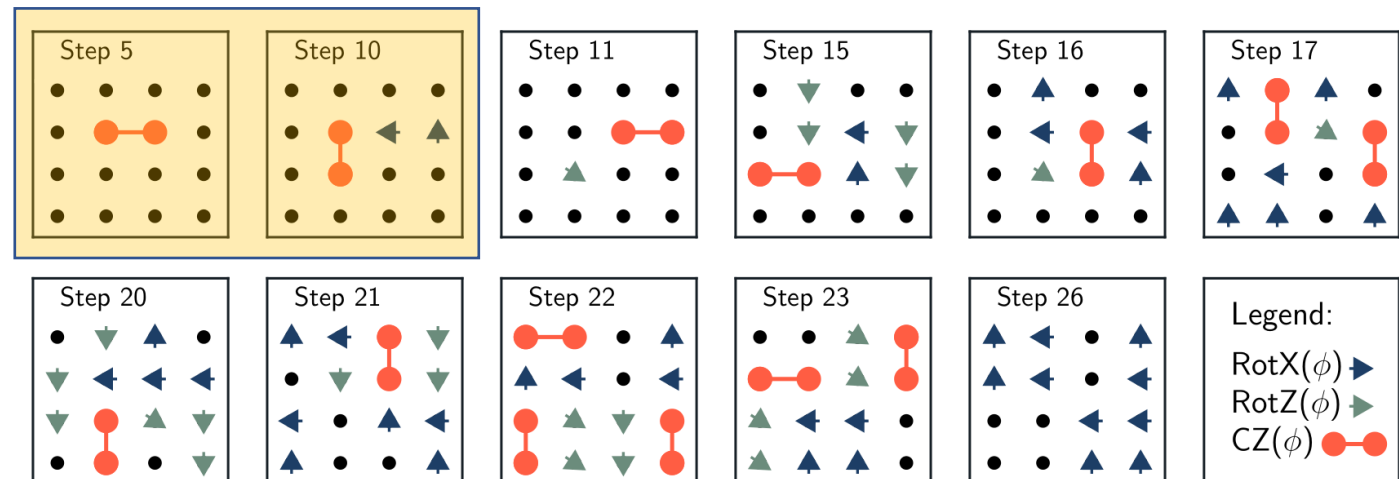
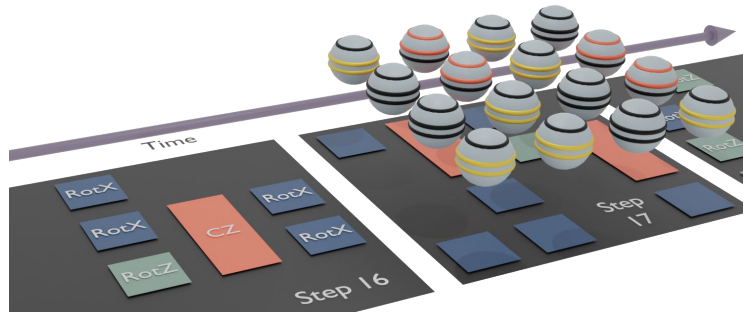
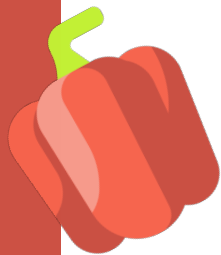
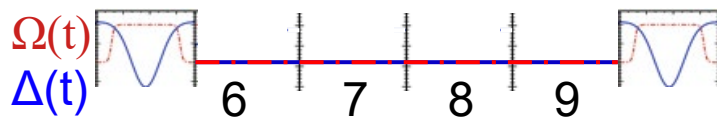
- Translate Hadamard into native gate set

$$H = \text{Rot}_Z\left(\frac{\pi}{2}\right) \text{Rot}_X\left(\frac{\pi}{2}\right) \text{Rot}_Z\left(\frac{\pi}{2}\right)$$

- Translate CNOT into >10 native gates, CZ ... 

2. Dedicated GHZ compiler

- Set minimal distance r_g between CZ gates in parallel and track all the possibilities



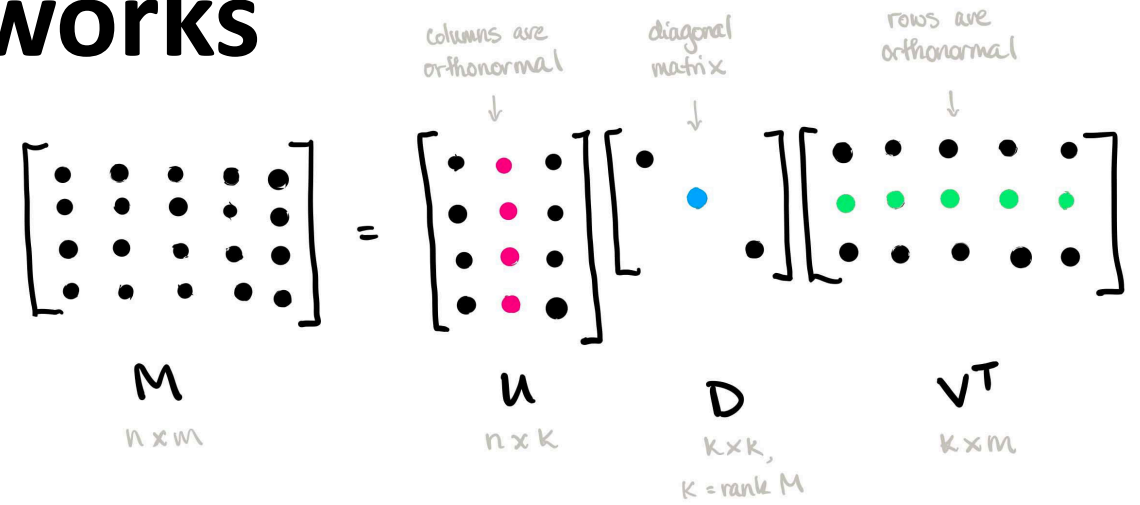
Idea behind tensor networks

Singular Value Decomposition (SVD)

The entries of the diagonal matrix **D** are non negative numbers called **singular values**.

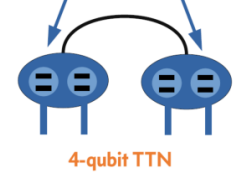
Intuitively, they indicate the amount of "interaction" between the information stored by **U** and **V**, and they mediate how those interactions contribute to the information represented by **M**.

example: image compression



Schmidt decomposition

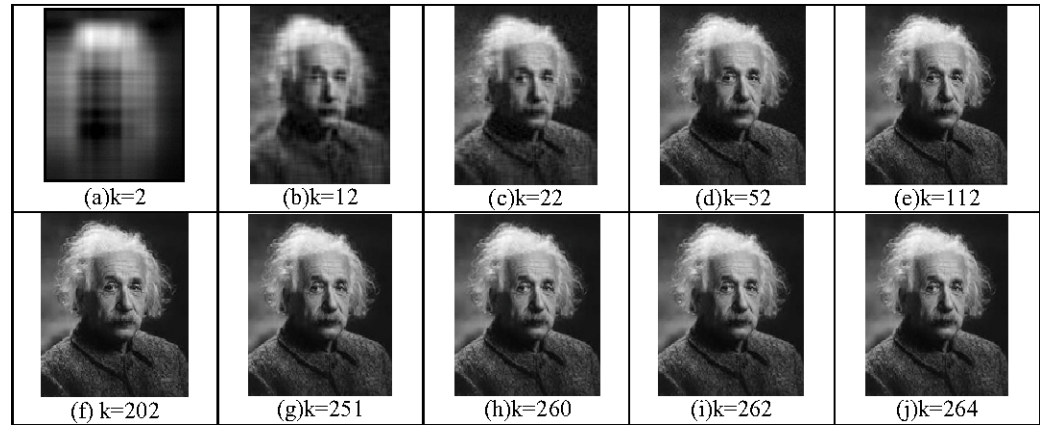
$$|\psi_{1,2,3,4}\rangle = \sum_{i=1}^n \lambda_i |\psi_{1,2,i}\rangle |\psi_{3,4,i}\rangle$$



In physics language...

M matrix represents a *quantum state*

D captures the *entanglement* in the system



k = number of singular values = *bond dimension*

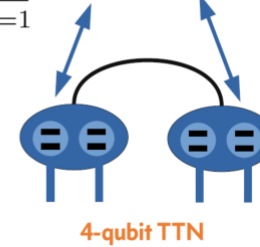
K. M. Aiswarya, International Conference on Wireless Communications (2016)
<https://www.math3ma.com/blog/understanding-entanglement-with-svd>

Numerical simulation with TTN

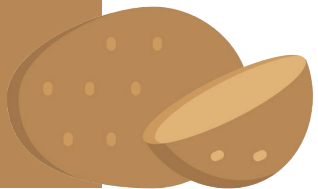
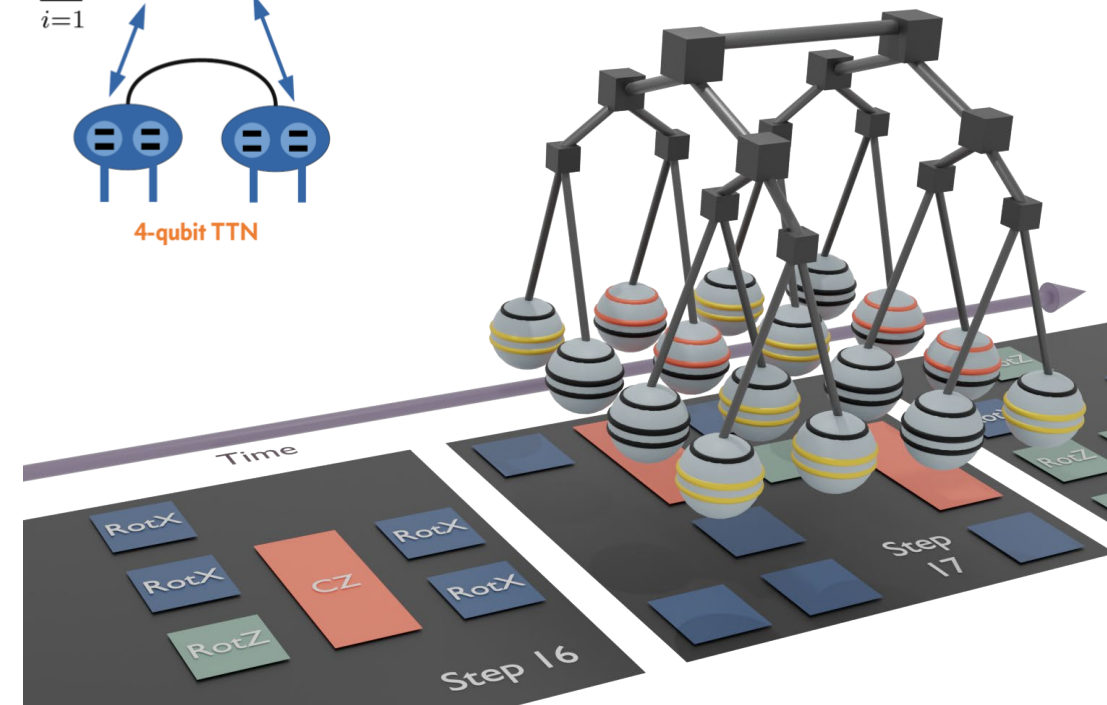


- We solve the Schrödinger equation
- Tree Tensor Networks (TTN) run Hamiltonian evolution
- Truncation in entanglement via Schmidt decomposition
- Time evolution via time-dependent variational principle
- Van der Waals interaction included up to $r_g + d_{offset}$

$$|\psi_{1,2,3,4}\rangle = \sum_{i=1}^n \lambda_i |\psi_{1,2,i}\rangle |\psi_{3,4,i}\rangle$$



Maximal bond dimension $3^8 = 6561$



GHZ state preparation

Question

➤ Prepare global GHZ state

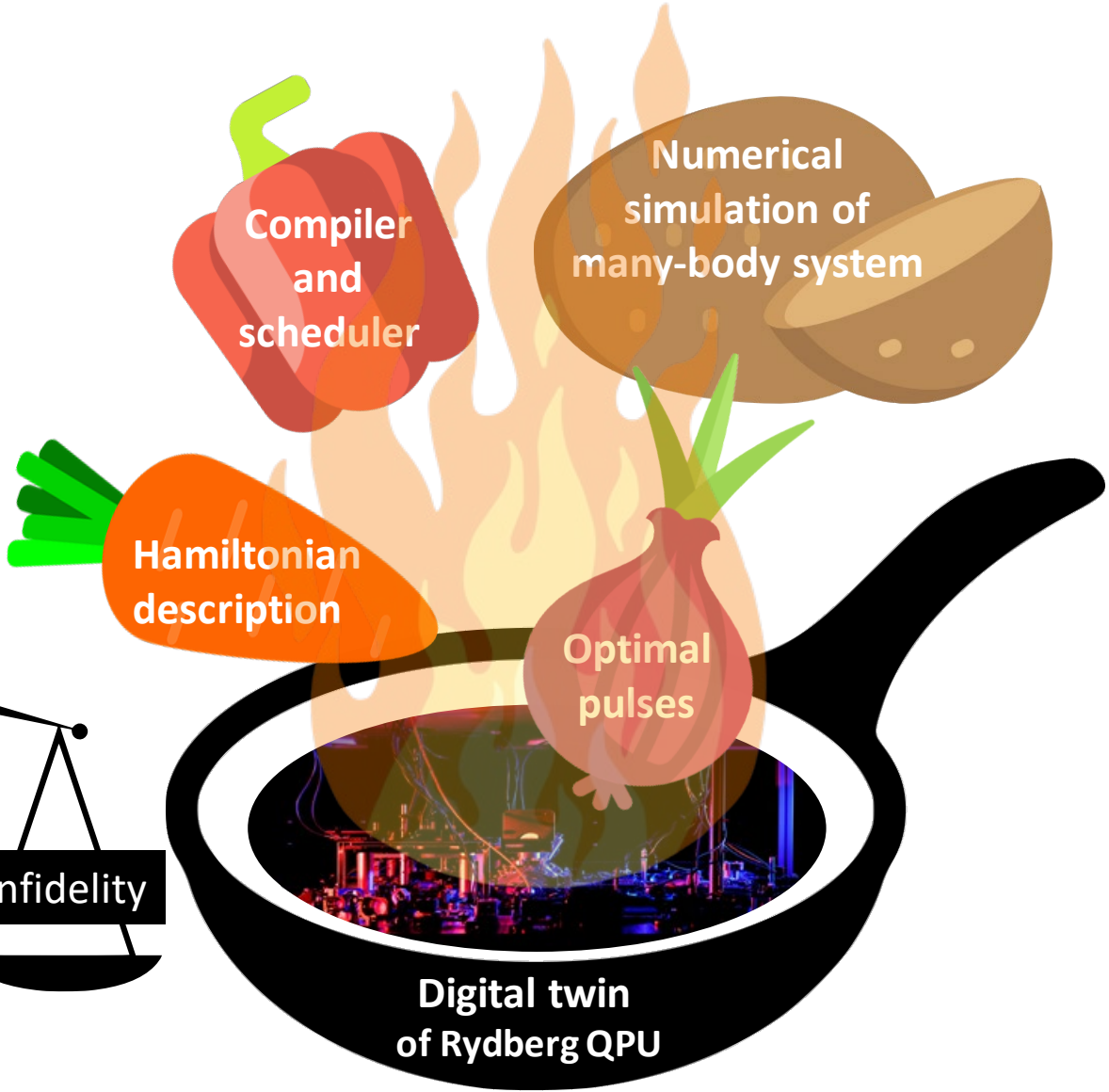
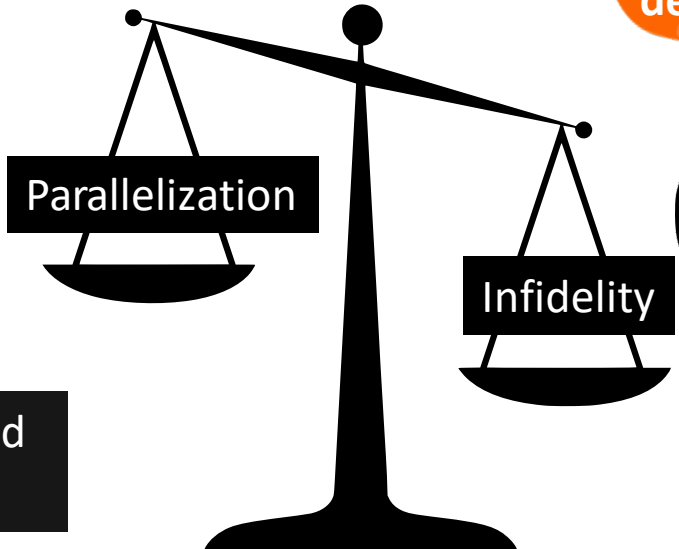
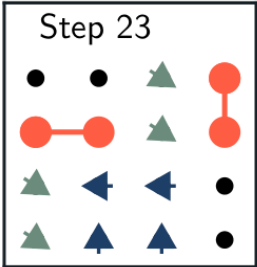
To which extent can we profit in 2d Rydberg systems from parallelization?

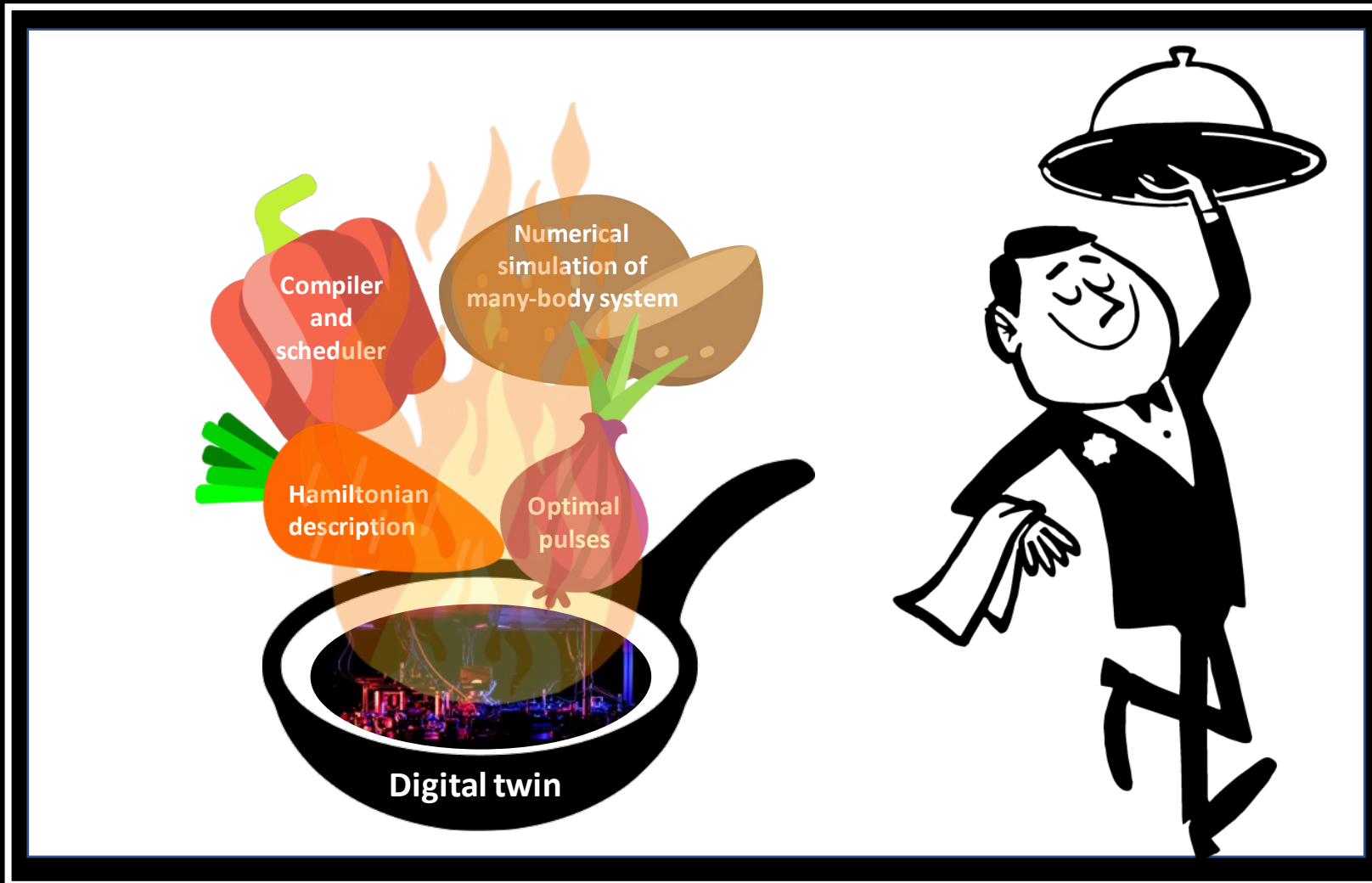
Issue

➤ Rydberg interaction is long-range and introduces crosstalk

Find minimal distance r_g required between CZ gates in parallel

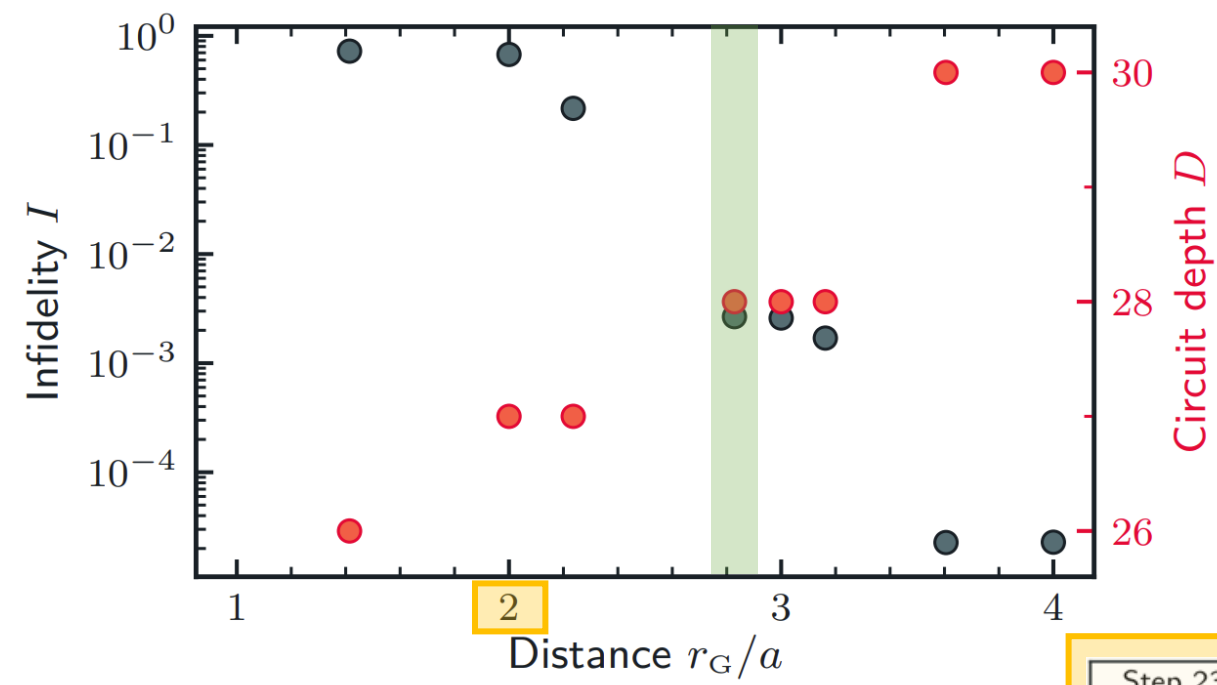
$$r_g = 2a$$





Results of crosstalk analysis

Quantify crosstalk 4x4

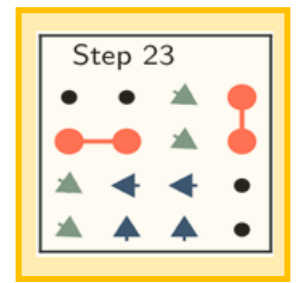


- The fidelity F of the algorithm is the state fidelity at the end

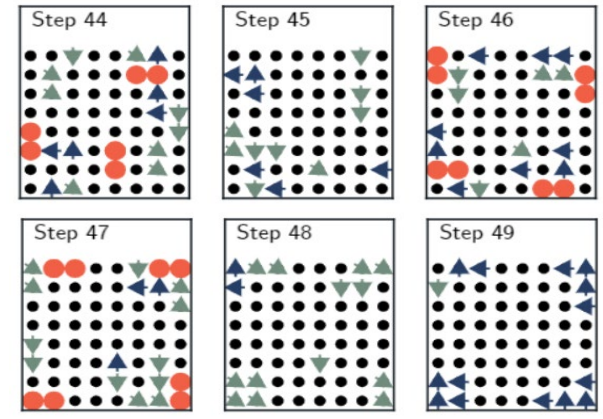
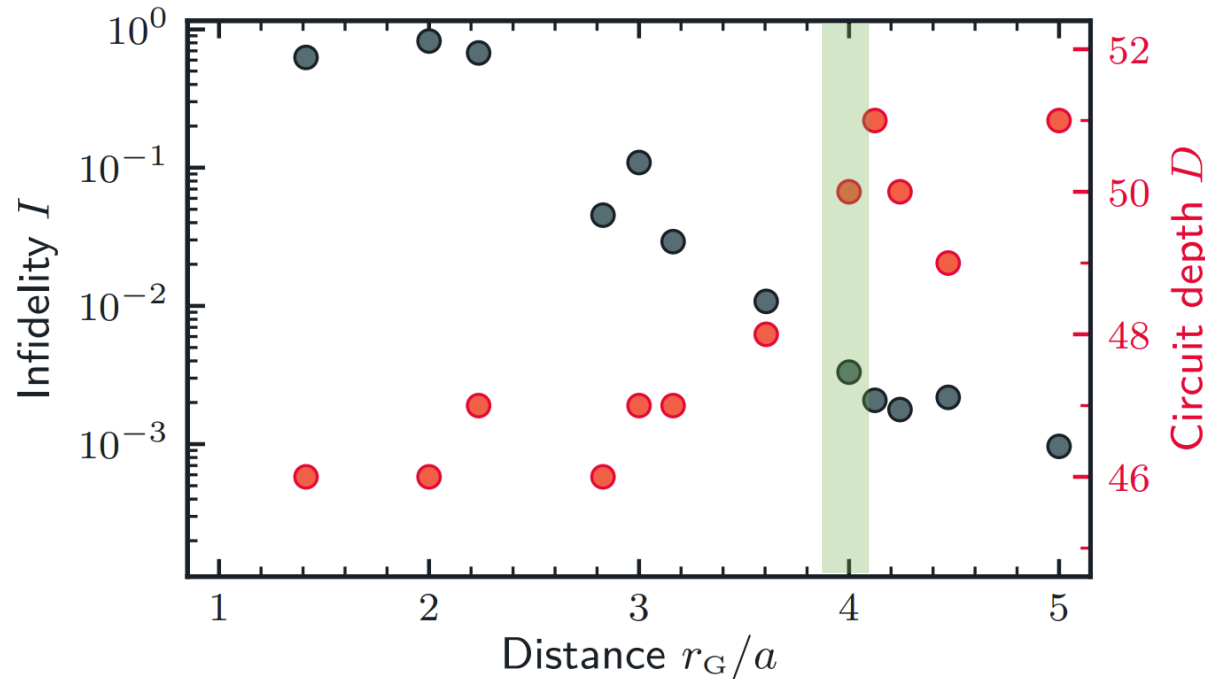
$$F = |\langle \psi(\tau) | \psi_{\text{GHZ}} \rangle|^2$$

$$I = 1 - F$$
- 16 qubit GHZ state can reach fidelities above 0.9999 in a closed system
- We define the safety distance for parallel execution of CZ gate at $\sqrt{8a}$

$$\mathcal{F}_{CZ}^{15} = 0.999998355^{15} = 0.999976$$



Quantify crosstalk 8x8



- 64 qubit GHZ state can reach fidelities above 0.99 in a closed system
- We define the safety distance for parallel execution of CZ gate at $4a$
- Larger system sizes profit more from parallelization

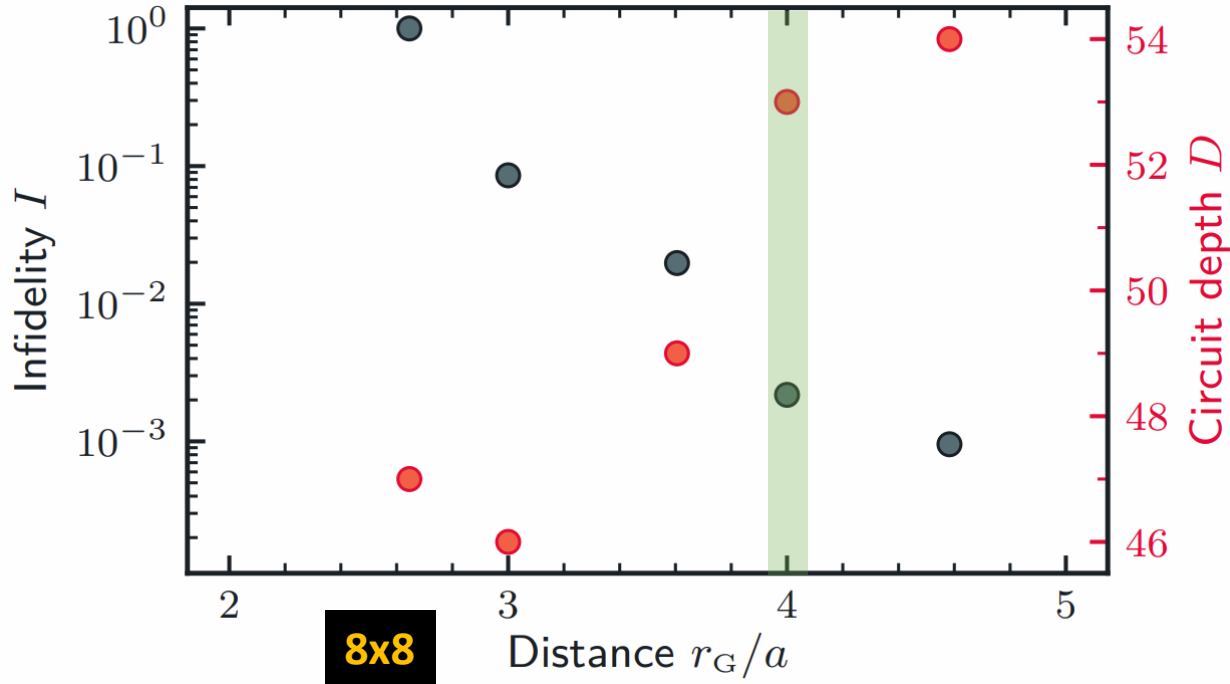
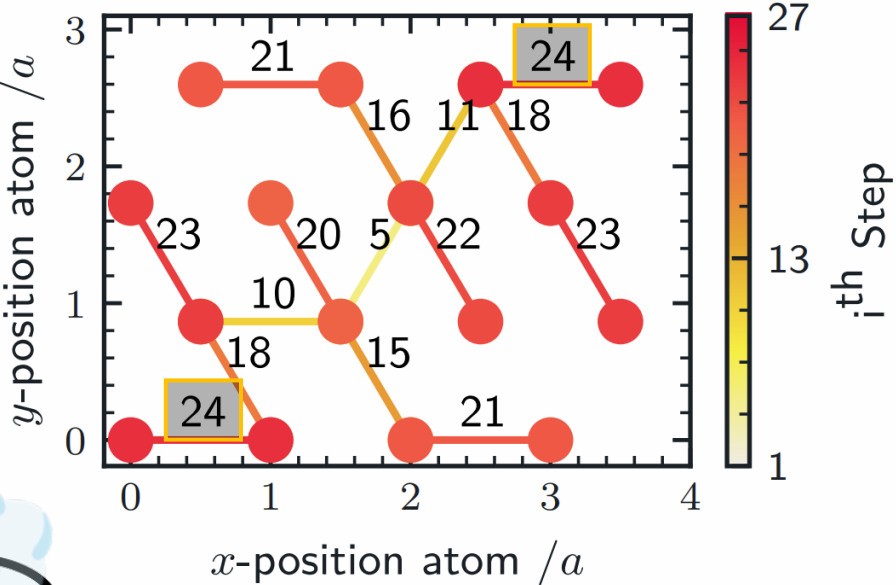
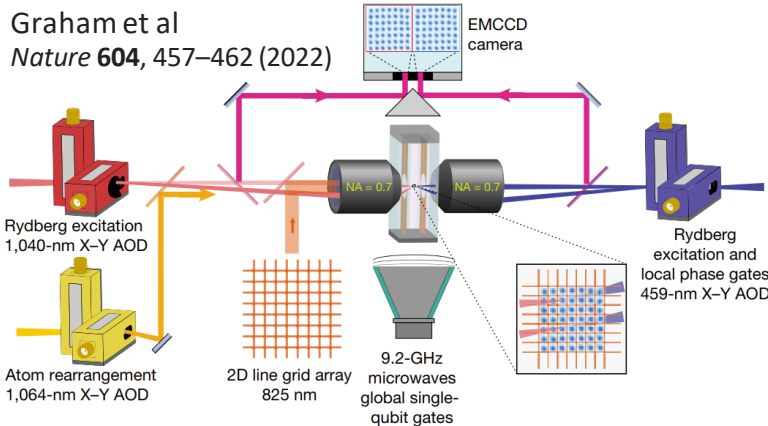
Circuit property	4×4 (16)	6×6 (36)	8×8 (64)
r_S	$\sqrt{8}a$	$4a$	$4a$
D_{\min}	23	33	43
$D(r_S)$	28	39	50
$D_{\text{CZ-serial}}$	30	50	78
D_{serial}	168	388	696

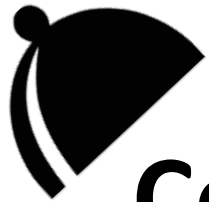
64 < 15% overhead compared to min r_g circuit
 > 35% speedup compared to CZ-serial circuit
 > 92% speedup compared to all-serial circuit



Triangular lattice layout

- Different qubit layout can be implemented
- An atom can have 6 nearest neighbors





Conclusions



thequantumlaend.de



- Overview of Rydberg Quantum Processing Unit
- Develop digital twin of a quantum computer for Rydberg QPU
- Prepare global GHZ state and study gate crosstalk
 - For 8x8 array, parallel CZ must be four lattice spacings apart
 - Then, crosstalk is negligible in comparison to other sources of error

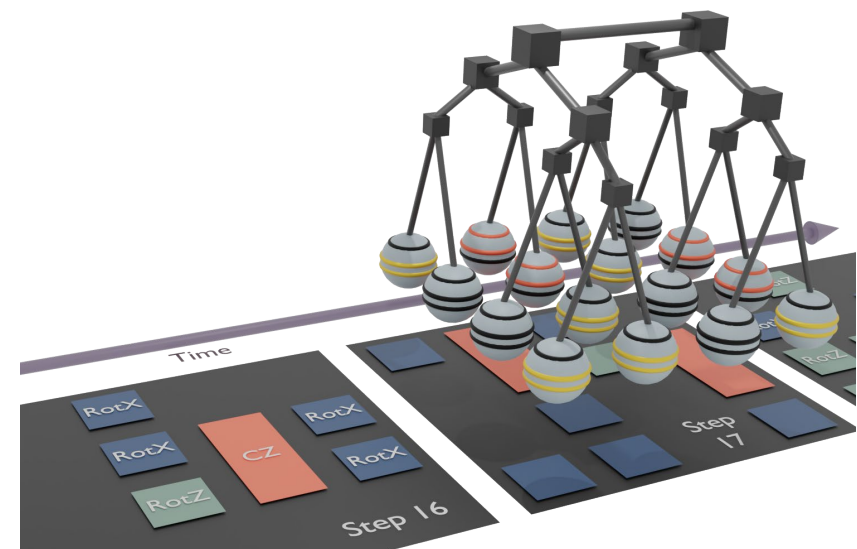
	We don't do	We do
Gate:	Matrix	Pulse
Qubits:	>100	>100
Speed:	Faster	Slower

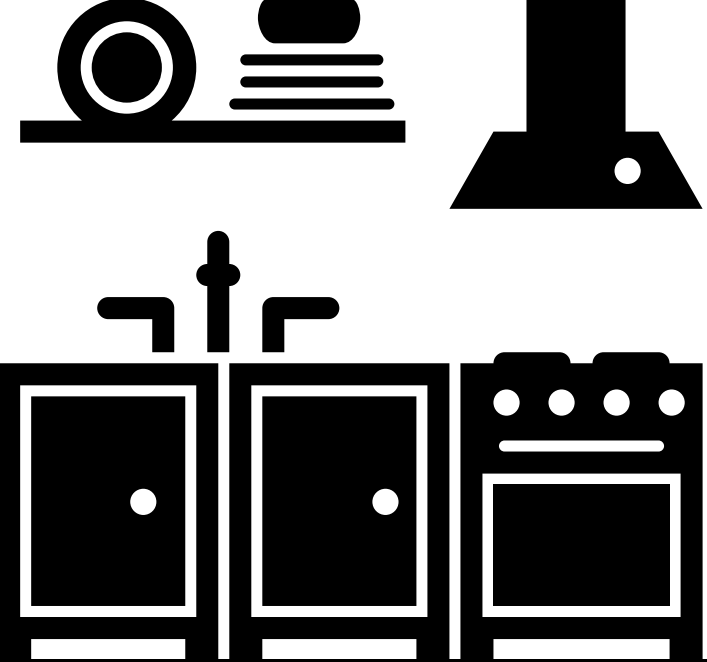


64 qutrits \approx 100 qubits



THANK YOU !





Backup slides

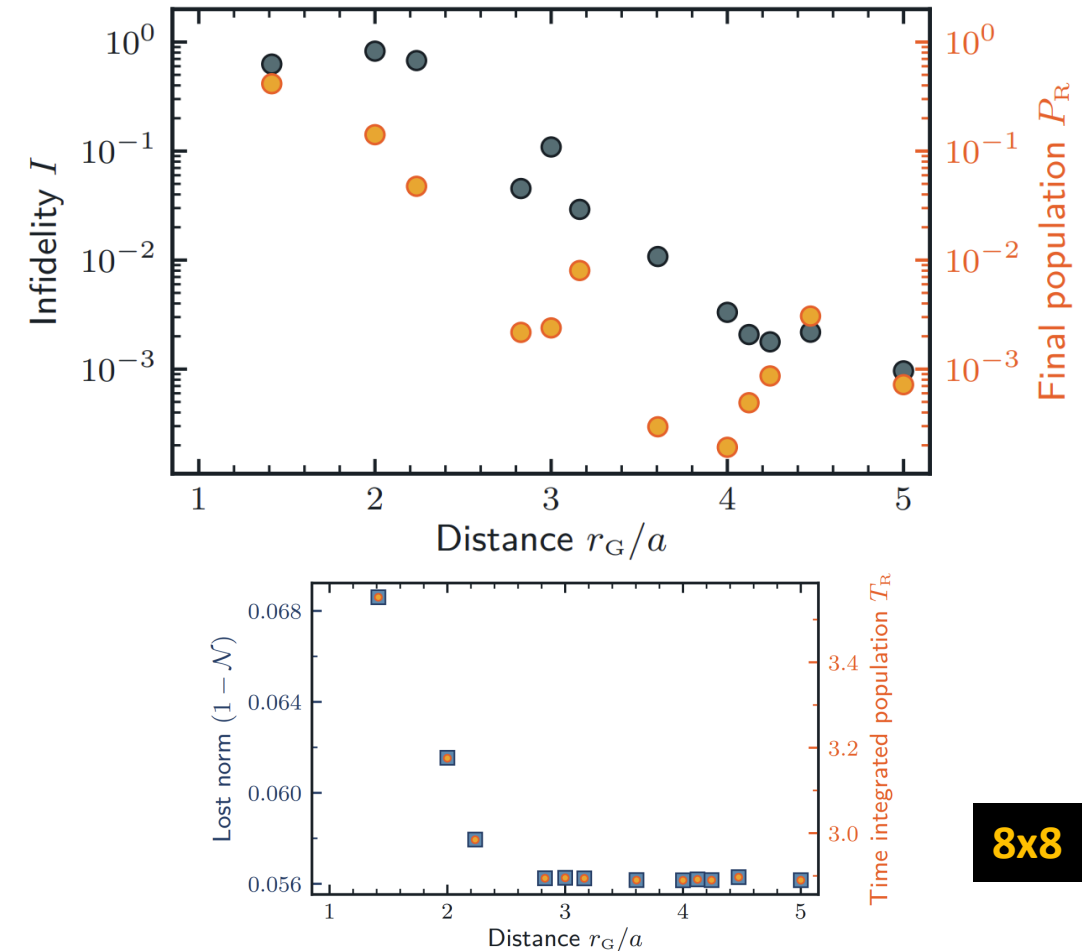
Rydberg measurement 8x8

- Decay from the Rydberg state is the most important source of error for a single CZ gate

$$H_{\text{OQS}} = H_{\text{Ryd}} - i\gamma \sum_{j,k} |r\rangle \langle r|_{j,k}$$

$$L_{\text{decay}} = |d\rangle \langle r|$$

- Parallel execution of CZ gates leads to a remaining population in the Rydberg state as the gate is designed for serial use
- Remaining population quantifies the crosstalk: indicator of the fidelity of the state preparation.



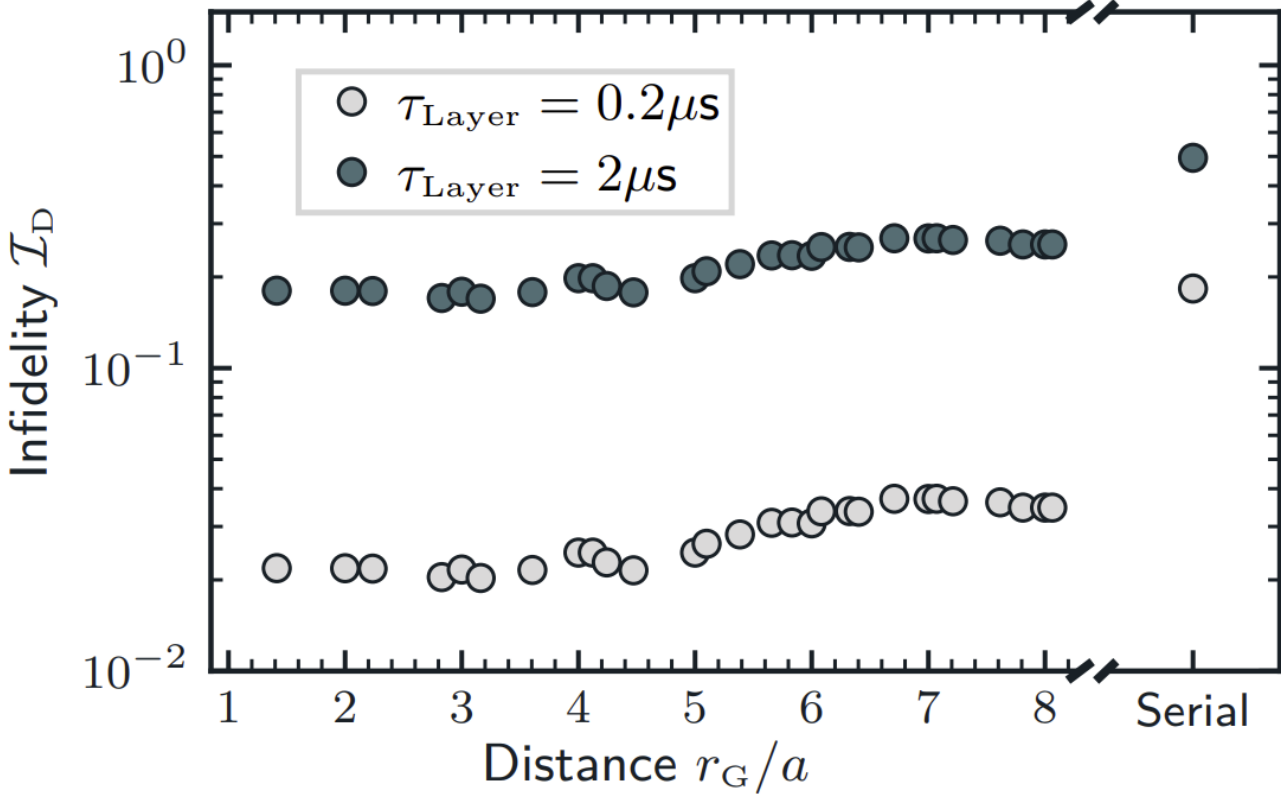
8x8

Dephasing 8x8

- Fluctuations around the magic trapping condition lead to decoherence
- Fidelity between GHZ of n qubits and perfect GHZ state

$$\mathcal{F}_D(t) = \frac{1}{2} + \frac{1}{2} \exp\left(-\frac{n \cdot t}{T_2}\right)$$

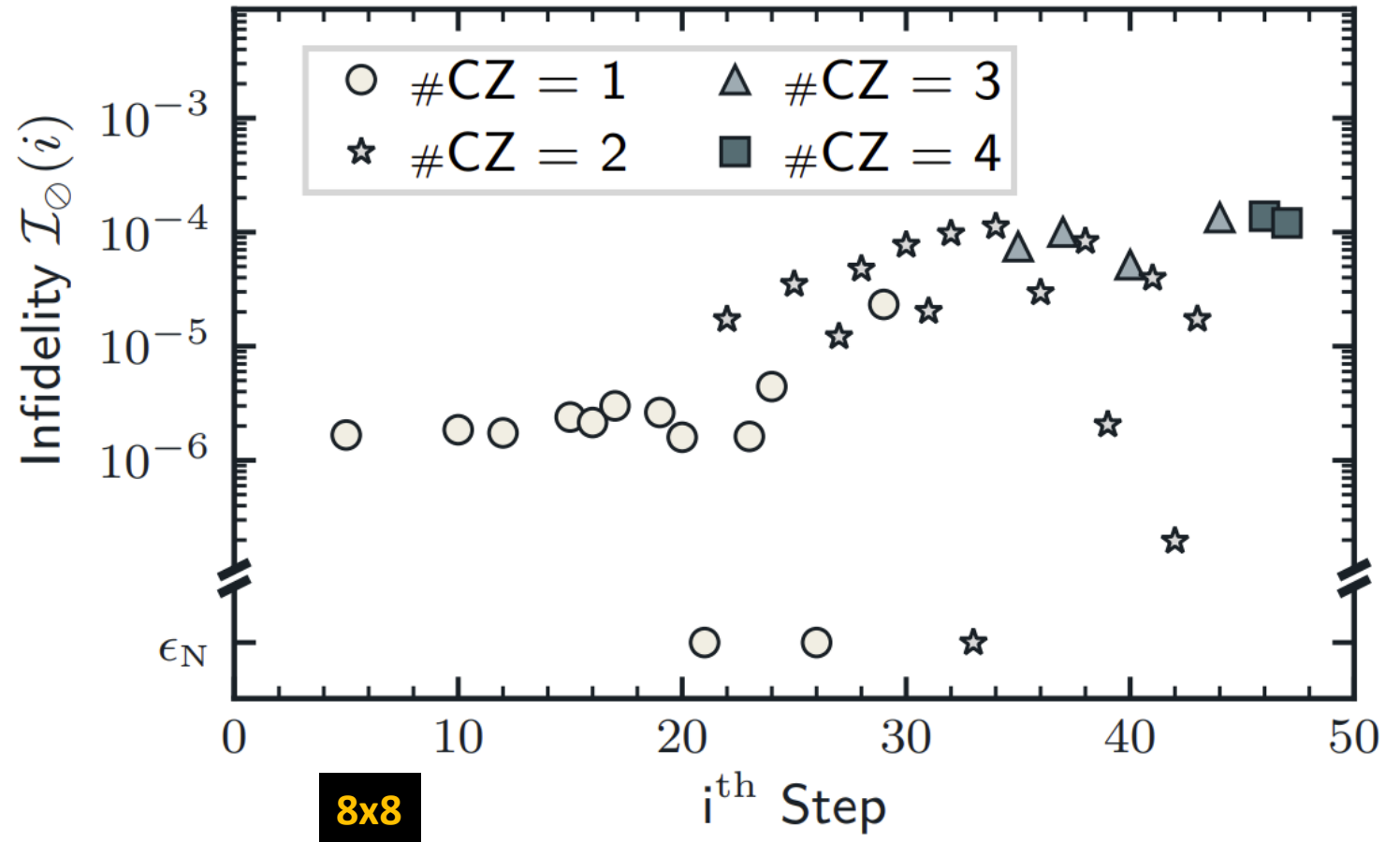
- Proves the need to parallelize the circuit



8x8

Average error per layer

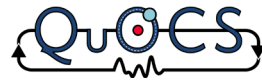
- Evaluate which layers contribute the most to the final infidelity



Design of high-fidelity controlled-phase gate

Reproduce protocol of Levine et al,
PRL 123, 170503 (2019) for Rubidium

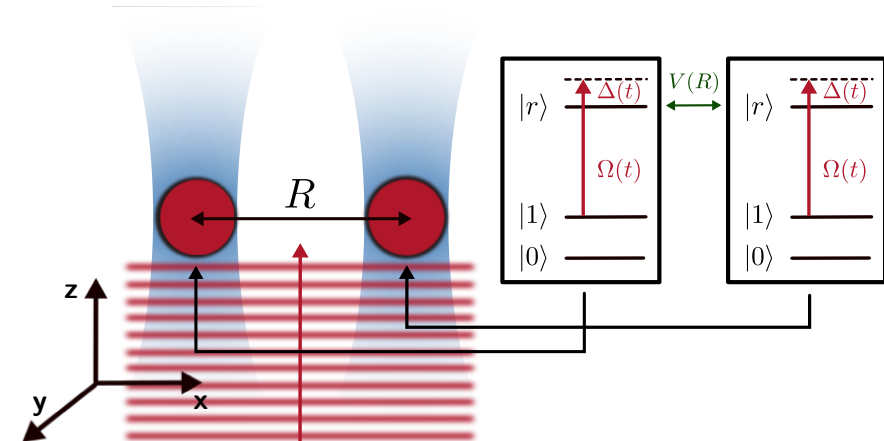
Can we go faster with optimal control?



- ✓ Reduce the time spent in the Rydberg state with time-dependent detuning pulses.
- ✓ Identify largest sources of errors for a realistic Rydberg setup.

Error budgeting for a controlled-phase gate with strontium-88 Rydberg atoms

Alice Pagano¹, Sebastian Weber², Daniel Jaschke^{1,3}, Tilman Pfau⁴, Florian Meinert⁴,
Simone Montangero^{1,3,5} and Hans Peter Büchler²



R = atom's distance

$\Omega(t)$ = Rabi frequency

$\Delta(t)$ = Detuning

V = Rydberg interaction

\mathcal{T} = Duration laser beam

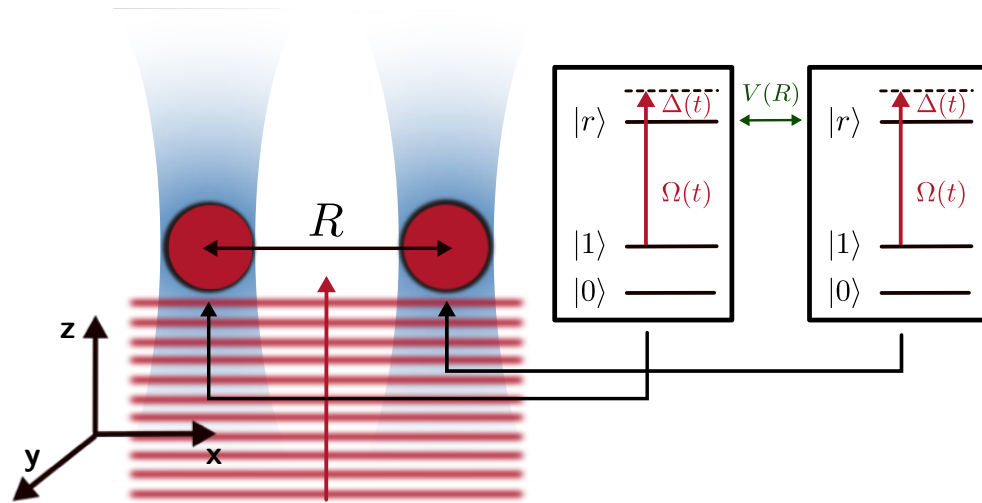
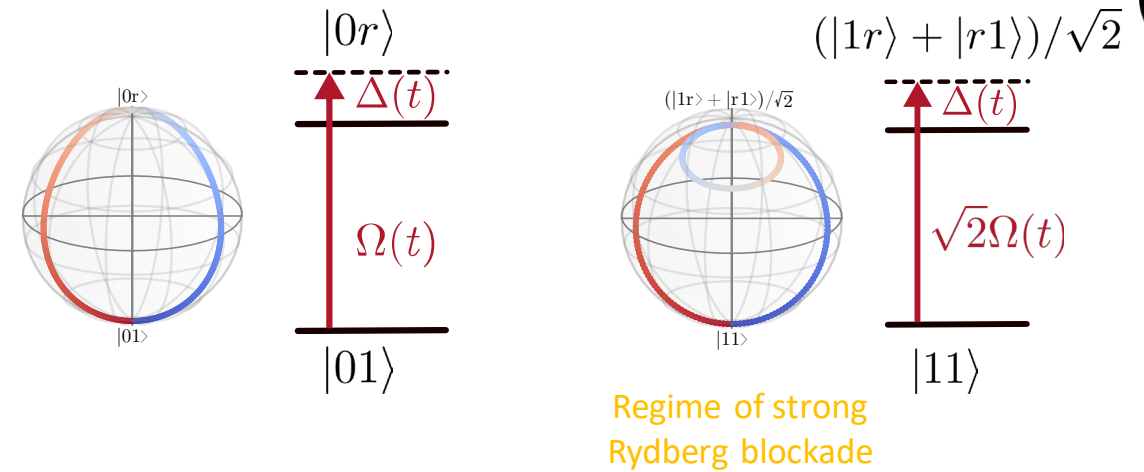
$$H = H_0 + H_{\text{int}}$$

$$H_0 = \hbar \sum_{i=1}^2 \left[\frac{\Omega(t)}{2} (\sigma_i^+ + \sigma_i^-) - \Delta(t) n_i \right]$$

$$H_{\text{int}} = V n_1 n_2$$

$$\sigma_i^+ = |r\rangle\langle 1|_i \quad \sigma_i^- = |1\rangle\langle r|_i \quad n_i = |r\rangle\langle r|_i$$

Design of high-fidelity controlled-phase gate



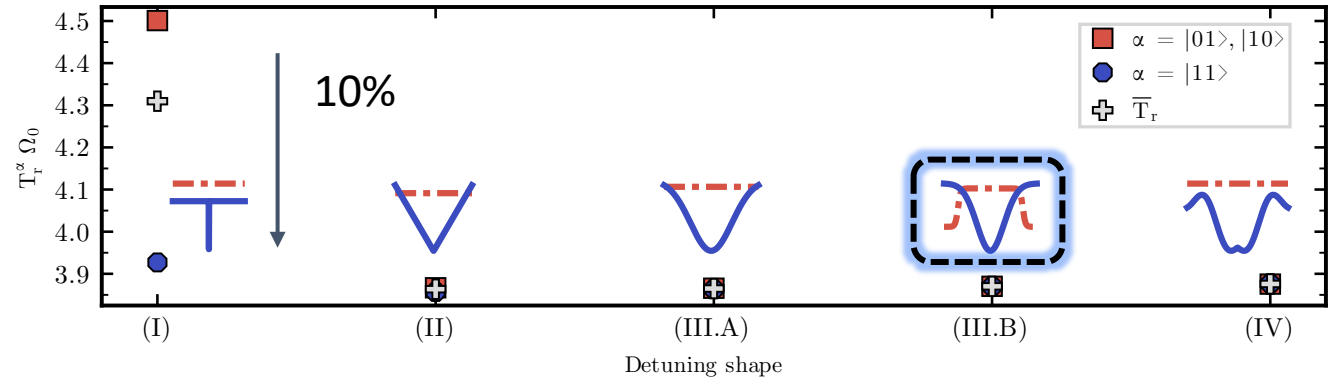
Basis states behavior for controlled-phase gate:

$$\begin{aligned}
 |00\rangle &\rightarrow |00\rangle \\
 |01\rangle &\rightarrow |01\rangle e^{i\phi_{01}} \\
 |10\rangle &\rightarrow |10\rangle e^{i\phi_{10}} \\
 |11\rangle &\rightarrow |11\rangle e^{i\phi_{11}}
 \end{aligned}
 \quad \text{Symmetry:} \quad \phi_{10} \equiv \phi_{01}$$

Condition: $\phi_{11} - \phi_{01} - \phi_{10} = (2n + 1)\pi$

$n \in \mathbb{Z}$

Design of high-fidelity controlled-phase gate



Time in $|r\rangle$ is reduced by 10% w.r.t. the protocol (I)

