

Technology Transfer

on high rate μ -RWELL

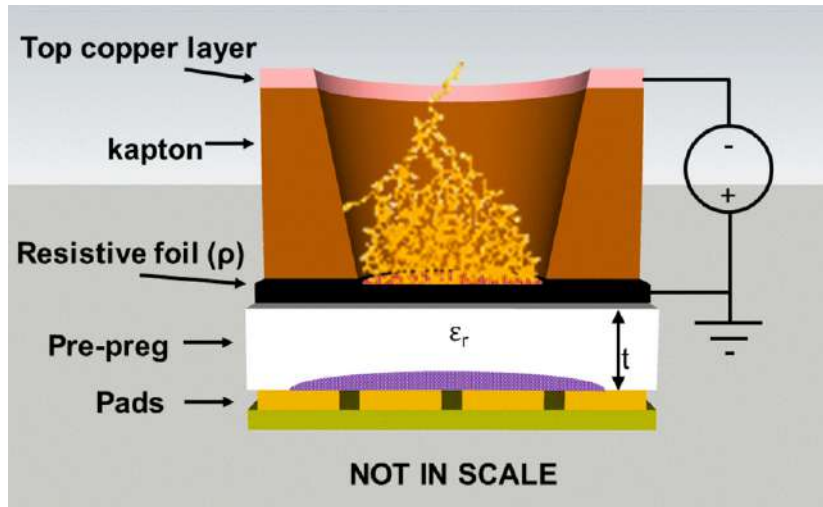
Matteo Giovannetti [LNF-INFN]

on behalf of
LNF-INFN (leading group)
Bologna-Ferrara INFN teams
R. De Oliveira - CERN-EP-DT-MPT Workshop
R. Pinamonti, M. Pinamonti - ELTOS S.p.A.

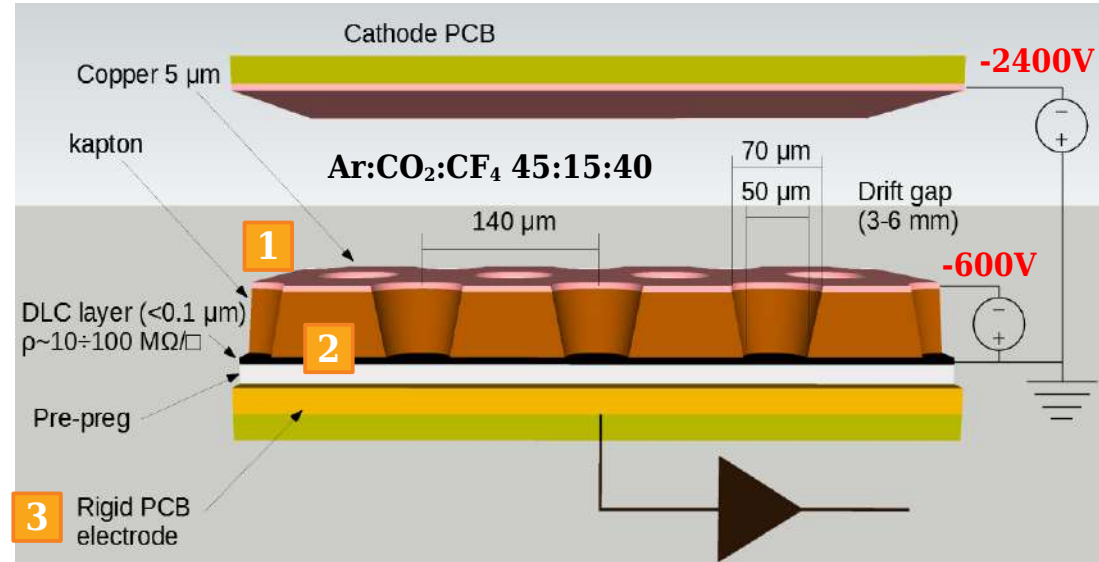


The μ -RWELL detector (reminder)

The μ -RWELL is a Micro Pattern Gaseous Detector (MPGD) composed of only two elements: the μ -RWELL_PCB and the cathode. **The core is the μ -RWELL_PCB**, realized by coupling three different elements:



Applying a suitable voltage between the **top Cu-layer** and the **DLC** the WELL acts as a **multiplication channel for the ionization** produced in the conversion/drift gas gap.



- 1 a WELL patterned kapton foil acting as **amplification stage** (GEM-like)
- 2 a **resistive DLC layer (Diamond-Like-Carbon)** for discharge suppression with surface resistivity $\sim 50 \div 100 \text{ M}\Omega/\square$
- 3 a standard readout PCB

Task objectives

DELIVERABLE - m30

- **D7.3:** μ -RWELL prototypes co-produced by industry under the guidance and supervision of the research team. A complete report will be provided (Task 7.3)

MILESTONE - m36

- **MS27:** build a $0.3 \times 0.3 \text{ m}^2$ prototype and the readout plane, with the new structure

Operative Meetings

21st Sept. 2021 - joint INFN-ELTOS-CERN meeting

- standardizing manufacturing procedures of μ -RWELL layout

1st-3rd Dec. 2021 - CERN-INFN meeting

- status of the R&D on the High Rate layout
- 2D layout based on the readout of a segmented amplification stage

7th-10th Dec. 2021 - 1st test batch in ELTOS

- DLC patterning
- PCB planarizing tests

7th-8th Mar. 2022 - 2nd test batch in ELTOS

- PCB planarizing tests
- Kapton DLCed foil coupling with PCB-readout

28th-31th Mar. 2022

1st AIDAInnova
Annual meeting

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31st Oct. 2022 - joint INFN-CERN meeting

- Discussion with Rui about the results obtained
- Planning 2023 production

5th Dec. 2022 - joint INFN-ELTOS meeting

- Planning 2023 production

20th-22th Mar. 2023 - 1st production batch in ELTOS

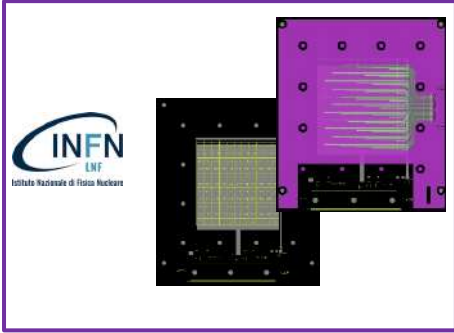
- DLC patterning
- PCB palnarizing (pregreg thickness scan)

24th-27th Apr. 2023

2nd AIDAInnova
Annual meeting

WP7.3.2: Technology Transfer (flow chart)

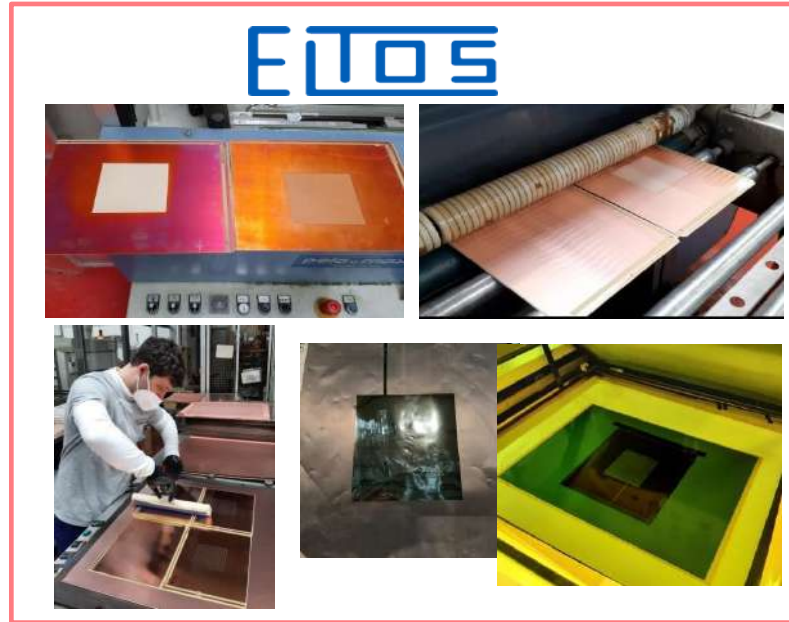
LAYOUT design



DCL foil production (*)



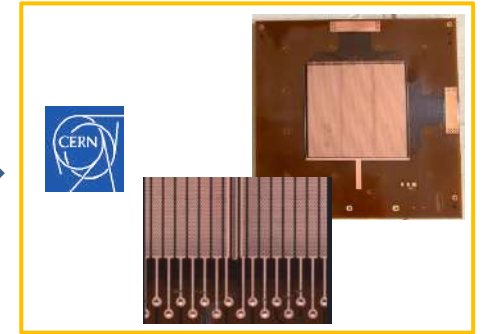
PCB production



Feedback from tests



Final detector manufacturing



*DLC Magnetron Sputtering machine
co-funded by INFN- CSN1

WP7.3.2: Technology Transfer 2022



Step 0 - Detector PCB design @ **LNF**

Step 1 - CERN_INFN DLC sputtering machine @ **CERN (+INFN)**

- delivery foreseen by the end of Oct. 2022
- **INFN** crew tbd & trained

Step 2- Producing readout PCB by **ELTOS**

- pad/strip readout

Step 3 - DLC patterning by **CERN**

- photo-resist → patterning with BRUSHING-machine

Step 4 - DLC foil gluing on PCB by **CERN**

- double 106-prepreg → 2x50 μ m thick
- PCB planarizing w/ screen printed epoxy → single 106-prepreg

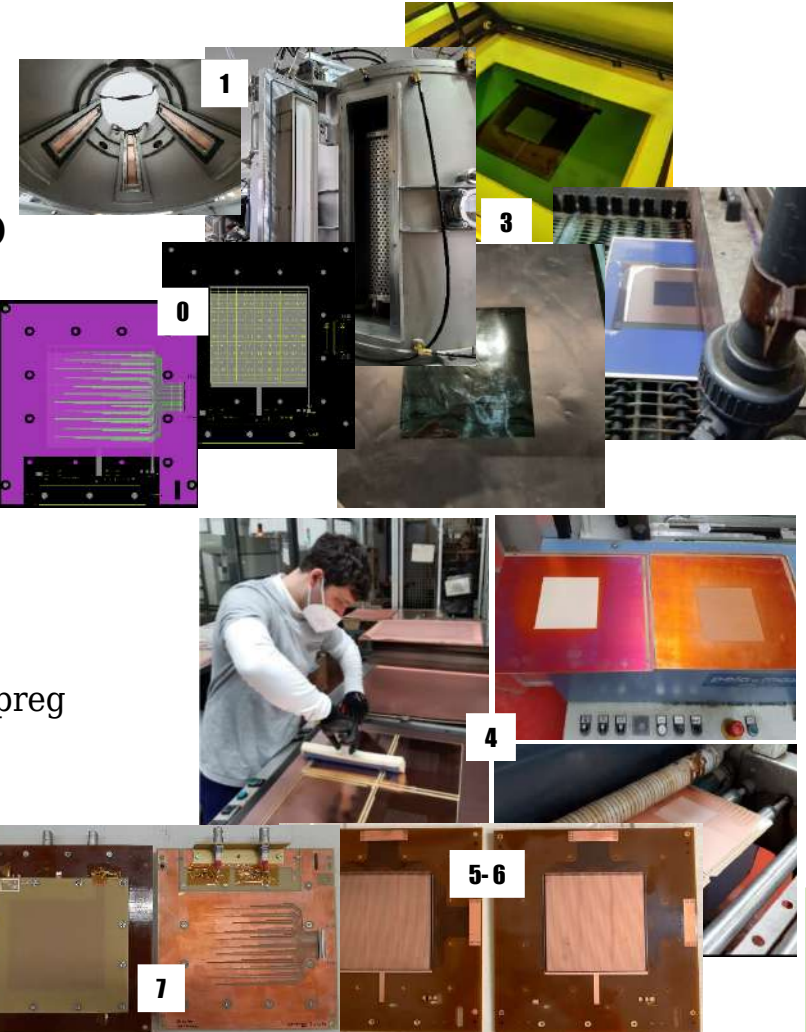
Step 5 - Top copper patterning by **CERN** (in future by **ELTOS**)

- Holes image and HV connections by Cu etching

Step 6 - Amplification stage patterning by **CERN**

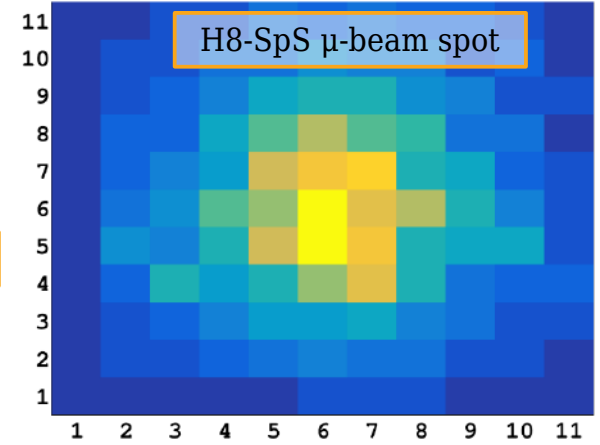
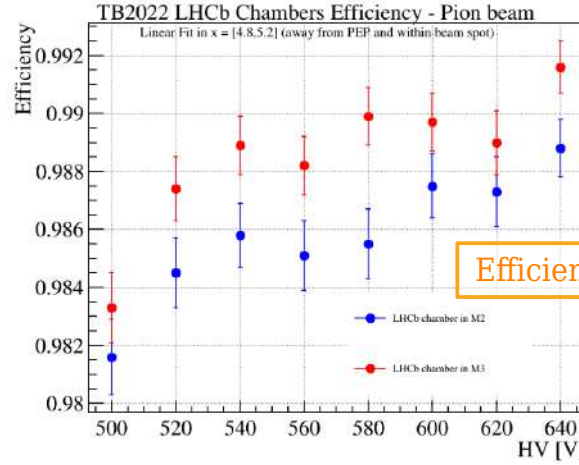
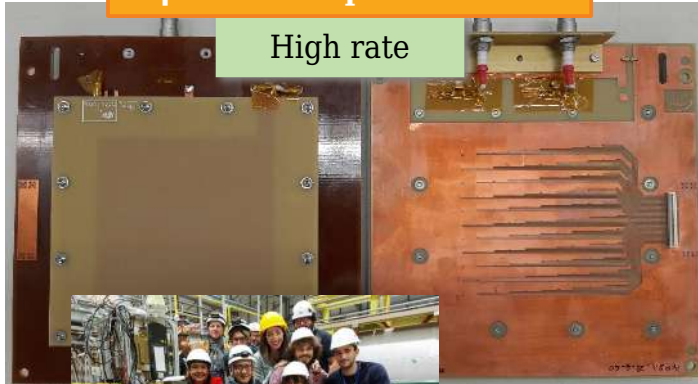
- PI etching → plating → ampl-holes

Step 7 - Electrical cleaning and detector closing @ **CERN**

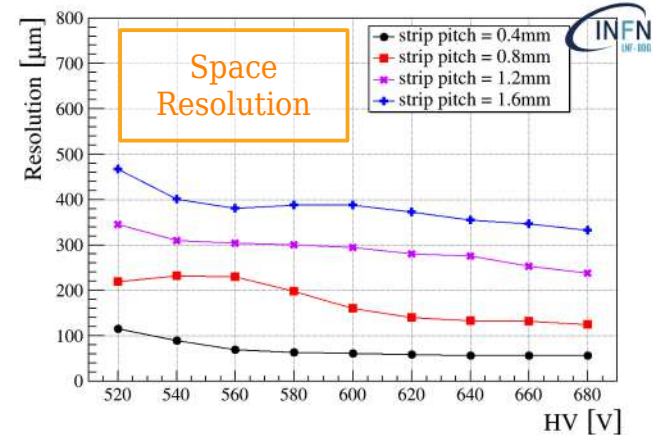
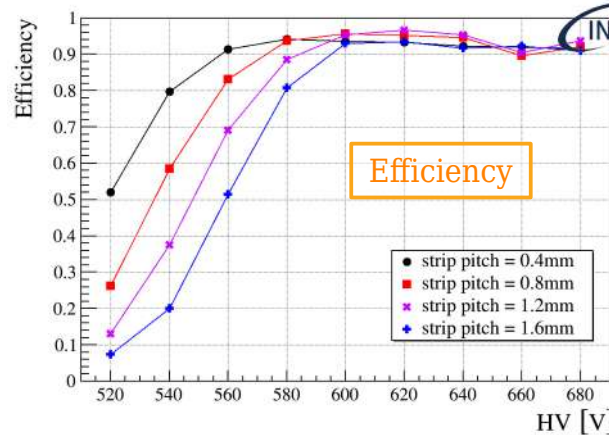


WP7.3.2: μ -RWELL layouts co-built by ELTOS & CERN (Oct.'22)

μ -RWELL - pad readout



μ -RWELL - 1D readout



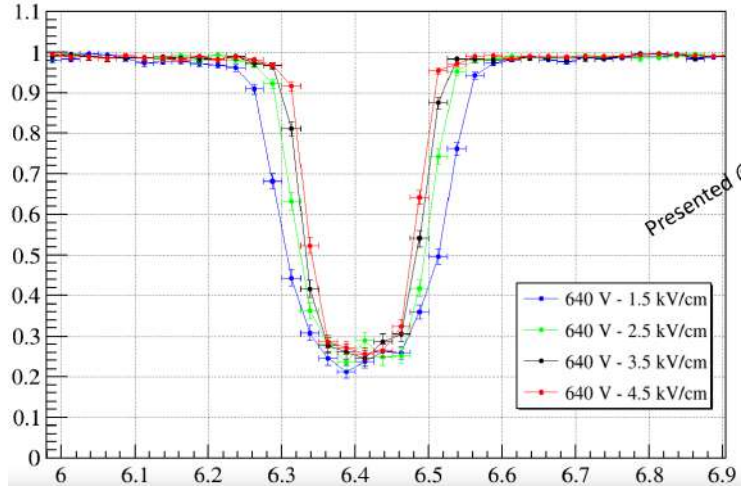
WP7.3.2: (June '23) PEP optimization

2022

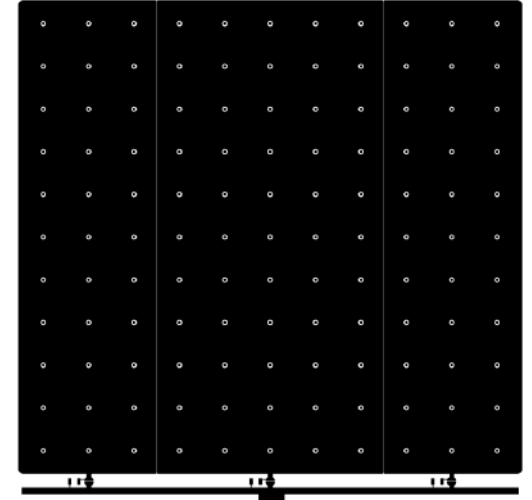
PEP μ -RWELL:
DLC grounding by
conductive groove

Pad R/O = $9 \times 9 \text{cm}^2$
Grounding:
- pitch = 9mm
- width = 1.5mm

→ 84% geometric
acceptance



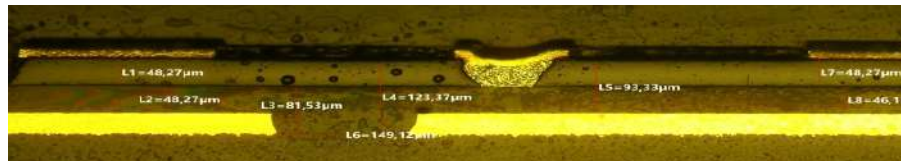
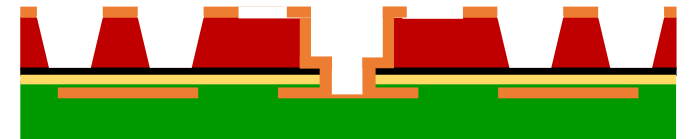
2023



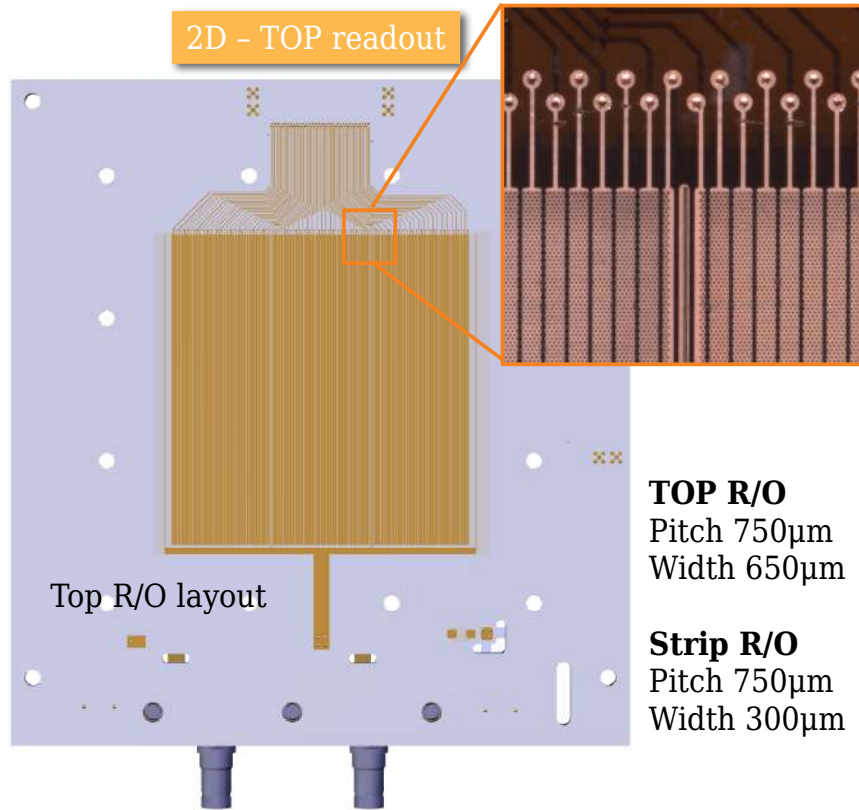
PEP-dot μ -RWELL:

DLC grounding by conductive dot
connected to the readout

Dot rim = 1.6mm | Pad R/O = $9 \times 9 \text{cm}^2$
→ 97% geometric acceptance



WP7.3.2: (June '23) 2D Read Out



2D - TOP readout

Top R/O layout

TOP R/O
Pitch 750 μ m
Width 650 μ m

Strip R/O
Pitch 750 μ m
Width 300 μ m

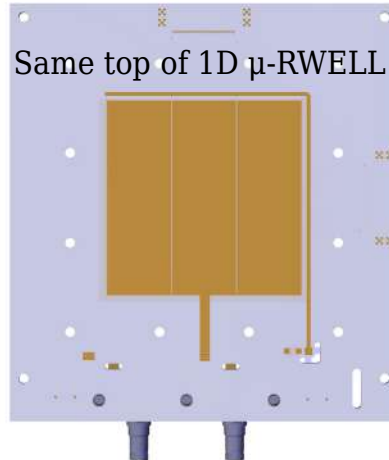
No PEP in the active area
Kapton foil between DLC and strip R/O:
stacking 25+12+25 μ m (pre-preg/kapton/pre-preg)
 $\rho_s = 20 \text{ M}\Omega/\square$

2D - capacitive sharing

Inspired by another μ -RWELL R&D:
K. Gnanvo et al., NIM A
1047 (2023) 167782

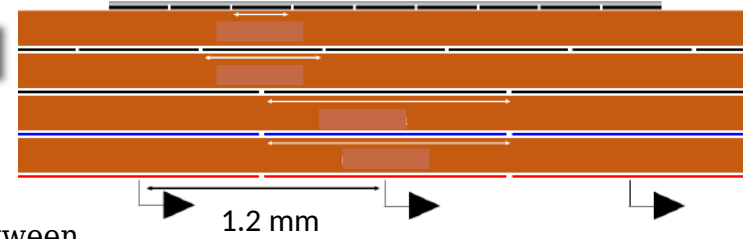
Capacitive coupling between
TOP and R/O:
3 layers of pads (L=0.3/0.6/1.2mm)

No PEP in the active area
 $\rho_s = 60 \text{ M}\Omega/\square$



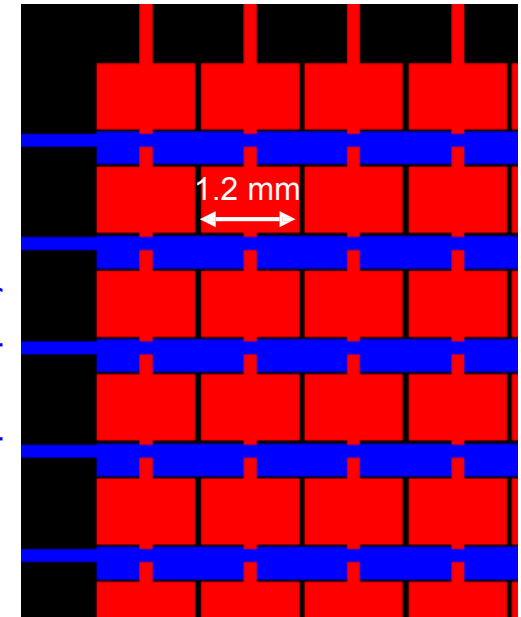
Same top of 1D μ -RWELL

Trasversal view



1.2 mm

Y-strips - bottom layer



1.2 mm

X-strips - top layer

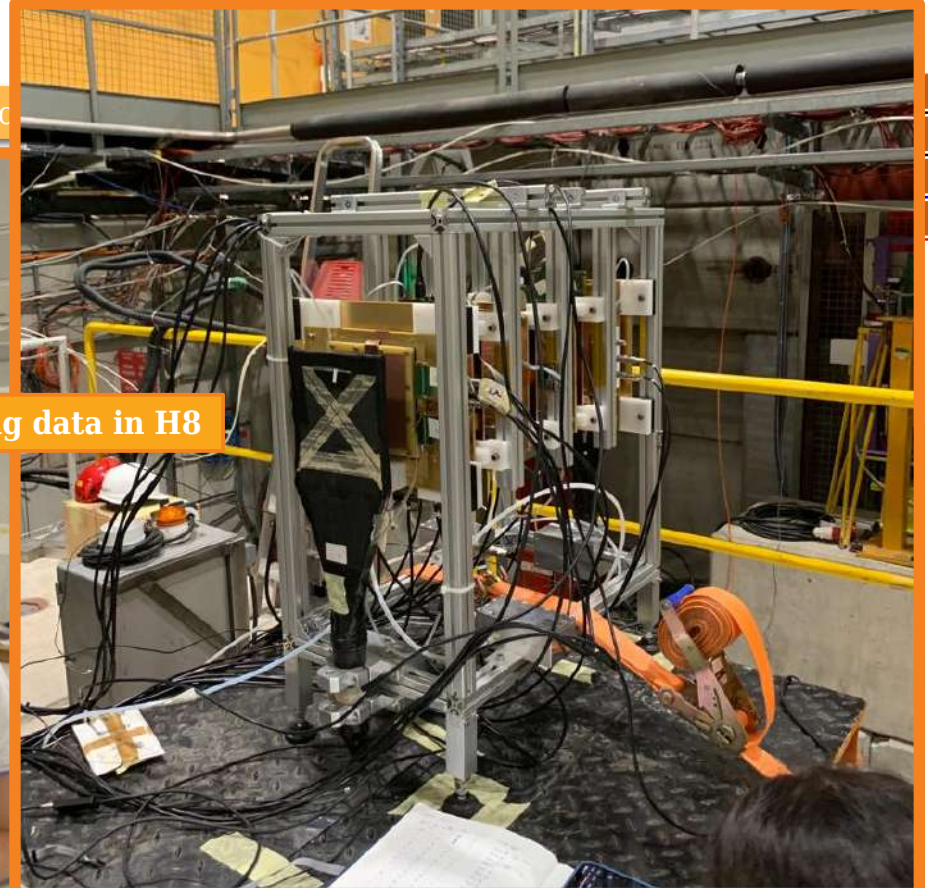
WP7.3.2: (June '23) 2D Read Out

2D - TOP readout

2D - capac



Right now taking data in H8



N
K
st
 $\rho_s = 20 \text{ MS}^2/\square$

X-S

WP7.3.2: Technology Transfer 2022



Step 0 - Detector PCB design @ **LNF**

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- **INFN** crew tbd & trained

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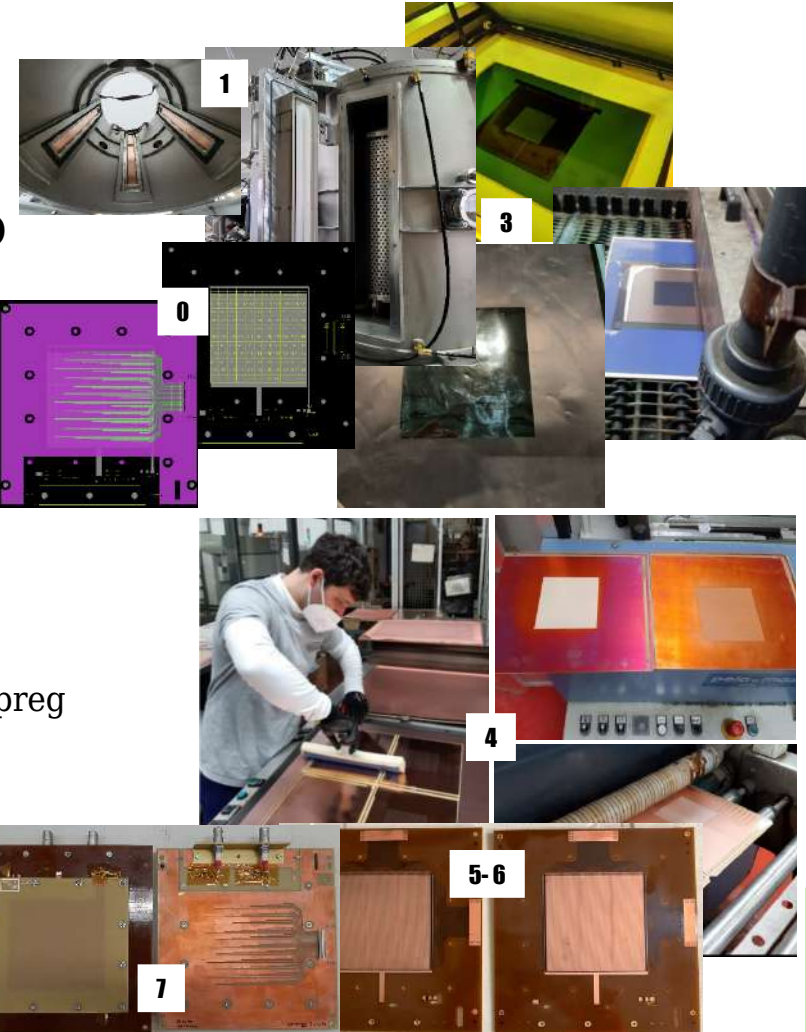
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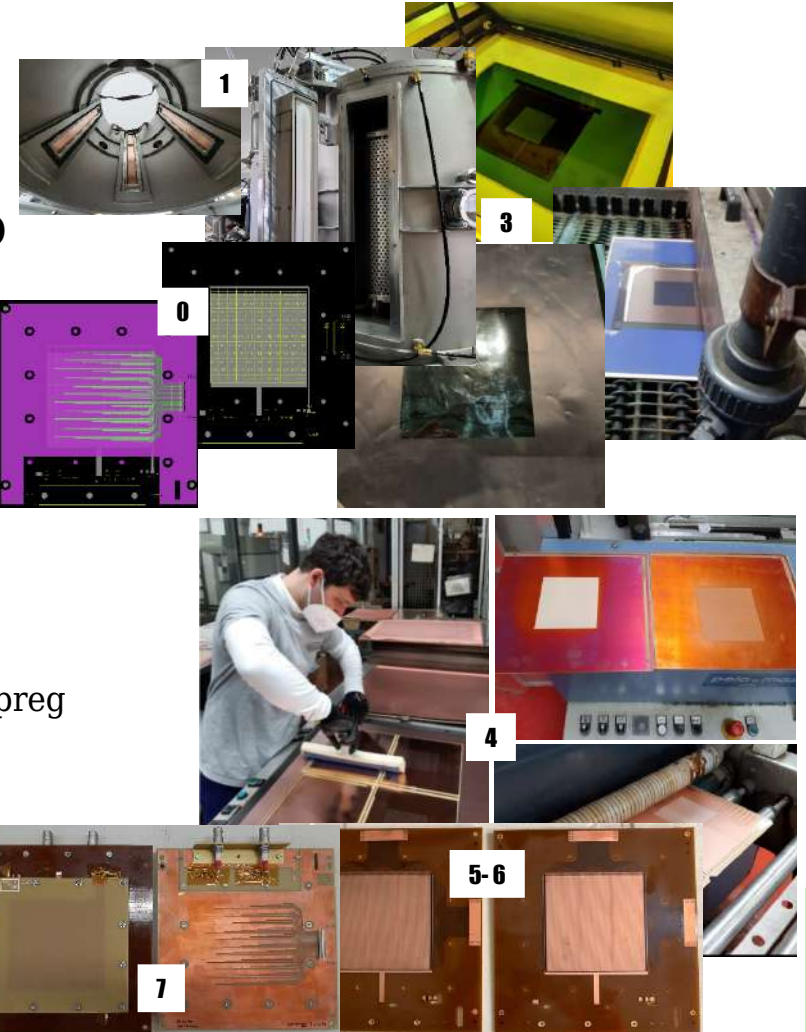
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WP7.3.2: update on the CERN-INFN DLC machine

31st Oct. 2022 - Delivered

31st Oct. - 4th Nov. 2022 - Commissioning & test training

21st - 23rd Nov. 2022 - First DLC sputtering test

- Ar + N₂ doping (increase sp³ bonds → resistivity)

19th - 28th Jun. 2023 - Second DLC sputtering test

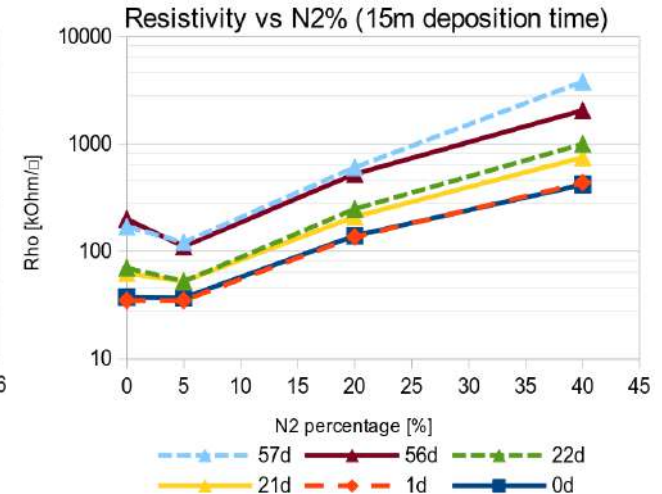
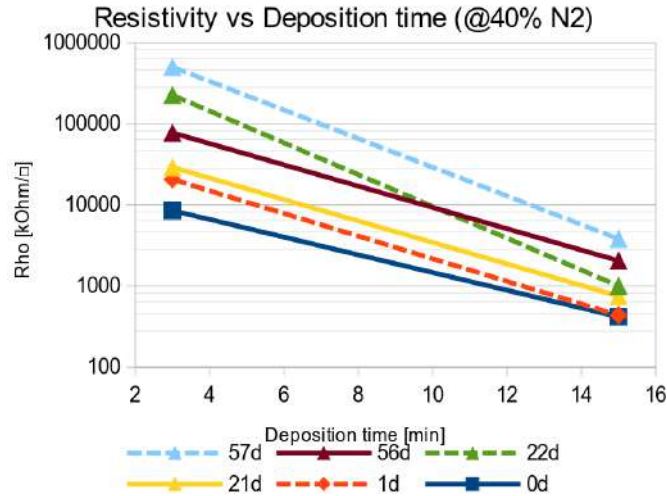
- Ar + C₂H₂ doping (study H presence in the plasma)

Some technical features:

- **Flexible** substrates up to 1.7m×0.6m
- **Rigid** substrates up to 0.2m×0.6m

Five cooled target holders, arranged as two pairs face to face and one on the front, equipped with five shutters.

The machine shall be able to **sputter or co-sputter different materials**, in order to create a coating layer by layer or an adjustable gradient in the coating.



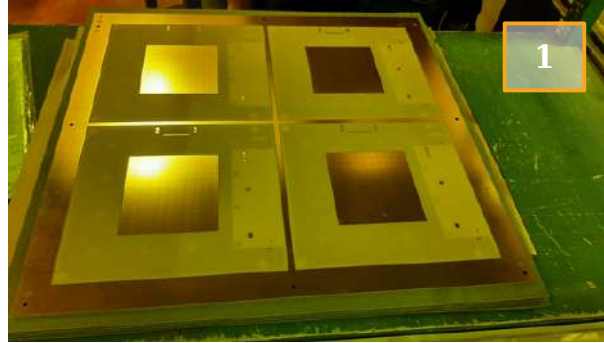
WP7.3.2: Mar.'23 ELTOS production – DLC patterning

Step 2:

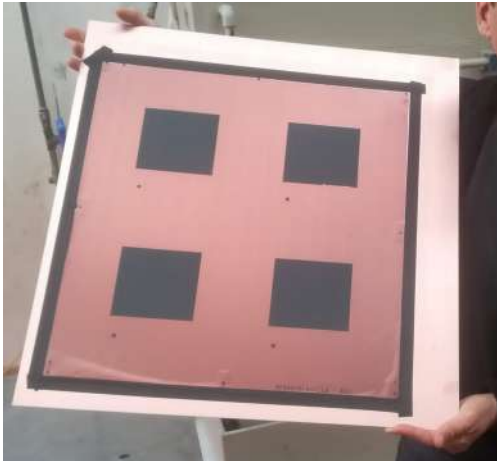
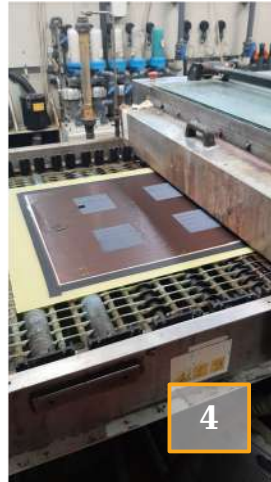
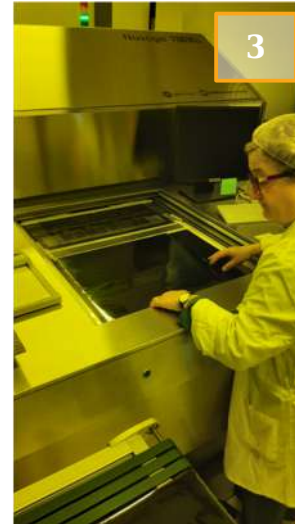
- 1) PCB production

Step 3:

- 2) Photoresist **lamination** for DLC protection
- 3) Photoresist **development**
- 4) **DLC patterning** with brushing machine
(@CERN different approach: JET-SCRUBBING)



DLC
Kapton
Cu



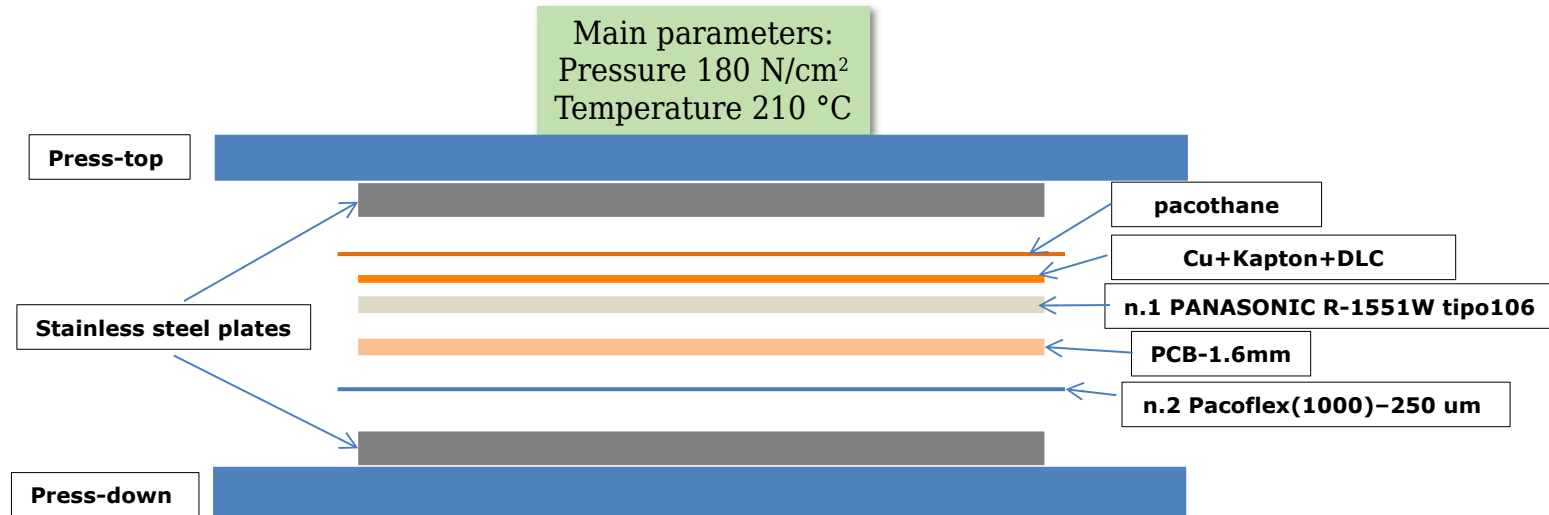
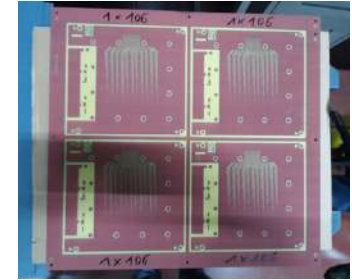
WP7.3.2: Mar.'23 ELTOS production – DLC-foil gluing

Step 4: Cu-Kapton-DLC gluing on PCB

Goal: 16 PEP-dot detectors ($9 \times 9 \text{mm}^2$ pad R/O), with **different pre-preg thickness:** systematic study of signal **pulse width as a function of coupling capacitance** between DLC and R/O pad.

Waiting for shipment to CERN and detector finalization (Step 5-7)

Pre-preg	Δx [μm]
106	50
1080	75
x2 106	100
x2 1080	150



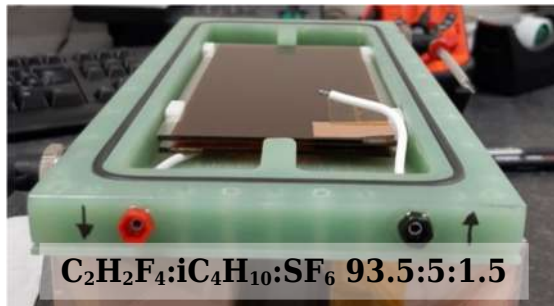
sRPC – an MPGD-tech based RPC



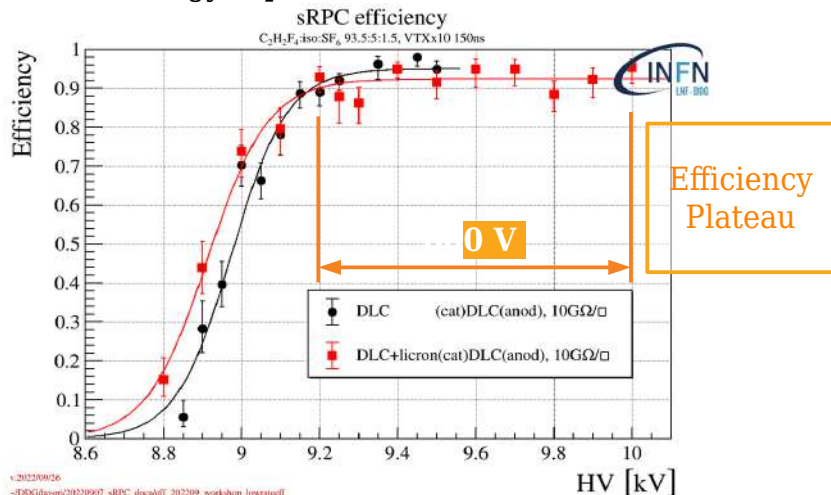
Glass 140×78 mm²
DLC 120×64 mm²

DLC-based RPC:

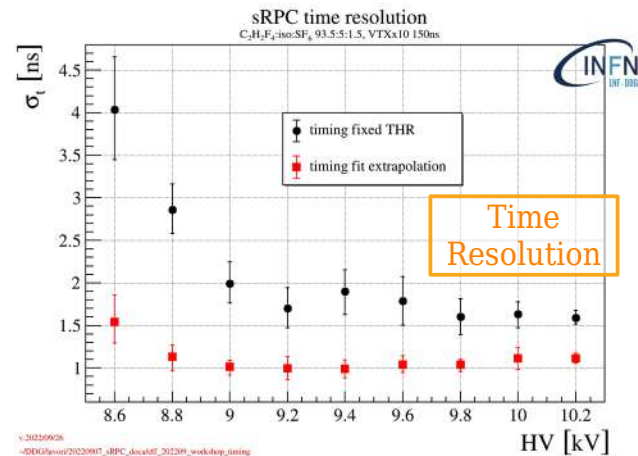
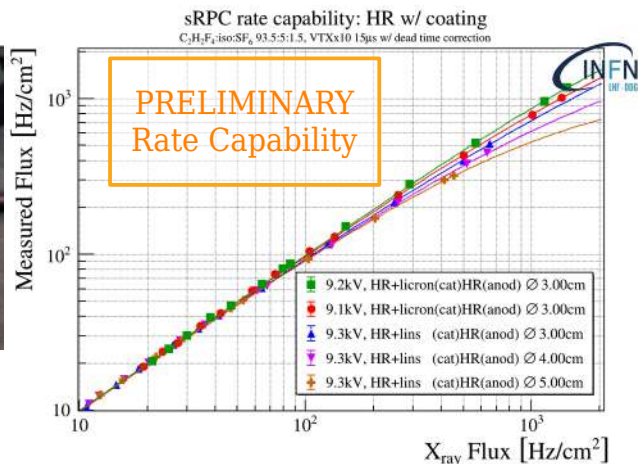
- **From bulk resistivity to surface resistivity:** easy tunable resistivity w.r.t. bakelite or glass
- μ -RWELL inspired **High Rate schemes**
- Flexible substrate



A promising novel technology, from MPGD material and technology experience.



v: 2022/09/26
~DDG/avm/20220907_sRPC_alca/6E_202209_workshop_lowrateoff



v: 2022/09/26
~DDG/avm/20220907_sRPC_alca/6E_202209_workshop_timing

WP7.3.2 – Summary

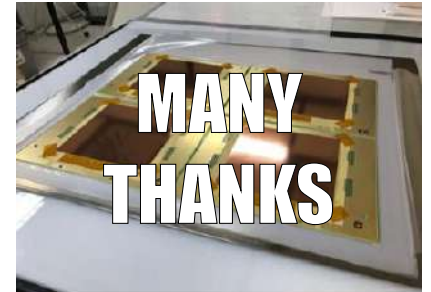
The activity of WP7.3.2 task proceeds on **two parallel paths**:

- 1) The **industrialization** of a part of the construction steps of the μ -RWELLS at the **ELTOS** company (Arezzo - IT), in close **collaboration** with the **CERN-EP-DT-MPT** Workshop:
 - ◆ Advances in the TT; **more construction steps** performed by ELTOS
 - ◆ Detector finalization (Kapton Etching, ...) performed at CERN
- 2) The R&D with CERN on the various μ -RWELL layouts is focusing on different items:
 - ◆ Design/optimization of the PEP layout → **PEP-dot** (97% geom. acceptance)
 - ◆ Design of two **2D strip R/O** (capacitive sharing and top strip patterned)
 - ◆ Prototype gain characterization with X-ray @LNF-INFN
 - ◆ Beam Test in **June'23 in H8-SpS** for all the new prototypes.

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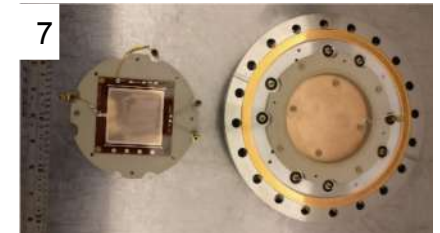
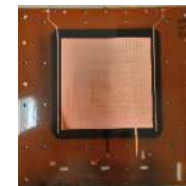
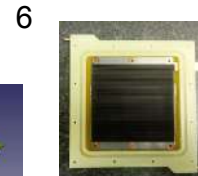
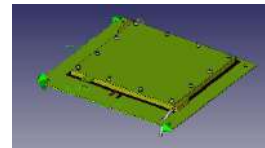
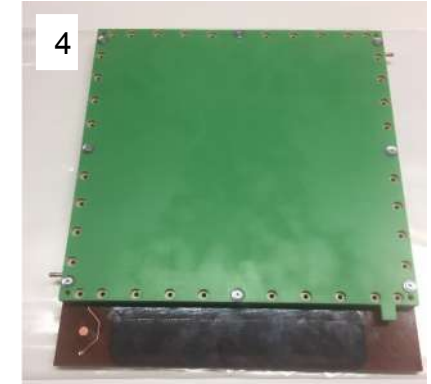
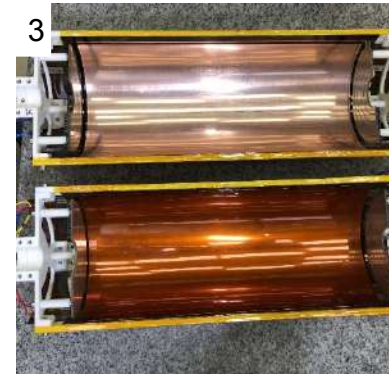
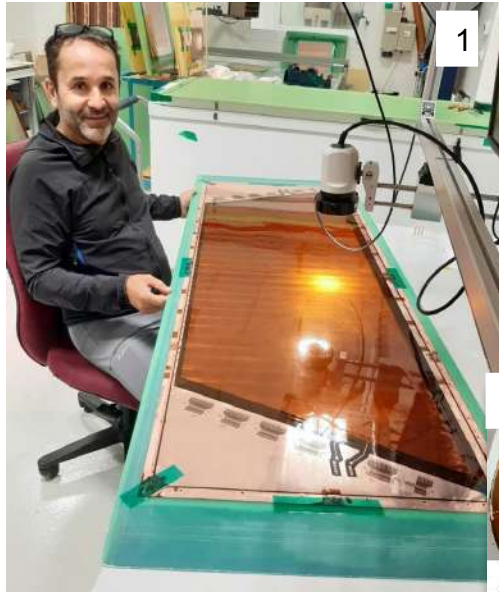




μ -RWELL technology spread

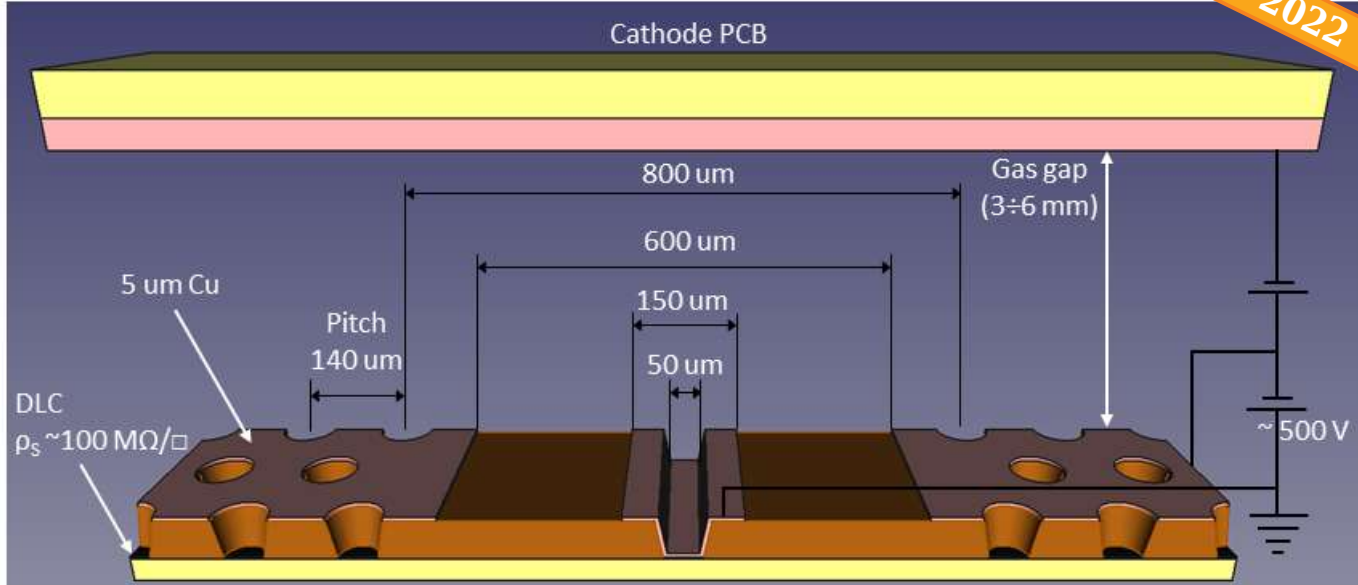
The μ -Resistive WELL is proposed in

1. **CLAS12 @ JLAB:** the upgrade of the muon spectrometer
2. **X17 @ n_TOF EAR2:** for the amplification stage of a TPC dedicated to the detection of the X17 boson
3. **TACTIC @ YORK Univ.:** radial TPC for detection of nuclear reactions with astrophysical significance
4. **Muon collider:** hadron calorimeter
5. **CMD3:** μ RWELL Disk for the upgrade of the tracking system
6. **URANIA-V:** a project funded by INFN-CSN5 for neutron detection,
7. **UKRI:** neutron detection with pressurized ^3He -based gas mixtures



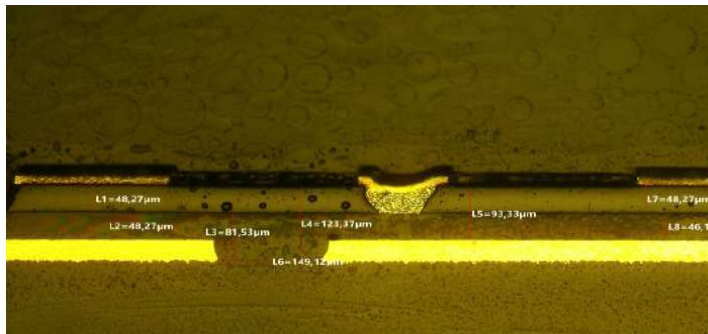
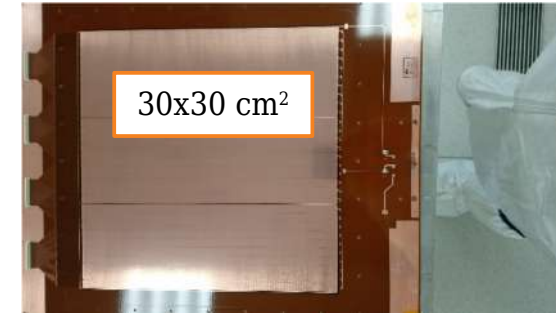
The PEP High Rate layout

2021-2022

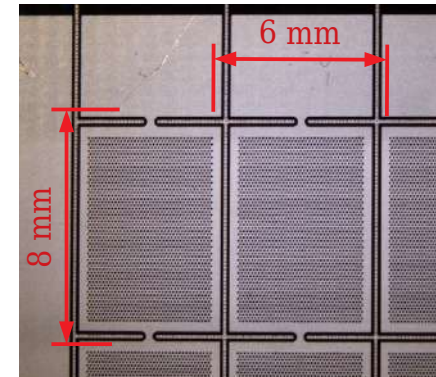


PEP: Patterning - Etching - Plating

- **Grounding from top** by kapton etching and plating
- **Scalable to large size**

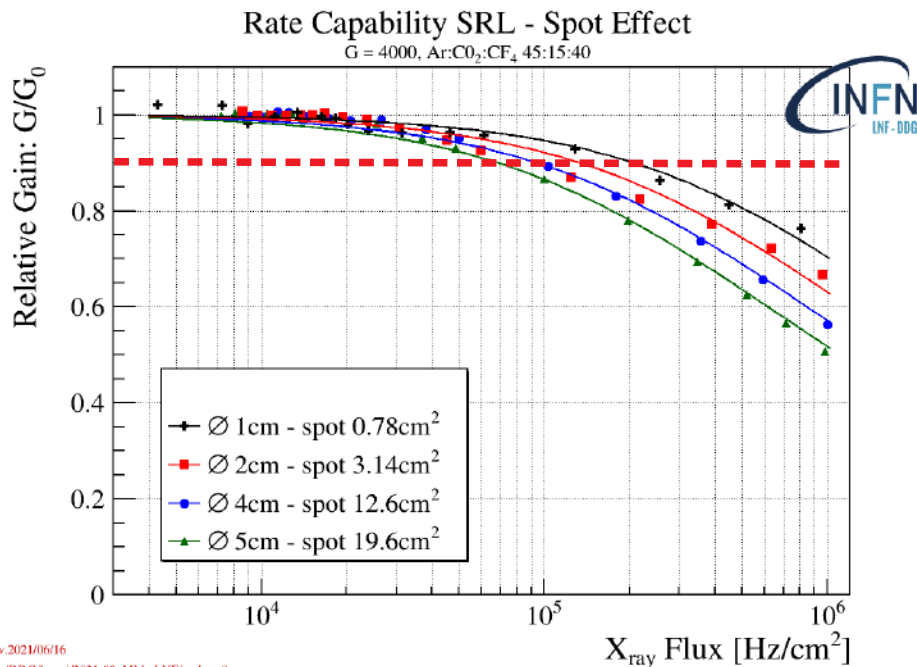


Unitary cell.
black - the exposed kapton is visible.



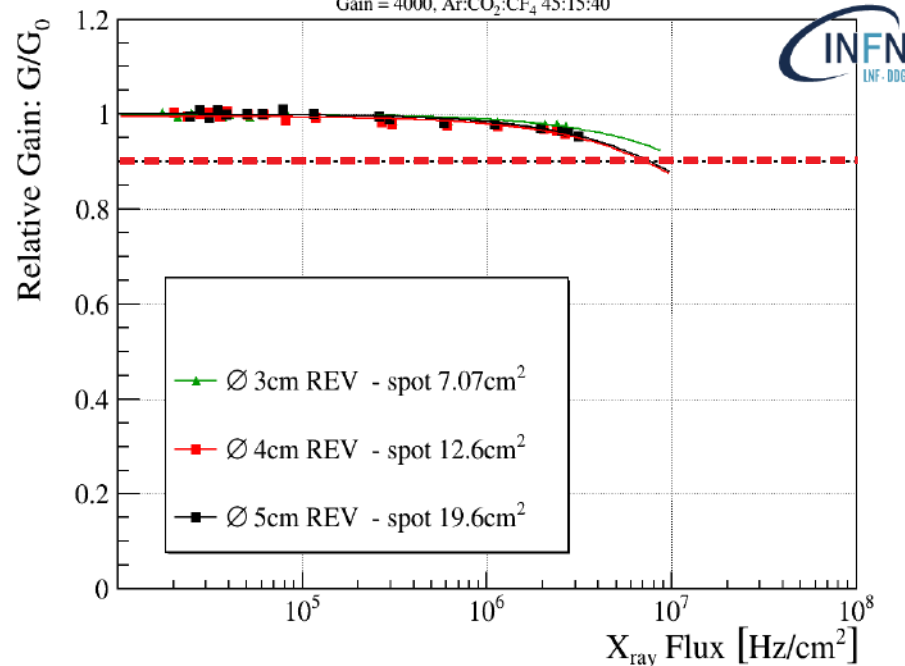
Rate capability – X-Ray measurements

Low Rate

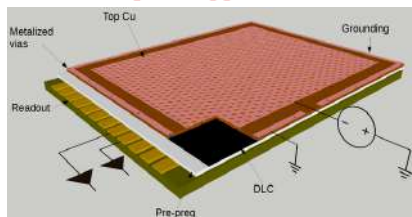


High Rate

Rate Capability PEP
Gain = 4000, Ar:CO₂:CF₄ 45:15:40

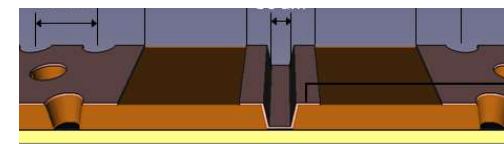


v.2021/06/16
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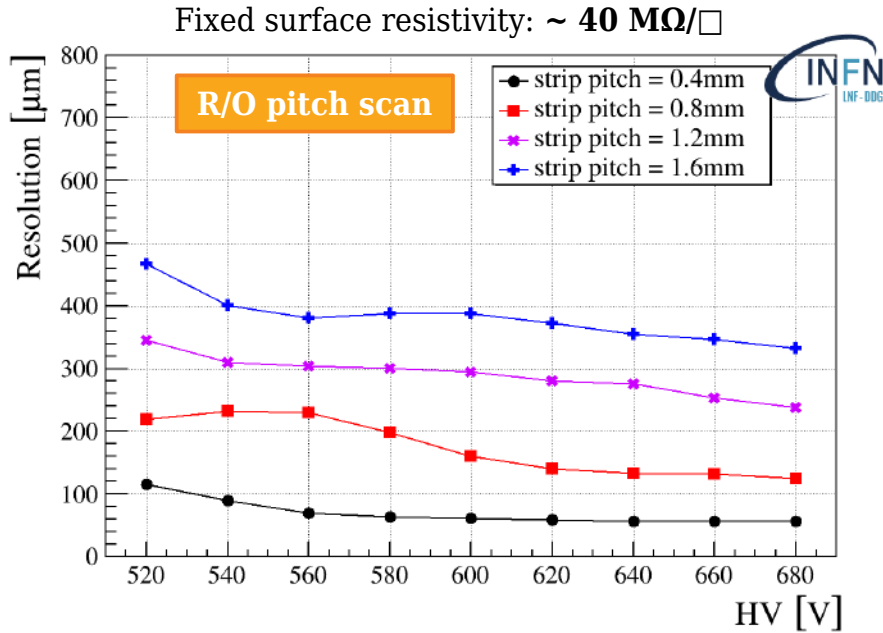
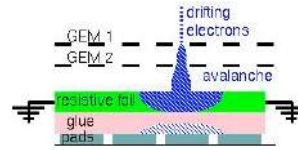


Rate capability compatible with m.i.p. as measured @PSI O(10MHz).

Different primary ionization ⇒ Rate Cap_{m.i.p.} = 3×Rate Cap_{X-ray}



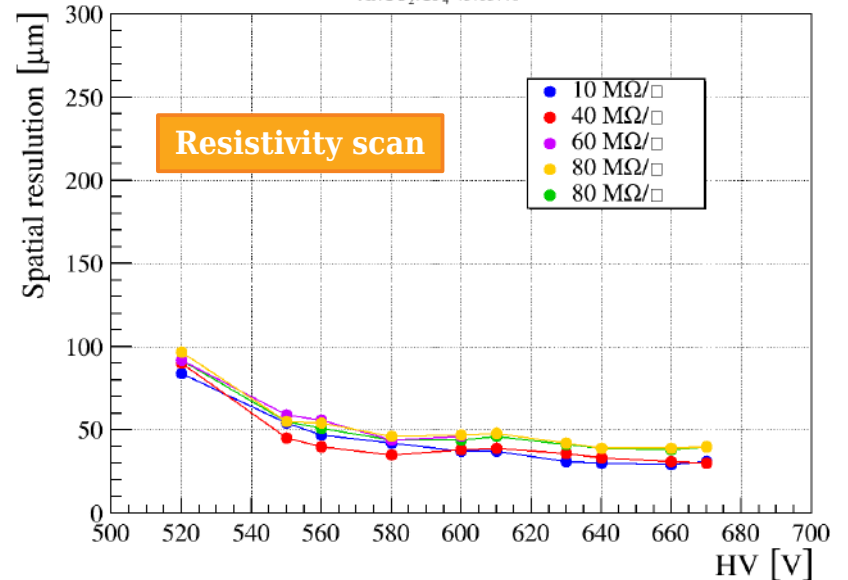
μ -RWELL: DLC charge spread



Need to **reduce** # FEE channels
 =
Increase the R/O pitch

As expected: reduction of the space resolution.

RD-FCC μ -RWELL, Residuals test resolution - 75ADC threshold
 Ar:CO₂:CF₄ 45:15:40

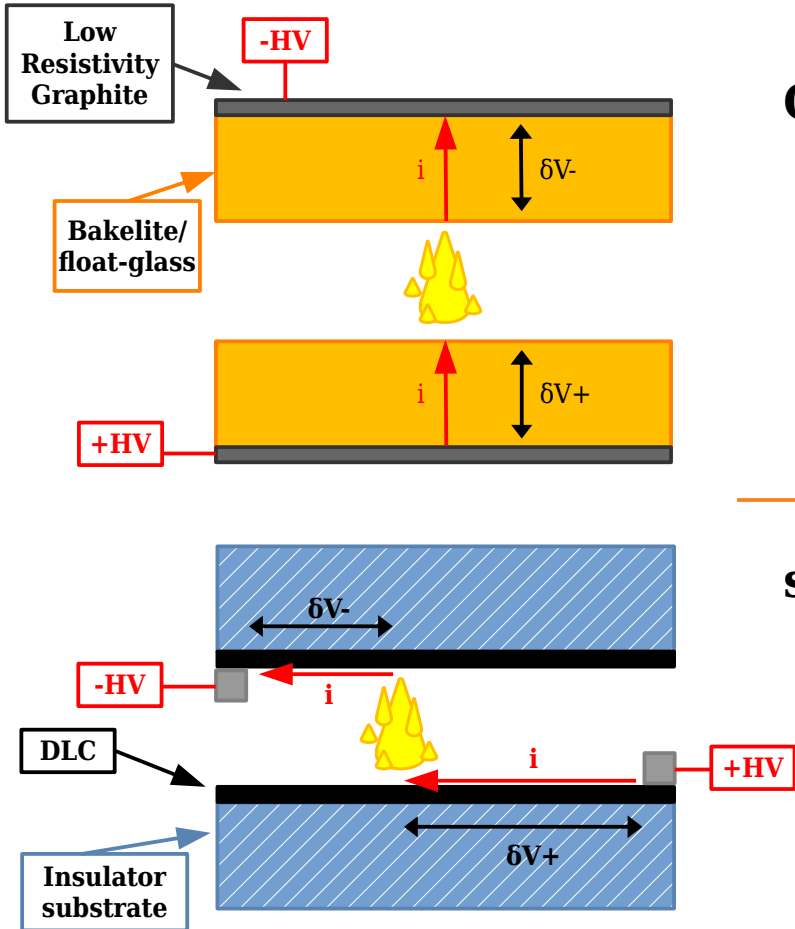


Fixed strip pitch (0.4 mm) and width (0.15 mm)

No effects in **this resistivity range**.

→ DLC resistivity uniformity is not a crucial parameter
 Near future: charge dispersion through different R/O architecture → capacitive sharing R/O

Bulk RPC vs Surface RPC



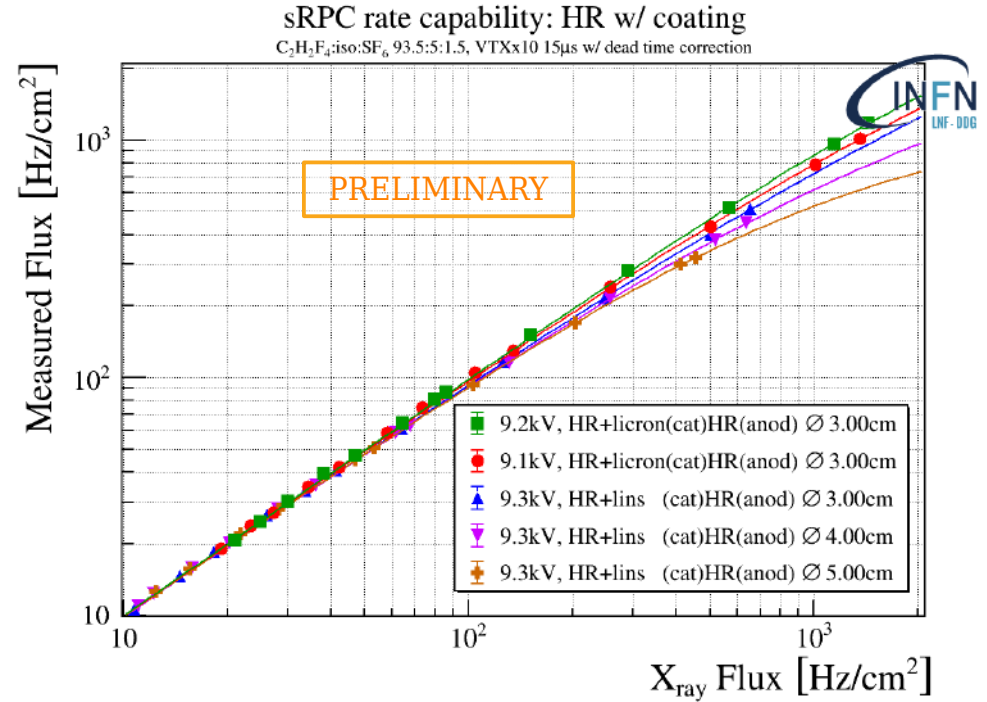
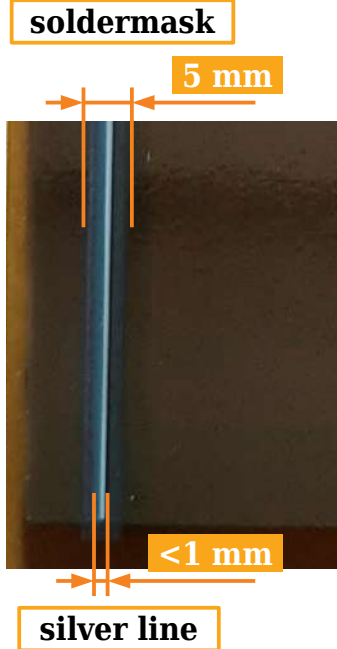
Classical RPCs

- Bulk resistivity electrodes (bakelite, float-glass, ...)
- Recovery time proportional to **volume resistivity, electrode thickness**
 - $\tau = \rho_v \epsilon_0 (\epsilon_r + 2d/g)$
 - Low volume resistivity and thin electrodes, together with the reduction of the gas gain (\oplus high gain low noise pre-amp) is the standard recipe to increase the detector rate capability.

sRPCs - surface RPC

- Surface resistivity electrodes manufactured with sputtering techniques of Diamond-like-carbon (DLC) on flexible supports
 - The technology allows to realise large electrodes with a DLC **surface resistivity** in a **very wide range: 10 M Ω / \square \div 10 G Ω / \square**
- High density current evacuation schemes, similar to those used for resistive MPGD (μ -RWELL and MicroMegs), can be implemented to improve the rate capability of the detector

High Rate layout – preliminary results



A **preliminary** measurement of the **rate capability** (defined as the radiation flux corresponding to an efficiency drop of 20%) of the high-rate layout has been **performed** by irradiating the detector **with a 5.9 keV X-ray gun** with a spot size comparable with the pitch of the conductive grid realized on the DLC ($\rho_{DLC} \approx 1.6 \text{ G}\Omega/\square$).

**Rate capability of
~1 kHz/cm²
with X-ray**