

# RD50-MPW3 noise simulation

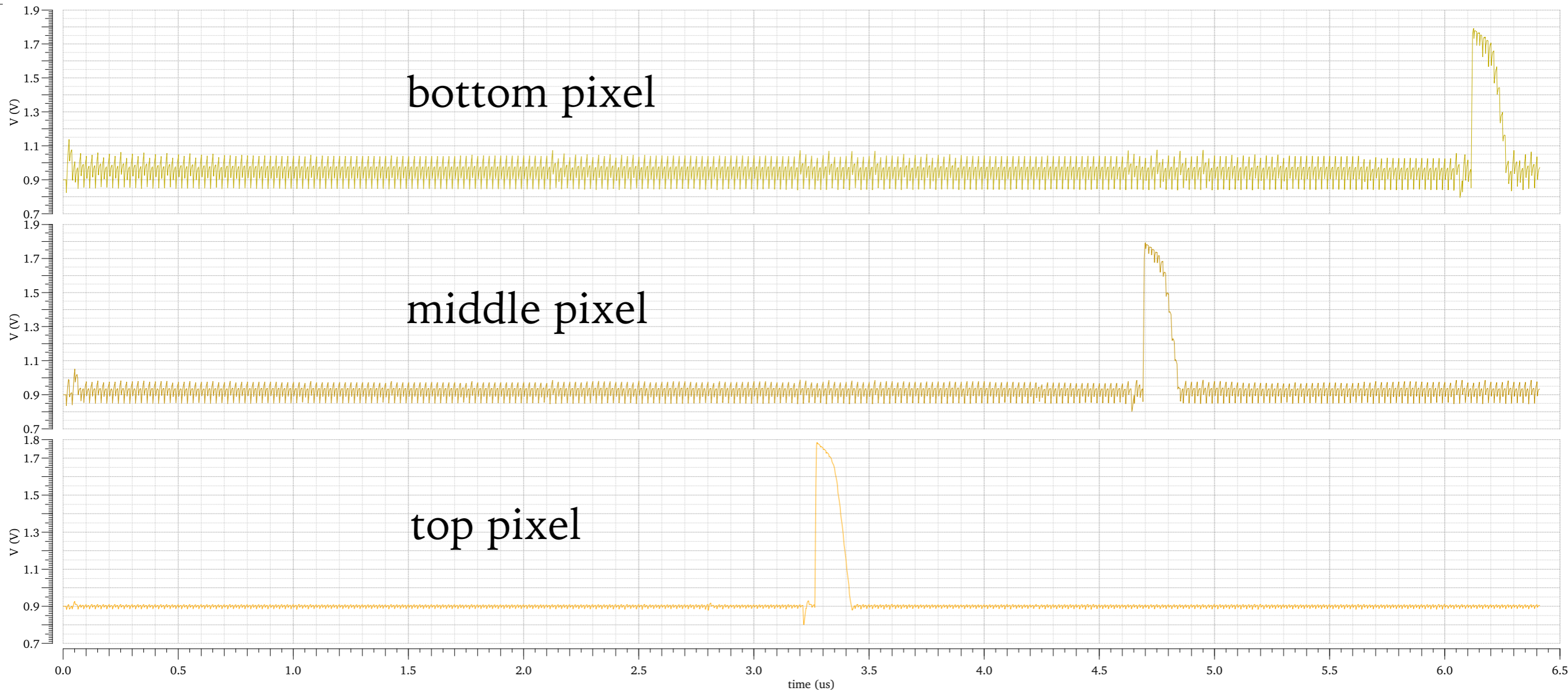
RD50 CMOS design meeting (2023.04.06)

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# HPOUT - Single digital gourd



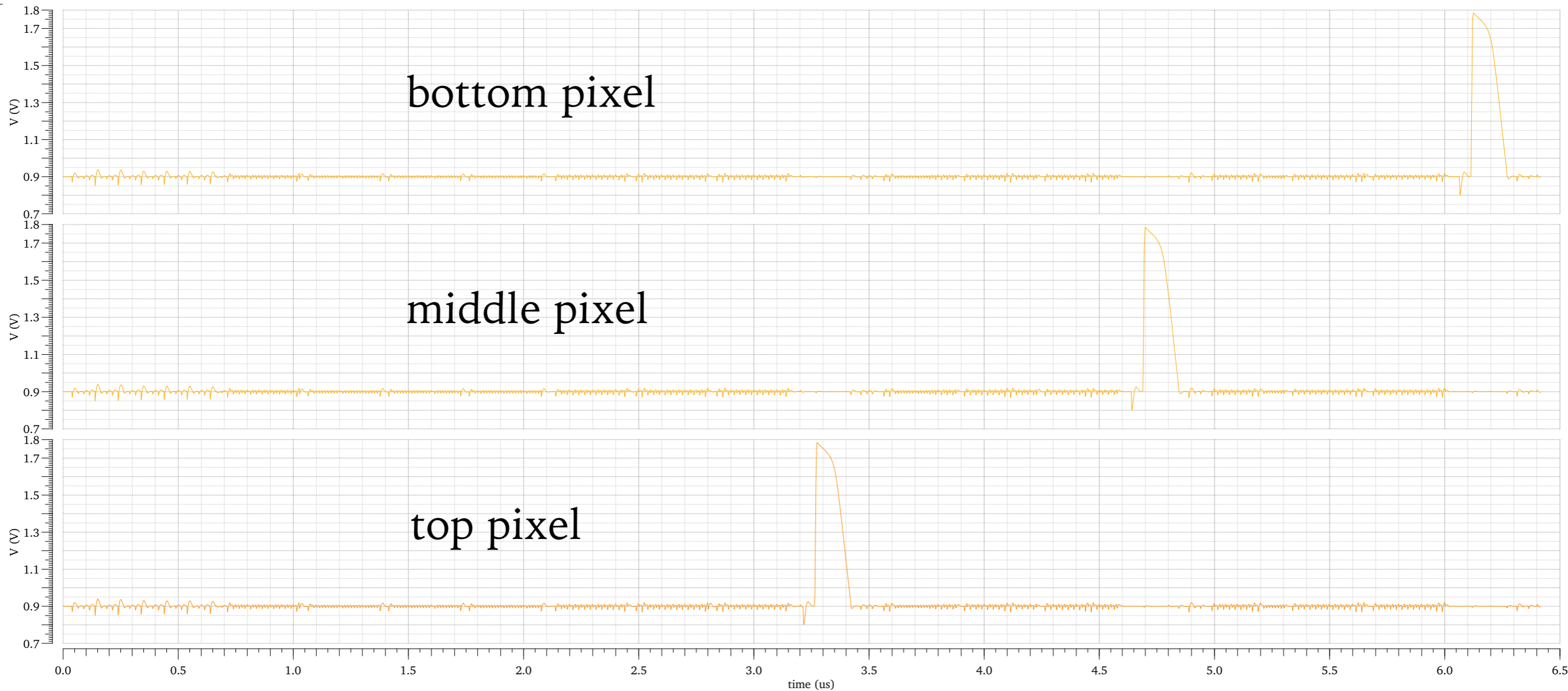
- Larger noise on the bottom pixel. (noise  $>$  100 mV)

# COMPOUT – Single digital gourd



- Noise cause false hits on bottom pixel.

# HPOUT - Separated digital grounds

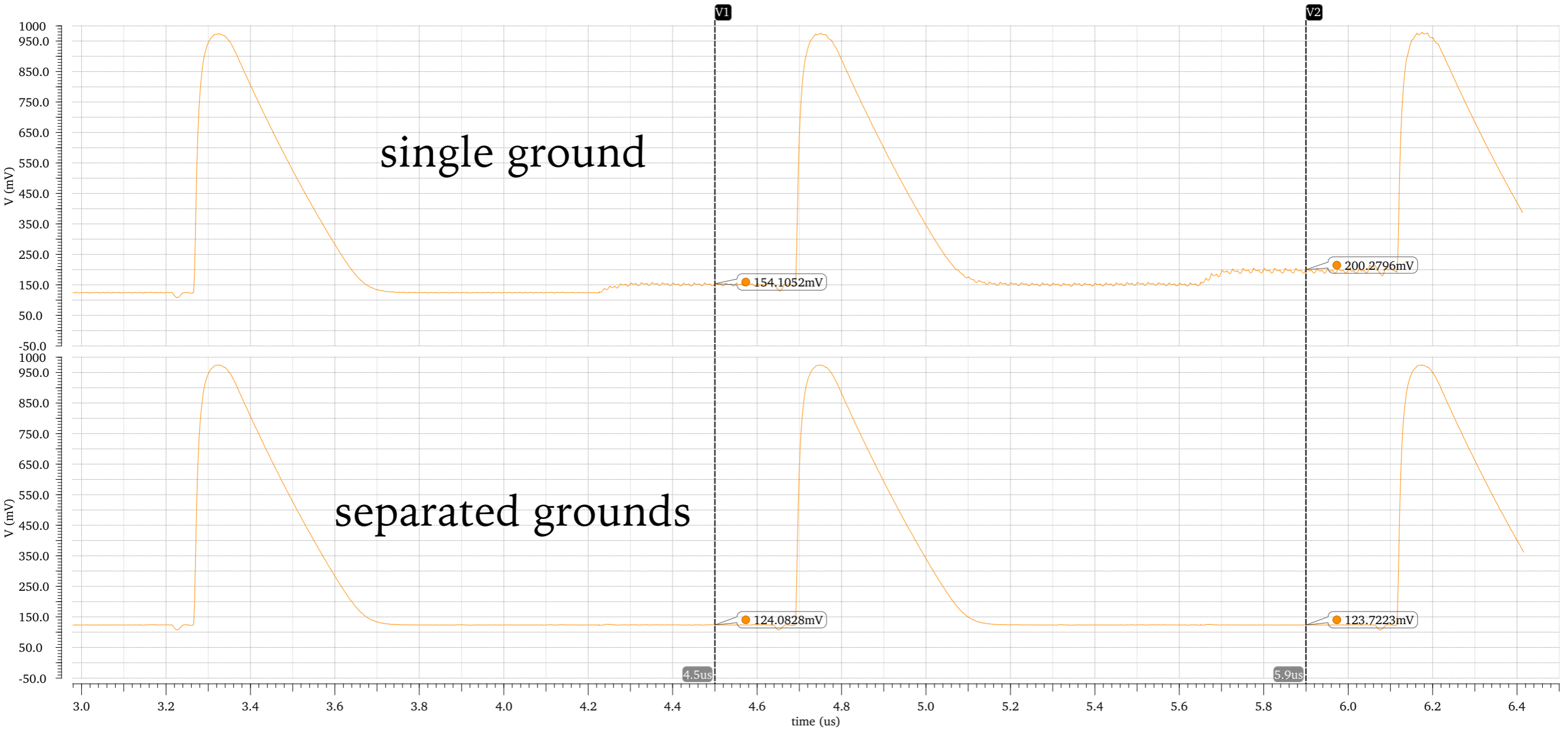


- Noise reduced with separated grounds.

top pixel

middle pixel

bottom pixel



- DC level of SFOUT\_BUFF changes when different pixels are enabled.