

#### Characterisation of Redlen High-Flux CdZnTe at >10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> using

# HEXITEC<sub>MHz</sub>

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## **1 HEXITEC<sub>MHz</sub> Overview**

The next generation of HEXITEC systems

#### 2 Redlen HF-CZT Overview

CdZnTe for use at high X-ray fluxes (<10<sup>9</sup> ph s<sup>-1</sup> mm<sup>-2</sup>)

#### **3** Initial HF-CZT Test Results

Results from HF-CZT characterisation carried out at the DLS B16 Test Beamline in December 2022

## **4** Recent HF-CZT Test Results

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## **5** Next Steps







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## HEXITEC<sub>MHz</sub>

New fully spectroscopic X-ray imaging detector:

- 1 MHz continuous frame rate
  - Spectroscopic X-ray fluxes of >10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup>
  - Facilitated by:
    - Integrating Front End with <70 e<sup>-</sup> ENC
    - 12-bit on-chip digitisation in TDCs
    - 20 × 4.1 Gbps serialisers for data output
- + 80  $\times$  80 pixels on a 250  $\mu m$  pitch



HEXITEC<sub>MHz</sub> ASIC



#### *Comparison of HEXITEC and HEXITEC*<sub>MHz</sub> specifications

Parameter	HEXITEC	HEXITEC <sub>MHz</sub>
Pixel Pitch (μm)	250	250
Array Size	80 × 80	80 × 80
Max Frame Rate (kHz)	9.81	1000
Max Spectroscopic Flux (ph s <sup>-1</sup> mm <sup>-2</sup> )	~104	> <b>10</b> <sup>6</sup>
Digitisation	Off-chip	On-chip
Detector Type	Track + Hold	Integrating
Gain Stages (keV in CZT)	200	100
	600	200
		300
FWHM@100keV (keV in CZT)	<1	<1
Power Consumption (W)	1.5	15

## HEXITEC<sub>MHz</sub>

New fully spectrosco							cif	ications	
• 1 MHz continuo	IOPscience	<b>Q</b> Jour	nals 👻 🛛 Books	Publishing Su	pport 🥹 Log	in 🕶		<b>C</b>	<b>HEXITEC<sub>MHz</sub></b>
Spectrosco	Journal of I	nstrumer	ntation						250
• Facilitated		nstramer	lation						80 × 80
<ul><li>Integr</li><li>12-bit</li></ul>		CESS							1000
• 20 × 4 • 80 × 80 pixels of	Spectrosco	opic X-ra	y imaging	g at MHz fr	ame rates	— the			> <b>10</b> <sup>6</sup>
	L. Jones <sup>2,1</sup> , S. Bell <sup>1</sup> , B. Cline <sup>1</sup> , T. Gardiner <sup>1</sup> , M. Hart <sup>1</sup> , M. Prydderch <sup>1</sup> , P. Seller <sup>1</sup> , M. Veale <sup>1</sup> and M. Wilson <sup>1</sup>						lson <sup>1</sup>	D	On-chip
	Published 11 October 2022 • © 2022 The Author(s). Published by IOP Publishing Ltd on behalf of Sissa Medialab. Journal of Instrumentation, Volume 17, October 2022 Citation L. Jones <i>et al</i> 2022 JINST 17 C10012 DOI: 10.1088/1748-0221/17/10/C10012					ab.	Hold	Integrating	
3								100	
1000 march								200	
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and the second s	0.		1 C 1	FWHN	/@100keV	(keV in CZT)	<1		<1
		HEXITEC	C <sub>MHz</sub> ASIC	Powe	r Consumpt	ion (W)	1.5		15
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#### Redlen High-Flux Capable CdZnTe

- Spectroscopic-grade CdZnTe.
  - Optimised e<sup>-</sup> transport properties and spectral resolution
  - Poor h<sup>+</sup> lifetime and mobility ( $\tau_h \approx 0.2 \ \mu s$ ,  $\mu_h \approx 0.1 \mu_e$ )
    - h<sup>+</sup> trapping and polarization >10<sup>6</sup> ph<sup>-1</sup> s<sup>-1</sup> mm<sup>-2</sup>
- Medical and Security CT applications require:
  - >10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> X-ray fluxes
- High-Flux Capable CdZnTe (HF-CZT)
  - Introduced in 2017 by Redlen
  - ~10× increase in  $\tau_h$  allows operation <10<sup>9</sup> ph s<sup>-1</sup> mm<sup>-2</sup>

#### **HEXITEC<sub>MHz</sub> HF-CZT detectors**

- 2 mm thick sensors two Pt electrodes, 25 μm inter-pixel gap
- ASIC hybridisation by UKRI STFC Interconnect cured at 150°C



HF-CZT sensor hybridised to HEXITEC<sub>MHz</sub> ASIC

#### Results from Thomas et al., 2017 [1]

 Table 1. A summary of the measured charge transport properties of three "high-flux" Redlen CdZnTe detectors [14, 16].

	$\mu_e \tau_e$ (×10 <sup>-4</sup> cm <sup>2</sup> V <sup>-1</sup> )	$\frac{\mu_e}{(\mathrm{cm}^2\mathrm{V}^{-1}\mathrm{s}^{-1})}$	$\frac{\tau_e}{(\times 10^{-6}\mathrm{s})}$	$\mu_h \tau_h$ (×10 <sup>-4</sup> cm <sup>2</sup> V <sup>-1</sup> )	$\frac{\mu_h}{(\mathrm{cm}^2 \mathrm{V}^{-1} \mathrm{s}^{-1})}$	$ \tau_h \\ (\times 10^{-6} \text{ s}) $
High Flux CdZnTe	$11 \pm 6$	940 ± 190	$1.2 \pm 0.8$	$2.9 \pm 1.4$	$114 \pm 22$	2.5 ± 1.4
Standard CdZnTe	100	1100	11	0.2	88	0.2



#### Redlen High-Flux Capable CdZnTe





## Agenda

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- B16 Beamline: August 2022, December 2022, December 2023
  - Monochromatic X-rays: 10 20 keV
  - Photon fluxes: 10<sup>5</sup> 10<sup>8</sup> ph s<sup>-1</sup> mm<sup>-2</sup>
  - 1 MHz data stream on one/four fast-data channels
  - Tested HF-CZT (2 mm), p-type Si (300 μm), GaAs (500 μm) devices
- Beamline scientists: Vishal Dhamgaye, Oliver Fox, Kawal Sawhney





B16 setup photos







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*Pixel (13,37) high-gain CSD spectrum with Gaussian fit to the 20 keV photo peak - 0.2 keV bin width* 



20 keV FWHM distributions in the beam

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#### 'Excess Leakage Current Effect' Identification - Comparing HF-CZT data at two fluxes



Average results in beam\*

Flux (10 <sup>6</sup> ph s <sup>-1</sup> mm <sup>-2</sup> )	Offset (ADU)	Offset (keV)	Offset (nA mm <sup>-2</sup> )
0.30	36 ± 7	$1.31 \pm 0.25$	0.80 ± 0.15
3.15	160 ± 28	5.91 <b>±</b> 1.05	3.60 ± 0.64

20 keV high-gain results at 0.30 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> and 3.15 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup>. (a) Pixel (13,37) raw spectra – 1 ADU bin width. (b) Map of 3.15 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> 0γ peak shifts. (c) Average 3.15 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> 0γ peak shift across each readout row

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\* Shift  $[A \text{ mm}^{-2}] = \frac{\text{Shift} [ADU] \times (ADU - \text{keV conversion factor})}{\text{Integration time} \times (CZT \text{ pair conversion energy}) \times \text{Pixel area}}$ 

#### 'Excess Leakage Current Effect' Identification - Comparing p-type Si data at two fluxes



#### *p-type Si FWHM results*

Energy (keV)	HG FWHM (keV)	MG FWHM (keV)
10	0.66 ± 0.06	0.77 ± 0.10
15	0.68 ± 0.06	$0.81 \pm 0.10$

10 keV p-type Si high-gain results at 0.63 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> and 1.13 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup>. (a) Pixel (15,44) raw spectra – 1 ADU bin width. (b) Map of 1.13 × 10<sup>6</sup> ph s<sup>-1</sup> mm<sup>-2</sup> 0y peak shifts. Beam position highlighted red

#### 'Excess Leakage Current Effect' Identification - Comparing p-type Si data at two fluxes



#### 'Excess Leakage Current Effect' Identification - Comparing p-type Si data at two fluxes



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#### **Format of Dynamic Datasets**

• Dynamic datasets taken under specific conditions to further characterise 'excess leakage current' effect



#### Variables investigated

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Variable	Values
Energy (keV)	12, 20
Incident Flux (10 <sup>6</sup> ph s <sup>-1</sup> mm <sup>-2</sup> )	0.3, 1.2, 3.1, 7.8
Sensor Temperature (°C)	2.5,10,20,30,40,50
Sensor Bias Voltage (-V)	500,750,1000

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Parameter	Energy	Gain	Flux	Тетр	Bias
Value	20 keV	High	1.2 × 10 <sup>6</sup> ph s <sup>-1</sup> mm <sup>-2</sup>	20 °C	-1000 V



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### **Spatial Dependency**

ixed Param	Energy	Gain	Flux	Temp	Bias
alue	20 keV	High	$1.2 \times 10^{6}  \text{ph s}^{-1}  \text{mm}^{-2}$	20 °C	-1000 V



Raw histograms at t  $\approx$  200 s – 1 ADU bin width

#### Average shifts in the ROI\*

Area	Shift (ADU)	Shift (nA mm <sup>-2</sup> )
1	51 ± 17	$1.1 \pm 0.4$
2	121 ± 21	$2.4 \pm 0.4$

#### \*250 ADU threshold limit used for statistics





Average Oy peak position shift in the beam region



#### Max Oy peak position shift in the ROI

## **Spatial Dependency**

Fixed Param	Energy	Gain	Flux	Temp	Bias
Value	20 keV	High	$1.2 \times 10^{6}  \text{ph s}^{-1}  \text{mm}^{-2}$	20 °C	-1000 V

#### For comparison:







#### **1γFWHM** in the ROI



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Mapping max 0y peak shifts against the 1y FWHM

- Photo peak FWHM dictated by bulk properties
- No correlation in fits of 0γ peak shifts against 1γ photo peak FWHM
  - Suggests excess current dictated by interface properties

uv dopopdopov	Fixed Param	Energy	Gain	Temp	Bias
ux dependency	Value	20 keV	High	20 °C	-1000 V



Pixel (26,50) raw histograms at t  $\approx$  200 s – 1 ADU bin width

#### Average Oy peak position shift decay times

Flux (10 <sup>6</sup> ph s <sup>-1</sup> mm <sup>-2</sup> )	Decay time to 15 ADU (s)*
0.30	0.91
1.22	5.59
3.08	9.42
7.81	12.20

\*15 ADU chosen as ~0.5 keV (~resolution of HEXITEC<sub>MHz</sub>)



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(a) 0γ peak shift at calculated fluxes. (b) Comparison of calculated and measured fluxes

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#### **Temperature dependency**

Fixed Param	Energy	Gain Flux		Bias
Value	20 keV	High	$1.2 \times 10^{6}  \text{ph s}^{-1}  \text{mm}^{-2}$	-1000 V







Average Oy peak position shift in the beam

- 0.13 eV E<sub>a</sub> associated with well-known CZT defect
  - A-centres generated by Indium doping

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#### **Next Steps**

#### Testing and delivery of new DAQ system

- Verification of 80 × 80 output, in-FPGA dark-correction and histogramming
- Will enable lab-based analysis of effect using lower-flux sealed sources

#### ESRF beamtime (Jul 2024) ≤75 keV

- Temperature-dependency of 'excess leakage current' effect
  - Further Arrhenius analysis
  - PICTS analysis of HEXITEC<sub>MHz</sub> data

#### DLS B16 beamtime (Dec 2024) ≤20 keV

- Novel HF-CZT HEXITEC-MHz detector variants
  - New low-temp hybridisation (<80 °C) method</li>
  - Redlen sensors with new Ti-anode technology





The HEXITEC<sub>MHz</sub> camera system



#### Summary

- Results revealed an 'excess leakage current' effect in HF-CZT
  - Not present in p-type Si results
- Arrhenius analysis suggests related to an Indium-based CZT defect
- Magnitude dependent on incident X-ray flux
  - Significant >10<sup>5</sup> ph s<sup>-1</sup> mm<sup>-2</sup>
  - + 3.6 nA mm^2 at 7  $\times$  107 ph s^1 mm^2
- At spectroscopic fluxes, lower excess currents measured.
  - + 0.48 nA mm^-2 at 3  $\times$  105 ph s^-1 mm^-2
  - In-FPGA HEXITEC  $_{\rm MHz}$  firmware enables real-time corrections
- Further characterisation planned with  $\mathsf{HEXITEC}_{\mathsf{MHz}}$ 
  - Validates detector's capability for MHz materials characterisation

Please contact me with further questions: <a href="mailto:ben.cline@stfc.ac.uk">ben.cline@stfc.ac.uk</a>





Team members at DLS B16 Test Beamline



Funded by the: Centre for Instrumentation (CFI) run by Marcus French

Technology

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<u>Detector</u>	Interconnect	Detector Systems	PCB Design Office
<b>Development</b>	Paul Adkin	<u>Software</u>	Darren Ballard
Ivan Church	Paul Booker	Dominic Banks	Dan Becket
Ben Cline	Toby Brookes	Adam Davies	Chris Day
Matt Hart	Navid Ghorbanian	Josh Harris	
Paul Seller	John Lipp	Tim Nicholls	Detector Systems
Dave Sole	Andreas Schneider	Joseph Nobes	William Helsby
Matt Wilson			
Matt Veale		ASIC Design	Electronic Systems
		Stephen Bell	<u>Design</u>
		Thomas Gardiner	Rob Halsall
		Lawrence Jones	Sooraj Pradeep
		Mark Prydderch	Matt Roberts
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