

Multiple Layer per Reticle "MLR1" submission: first sensor prototypes in TPSCo 65 nm process in collaboration with CERN EP R&D on monolithic sensors

Two small-scale analogue pixel prototypes chip with different versions of the output buffer: a source-follower (APTS-SF) and an operational amplifier (**APTS-OA**)

APTS-SF

addressed to measure the time resolution!

- \rightarrow purpose: testing pixel cell
- \rightarrow 6x6 pixels matrix

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- \rightarrow pitch: 10, 15, 20, 25 µm
- \rightarrow direct analogue readout of central 4×4 submatrix
- \rightarrow 3 process modifications







1/4 of a 300 mm MLR1 wafer (50 μ m-thick)

Several test beam campaigns from 2021

- use of charged particles (positive hadrons)
- reconstruction of particles tracks using the Corryvreckan framework: fit the cluster position on the reference planes
- tracks interpolated at the plane of the DUT
- association of tracks to cluster on the DUT plane

Detection efficiency and spatial resolution

Characterization goals:

- detection efficiency

radiation hardness

spatial resolution



