HEPiX Autumn 2023 Workshop



Contribution ID: 13

Type: not specified

Update on ARM for WLCG

Monday 16 October 2023 15:45 (25 minutes)

Over the last 18 months we have investigated the use of the 80-core Ampere Altra for WLCG workloads and have previously reported that for WLCG workloads this ARM-based machine delivers significant energysavings whilst being comparable in both speed and cost to typical AMD machines. More recently we have extended this work to the 128-core Ampere Altra Max and will present these results for the first time. In the meantime, the installation of a 2000-core Altra ARM-farm at Glasgow has allowed significant in-house ARM resources to be presented on the WLCG for the first time. The facility is being validated by ATLAS, re-running a recent google validation of their simulation workload and running reconstruction in tandem. Looking ahead, Glasgow is in the process of procuring a NVIDIA Grace (ARM-based) processor to characterise its performance (though we expect this to push performance rather than energy-efficiency) and hope to have new results to present on the AMD Bergamo processor available for the meeting.

Author: BRITTON, David (University of Glasgow (GB))

Co-authors: BORBELY, Bruno (University of Glasgow (GB)); SPITERI, Dwayne (University of Glasgow); SIM-ILI, Emanuele (University of Glasgow (GB)); STEWART, Gordon; SKIPSEY, Samuel Cadellin

Presenter: BRITTON, David (University of Glasgow (GB))

Session Classification: Computing & Batch Services

Track Classification: Computing & Batch Services