Low temperature electrical characteristics of irradiated strip sensors

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CONTENT

Irradiation of ATLAS07 miniature sensors on reactor in Rez near Prague

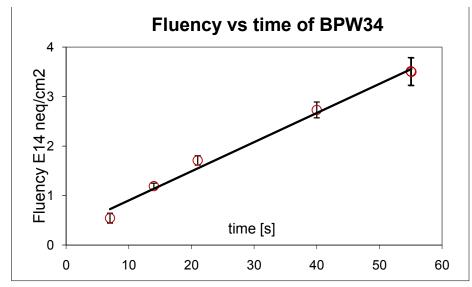
Experimental set-up for temperature -30deg C

Bulk IV and CV characteristics

Coupling and an inter-strip capacitance

Punch through voltage measured by DC method

An exposition time was calibrated by diodes BPW34 from CERN Fluency Φ [neq/cm2/mV]=9.1E09*(Virr--Vo), where Virr and Vo are voltages generating current of 1mA at 20°C in open direction.



4E14	2E15	1E16
W12BZ4AP4	W13BZ4AP4	W13BZ1P7
W12BZ4BP10	W13BZ4BP10	W13BZ2P2
W12BZ4CP16	W13BZ4CP16	W13BZ5P11
W12BZ4DP22	W13BZ4DP22	WB13BZ6P202Liver

Pneumatic transport of sensors in the channel H1.

Mini-sensor is closed in PVC envelops which is placed inside plastic tube \emptyset =3cm and length of 8cm.

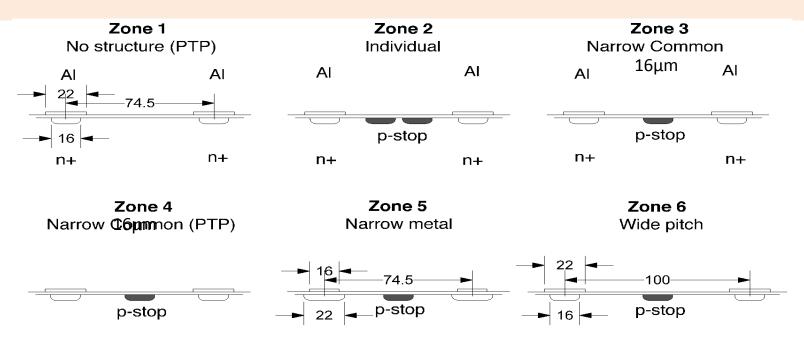
There is no cooling in the channel H1 and therefore after 3 min the tube with sensor must be removed from reactor due to high temperature (~55°C) and cooled before next step of irradiation

Fluency rate on LVR15 at 9.9MW

Fluence neq/cm2	Exposition seconds	3min irrad
4E14	54.6	1x
2E15	272.9	2x
1E16	1364	8x

Annealing: ~60 days at 23C

Basic sensor characteristics



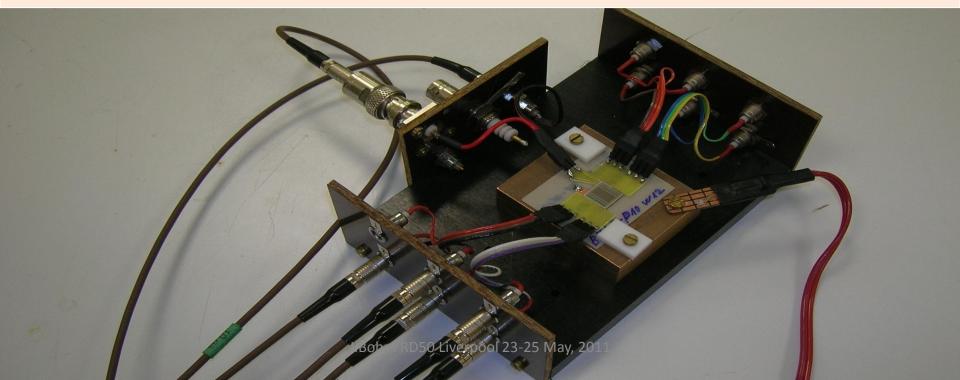
The micro-strip silicon **miniature sensors of 1cmx1cm** (strip length 0.8cm) are ATLAS07 Series I fabricated by Hamamatsu Photonics (HPK) using 6" (150 mm) process technology . The baseline is *p-type* float zone silicon of resistance ~6.7k' Ω with crystal orientation <100> and having **thickness of 320 µm**. Sensors are single-sided with capacitivly coupled readout *n-type* strips which are biased through polysilicon resistors. The readout strips with *pitch 74.5 µm* are electrically isolated by a common and floating p-implant ('**p-stop' isolation**) with doping concentration of **1x10¹²ions/cm²** (wafers W12 and W13)

EXPERIMENTAL SET-UP FOR TEMPERATURE -30 C

Measurement of the sensor basic characteristics at working temperature -30C which is supposed for upgraded ATLAS ID at sLHC.

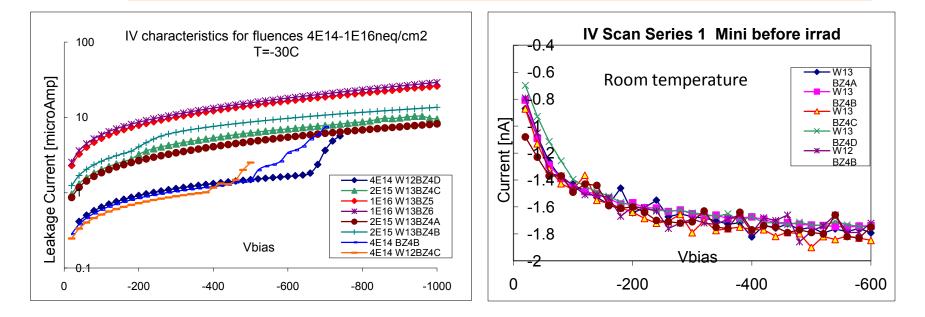
Six AC and DC pads of the randomly selected neighboring strips are bonded to PCB and contacted to connectors . Ceramic plate 0.4mm with high heat conductivity is placed on Cu-cube. The Cu-tape serves as the backplane electrode glued to sensor.

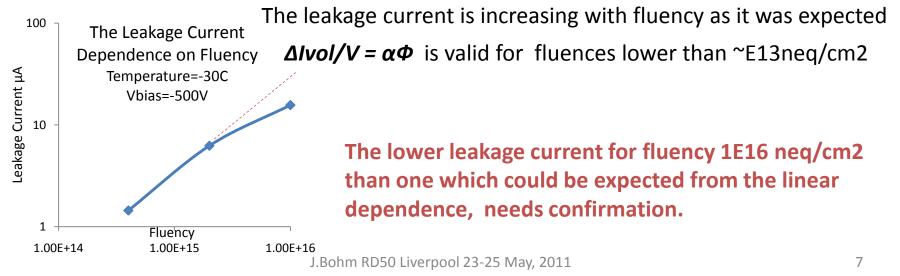
Results are still preliminary



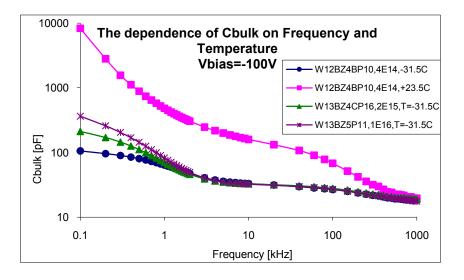
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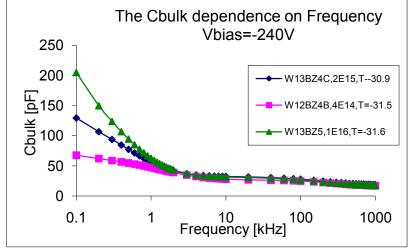
BULK IV AND CV CHARACTERISTICS

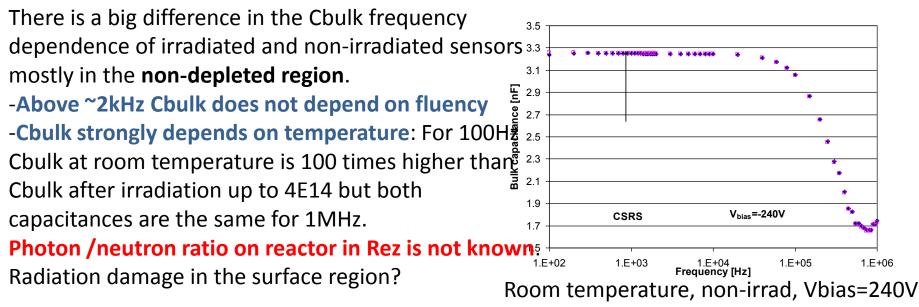




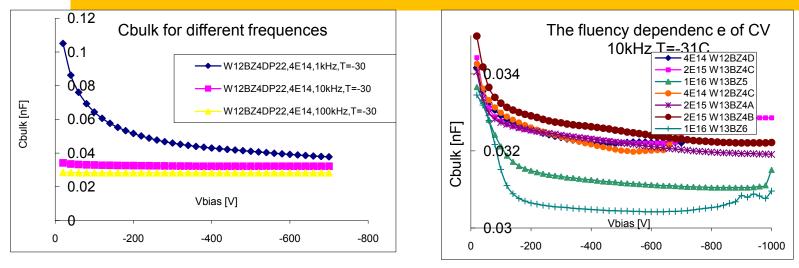
Bulk CV Characteristics



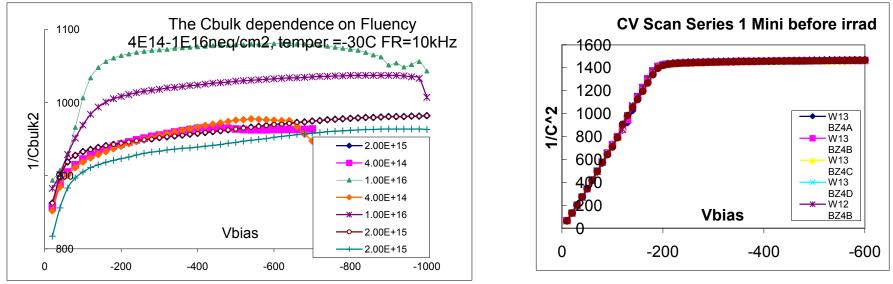




BULK CV CHARACTERISTICS

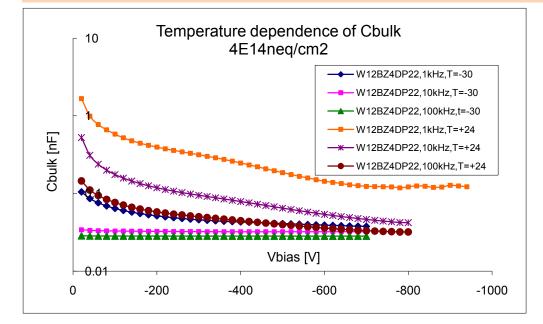


The Vbias dependence of Cbulk for 10kHz is different for various fluences Vfd for various fluences seems to be even lower than one for non-irradiated sensors after long annealing. It will be checked by CC measurement with laser.



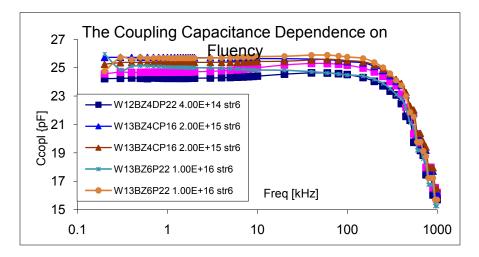
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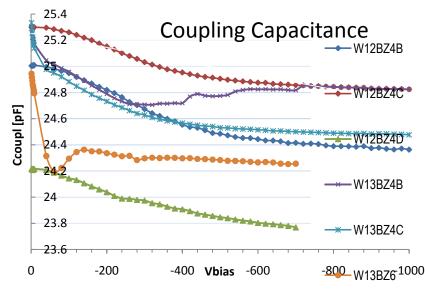
Bulk CV Characteristics

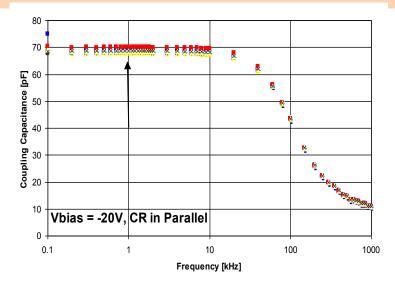


Behavior of Cbulk characteristics needs more delicate investigation

Coupling Capacitance

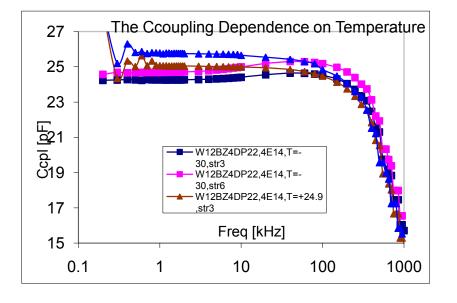


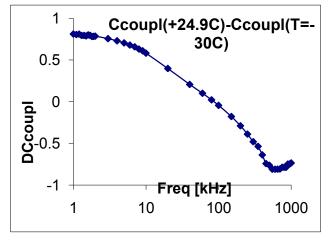




Frequency characteristic of C coupling is flat up to 100kHz for irradiated sensors and it does not depend on fluency. <Ccoupl>=30pF/cm

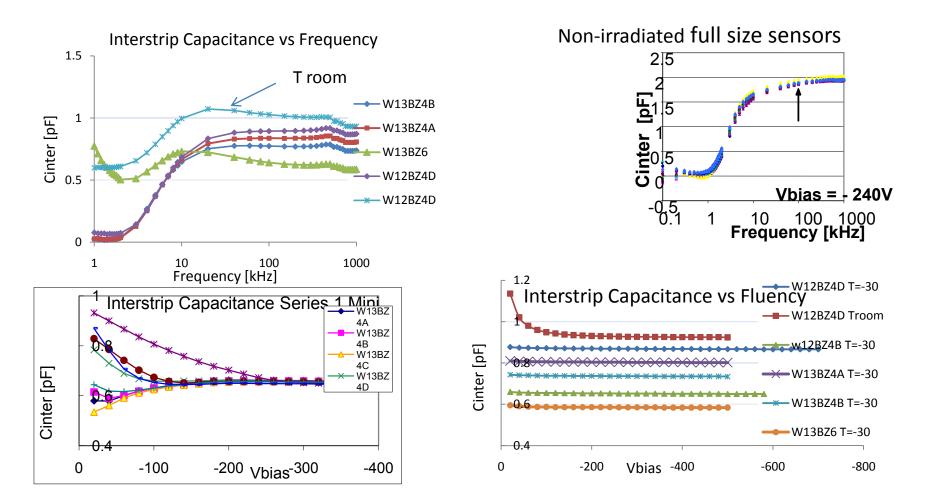
Coupling Capacitance





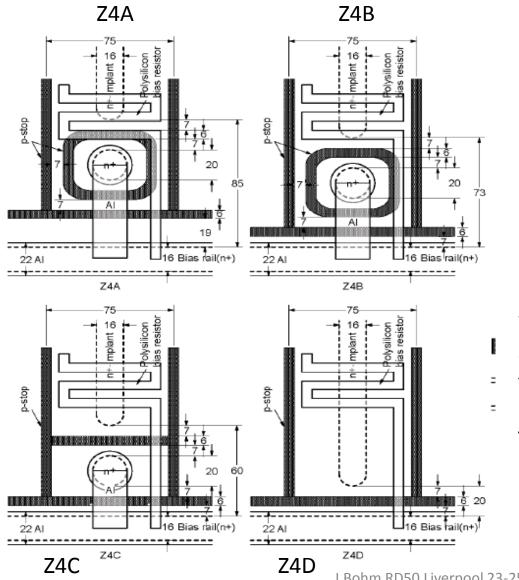
Temperature dependence of Ccoupl is very weak

Interstrip Capacitance



Weak dependence of the Interstrip Capacitance on fluency: <Cinter>=0.9pF/cm

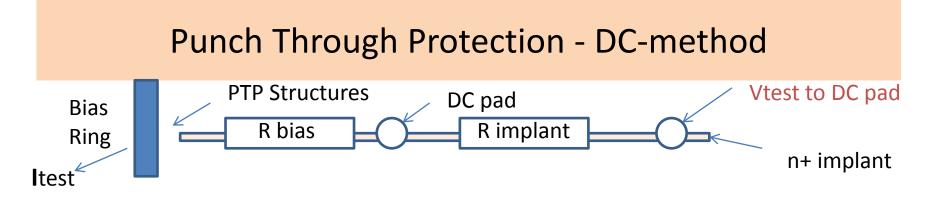
Punch Through Protection Structures



When a large amount of charge is deposit in the sensor by a beam splash, a large current flows through the bias resistor and drops the potential of the n-strips implant toward the backplane bias voltage, thus generating a spike of voltage across the AC coupling insulator.

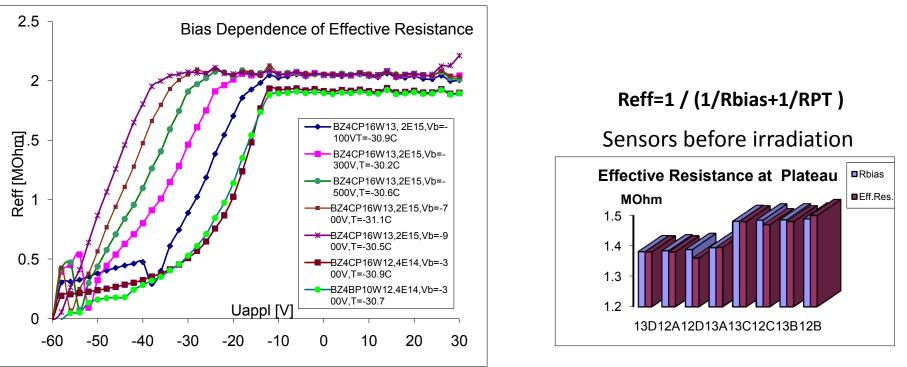
When the distance between the bias rail

- and the n-strip implants is appropriate,
- this voltage between the bias rail and the
- n-strip implants can be limited.
 This distance is 20 μm.



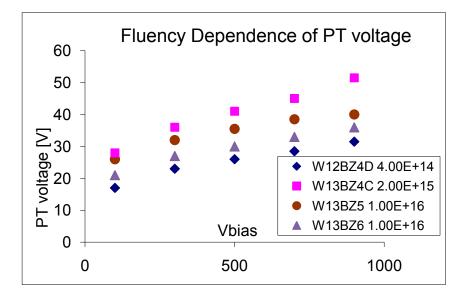
Reff=Vtest/Itest, where Vtest is an applied voltage (Uappl) to DC pad and Itest is an current between DC pad and the bias ring. **Rpt** is supposed to be parallel to Rbias

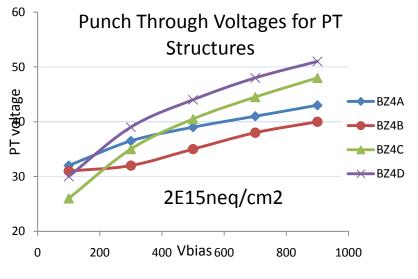
Punch-Through Voltage is the Test Voltage for Rbias=Rpt, i.e. for Reff=Rbias/2



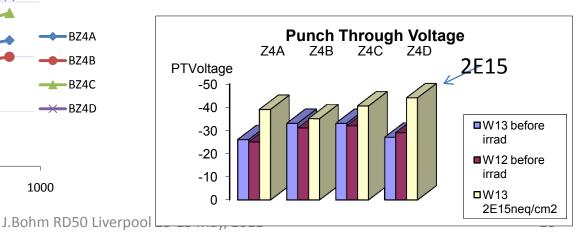
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Punch Through Protection - DC-method

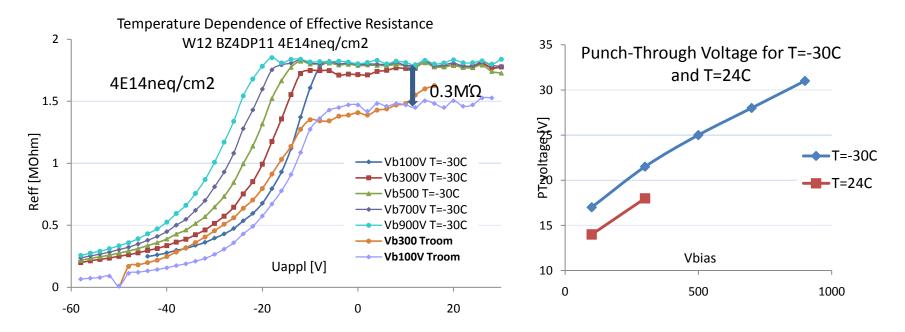




-PT voltage is increasing with bias voltage -PT voltage is increasing with fluency -PT voltage is higher for irradiated sensors than one for non-irradiated sensors. -PT voltage for sensors Z4D and Z4C (the simplest PTP structure) is higher by ~10V than one for 74A and 74B. There is no saturation of PT voltages even for PTV≥Vfd. It agrees with the Santa Cruz result (C.Betancourt, Oxford, March 2011): 'Finite implant resistance (Rimp=19.4 $k\Omega$) isolates PTP structure from the far implant end ."



Temperature Dependence of Punch-Through Voltage?



PT voltage is slightly higher for low temperature (-30C) than for room temperature (24C). The difference would be explained by lower bias resistance for room temperature than for -30C.

It is worth to measure temperature dependence of bias resistor.

Conclusion

- * ATLAS07 Series I mini-sensors of HPK have been irradiated on reactor LVR15 in Rez up to three fluences: 4E14, 2E15 and 1E16neq/cm^2
- * Sensors were measured at temperature -30C which is planned for ID and sLHC
- * A rise of the leakage current with fluency is not linear. Values at 1E16neq/cm2 should be confirmed.
- * Above ~2kHz Cbulk does not depend on fluency
- * Cbulk strongly depends on temperature
- * Vfd after 2 months annealing seems to be lower than one for non-irradiated sensors? Confirmation by CC measurement is needed
- * Frequency characteristic of C coupling does not depend on fluency and temperature: < Ccoupl>=30pF/cm
- * Weak dependence of the Interstrip Capacitance on fluency: <Cinter>=0.9pF/cm
- * PT voltage is increasing with fluency.
- * There is no saturation of PT voltage even for Vpt≥Vfd
- * An evidence that Rbias rises with decreasing temperature it should be confirmed

Measurement will continue also with a laser. More mini sensors are needed