



Circuit of 77 octupole spool pieces in MBA dipoles (part of combined MCOD)

120 A circuit using a 120 A converter (RPLB type) with QPS input (exceptionally)

3 events:

11 Feb 201100h2413 Feb 201100h5913 Feb 201116h59

See also presentation Markus on the RSS.A78B1

A. Verweij, 8 March 2011, MP3 mini workshop



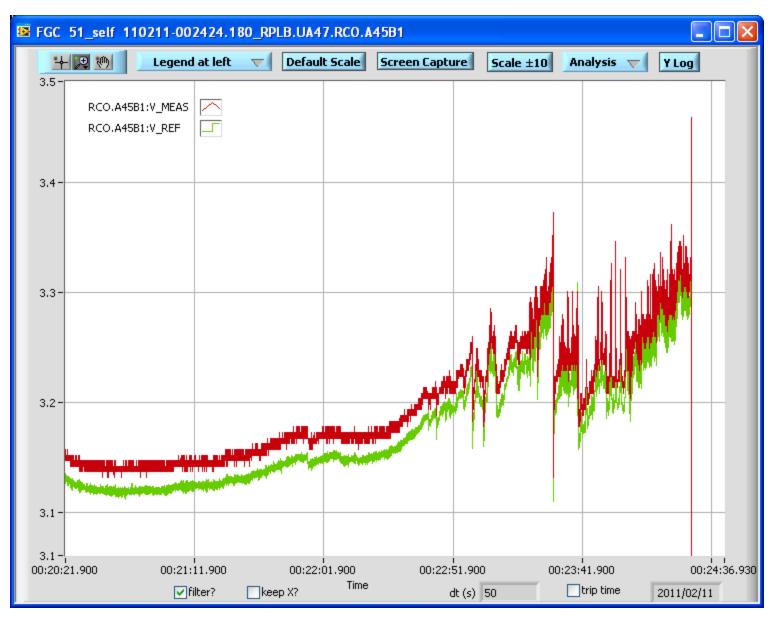
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				GENERAL INFORMATIO	N POWER CONVERTERS QPS WARM CABLES	
CO A45B1 : Octupole	snool nieces, in series	per sector only in MBA (dipoles (Part of combined M	COD magnet type) Link	to MTF	
		per sector only in MDA (apples (Fart of combined M		to Mill	
D : 254295, Circuit version : ST Power Converters in the Circ			PC Lo	cation	Rack Name/Slot	
RPLB.UA47.RCO.A45B1 (MT			UA		YLC03=UA47 1.1	
agnets in the Circuit	, TE-El O Database)		Nurr		1.1	
			7			
Current Leads in the Circuit				•		
OFLFS.7R4.1						
DFLFS.7R4.2						
Magnets per Power Convert	er					
RPLB.UA47.RCO.A45B1						
NCO			7	7		
Nominal :	100 A	I Ultimate :	110 A			
0/7	.0 A	I Overload :	126 (+-3%) A	I Min Op :	.0 A	
Offset :			004400.01	R tot Measured :	.030370 Ohm	
	.031 H	R tot :	.031160 Ohm	it tot measureu .	.000010 01111	
_ tot :	.031 H 40.00 s	R tot : max(di/dt) :	3.000 A/s	Time Constant :	.988 s	
L tot : Ramp Time :						
. tot : Ramp Time : J leads :	40.00 s	max(di/dt) :	3.000 A/s			
. tot : Ramp Time : J leads : J Boost :	40.00 s .100 V	max(di/dt) : U Extr :	3.000 A/s .000 V	Time Constant :	.988 s	
. tot : Ramp Time : J leads : J Boost : Narm Cable Verification : Circuit Parameters	40.00 s .100 V .092 V ✓	max(di/dt) : U Extr :	3.000 A/s .000 V	Time Constant :	.988 s	
I Offset : L tot : Ramp Time : U leads : U Boost : Warm Cable Verification : Circuit Parameters Operational Temperature :	40.00 s .100 V .092 V ✓ 1.90 K	max(di/dt) : U Extr :	3.000 A/s .000 V	Time Constant :	.988 s	
L tot : Ramp Time : U leads : U Boost : Warm Cable Verification : Circuit Parameters	40.00 s .100 V .092 V ✓ 1.90 K NO	max(di/dt) : U Extr :	3.000 A/s .000 V	Time Constant :	.988 s	

Friday, March 04, 2011 5:19:47 PM

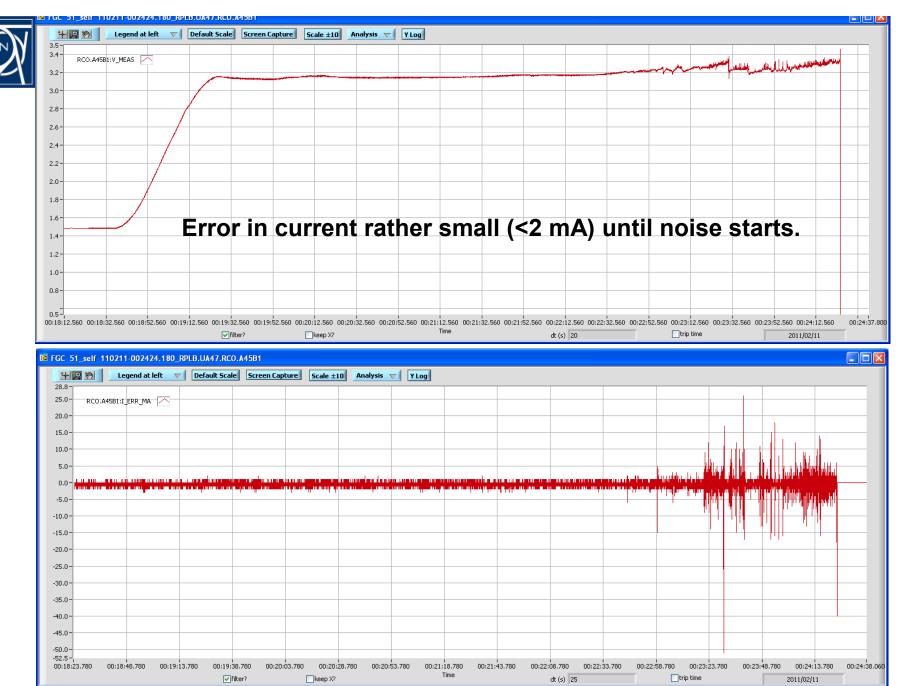


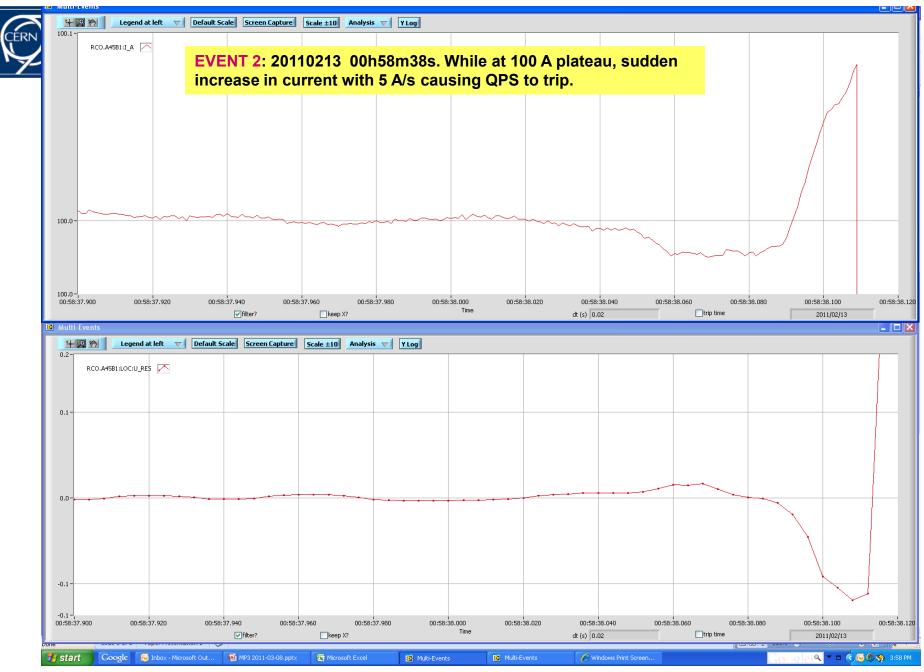


Strong noise in V_meas and V_ref already present during 100 s before the trip



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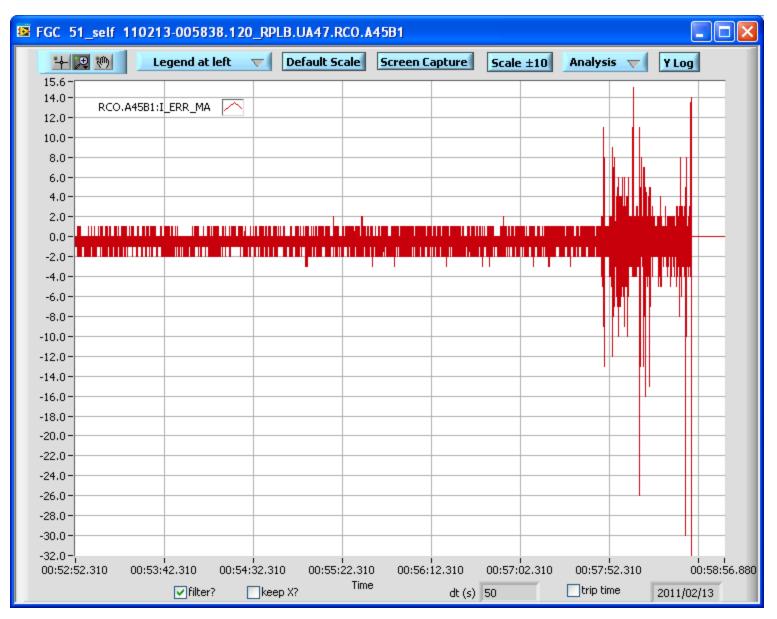
Strong noise in V_meas and V_ref already present during 50 s before the trip

🖼 FGC	51_self 110	213-005838.	120_RPI	B.UA47.RCO.A	45B1			
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3.6-	RCO.A45B1: RCO.A45B1:							
3.5-							1	
3.4-						الله		
3.3-							Mar A	
3.2-								
3.1-								
		i 32.100 00:56 ⊡ filter?	:52.100	Time	00:57:32.100 00:5 dt (s)		58:12.100	00:58:46.810

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Error in current rather small (<2 mA) until noise starts.





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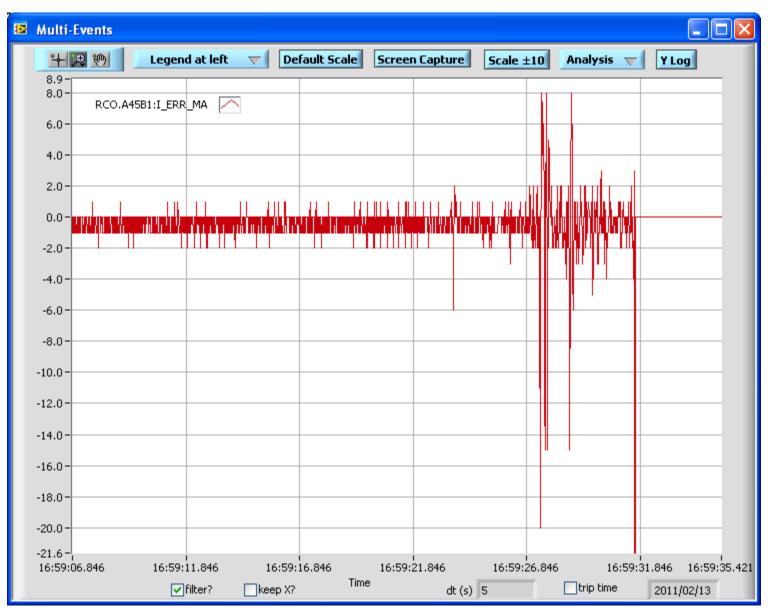
Noise in V_meas and V_ref already starting 4 s before the trip



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Error in current rather small (<2 mA) until noise starts.





https://edms.cern.ch/file/1131600/1/LHC-bad-connexion-signature.pptx

LHC Circuit Connections badly Screwed TOGETHER analyze



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Yves Thurel, 7 March 2011



A connection badly screwed together:

- Is not easy to detect in operation: no huge l.error expected, and very long period of very acceptable errors
- Will not obviously be detected by a increase of the cable resistance (sometimes it decreases with time).
- Is erratic and can disappear for very long period.

Analyzing it on a particular event:

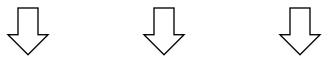
- Vmeas is the best curve to be used, even if low sample rate.
- Keep in mind that V.meas observed is equal to V.ref and is never a loose event, but is always the loop reaction.

Detection elsewhere in the machine: see presentation Markus



	Number: 1130233 v.1 RC0.A45B1 PN0.a3 110213-165315 EDMS Id: 1130233 v.1 VERWEIJ ARIAN Initiated Report - Non conformity 2011-02-13 Reference					
	Comments Vised in Access Rights Versions & other info					
Actions: Edit Description, External Reference a	Put File Set reservation Delete Doc. Add to caddie Notify Clone					
Description	PNO.a3. Quench during ramp up at 83 A, and circuit seems to degrade. In-dept analysis is needed!!! Circuit superlocked for the moment.					
External Reference	Thous, guilten daning tang up at 65 A, and cheate seems to degrade, an depending sis is needed in cheate superfocked for the moment.					
Keywords						
Special Properties						
Class	Cold performance					
Disposition						
Importance	Critical					
Files of the Document						
Sub-Documents						
Associated URL (CDD Drawing Fo	Idat Urany)					
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Context						
What's next ?	List of Local Administrators for any questions regarding this document (access rights, lifecycle)					
Context	LHC-HWC-MTF: General Context for Hardware Commissioning slots and jobs in MTF					
Release Procedure	Release Procedure for NCRs					
Release Procedure Equipment Code	Release Procedure for NCRS -					
	Release Procedure for NCKs					

Non conformity opened on 13 Feb, and closed on 4 March





	INFO Your comments have been stored	
Summary Su	v-Documents Approval & Comments Used in Access Rights Versions & other info	
Actions	Change Status Give Comments	
	ition & next steps Hide	
Current st	atus of this Version is Closed.	
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Status Histo	y & Comments	
Created by A	rjan VERWEIJ on 2011-02-13, 18:57	
Status chang	ed to Closed by Arjan VERWEIJ on 2011-03-04, 17:40	
Given Comm	ents (2 records) Hide	
	Normal display Text display Show all pages Hide all pages Sort: Date Reviewer Page	
	Arian VERWEIJ on 2011-03-07, 10:33 said:	Closed co
	It was a pb of the converter. Converter has been changed and powering was already authorized on 13 feb by Sandrine.	
	Arjan VERWEID on 2011-03-07, 17:40 said:	Closed co
	To be more precise: Problem with the unsufficient tightening of the bolts on the converter.	
EDMS Hyper	inks	
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