Case-study: CERN Sequencer

Soon it has the second se



Introduction to accelerator operations automation software

"Efficiency through Automation" Workshop Lukasz Burdzanowski | CERN 8th October 2023

Why Sequencer

To reduce the number of manual actions needed to to control the increasingly advancing machines and goals.

To shorten the turn-around times.



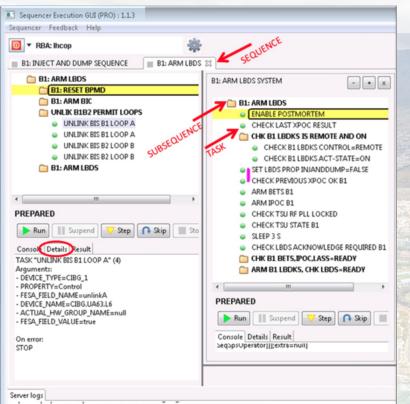




The well-defined and deterministic problem to address: efficiently execute n-steps to achieve the given state; repeatedly and reproducibly.



Sequencer - automation of operations



The Sequencer system: framework and dedicated applications, used to enable automation of operations and to help drive the accelerators through long sequence of tasks needed to produce the beam.

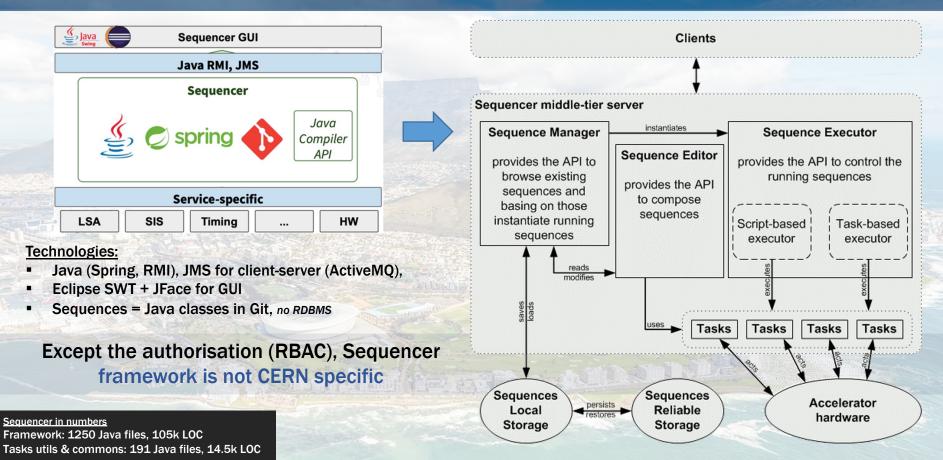
- Provides full control over task execution, allowing:break-points,
 - stepping, skipping, repeating, etc.
 - executing the tasks in parallel.



>

>

System architecture





Sequencer - a task

@Attrib(name = DEVICE_TYPE, description = "LSA device type", typeEnum = TypeEnum.LSA_DEVICE_TYPE)
@Attrib(name = LSA_HW_COMMAND, description = "HW command", typeEnum = TypeEnum.LSA_HW_COMMAND, value = HWC_LHC_COLL_LOAD_THRESHOLDS)
@TaskMethod(name = "LOAD COLL THRESHOLDS", description = "Load collimator thresholds", displayName = "Load collimator thresholds")
public static ArrayResult<?> loadCollThresholds(
 @Param(name = DEVICE, description = "Device name", typeEnum = TypeEnum.LSA_DEVICE) String deviceName,
 @Param(name = LSA_DEVICE_GROUP, description = "Device group name", typeEnum = TypeEnum.LSA_DEVICE_GROUP) String deviceGroupName,
 @Param(name = TIMING_USER, description = "Timing user (context)", typeEnum = TypeEnum.JAPC_SELECTOR) String timingUser)
 throws Exception {

return loadCollThresholdsIgnoringResidency(deviceName, deviceGroupName, timingUser, false);

Takeaways:

- tasks, from interacting with HW equipment, to running the algorithms, are procedures programmed in Java
- code-completion in IDE for developers, creating a task = writing code
- task meta-data (@Attrib) can be used to enable validation, and to limit generic tasks to specific scenarios (e.g. machines)
- like any code, tasks reside in VCS (version control system)



Sequencer – a sequence

				PREPARE COLLISIONS 22	PREPARE COLLISIONS TEST LBU		Edit task properties	+ . ×
				LOAD PC TABLE ORBIT H LOAD PC TABLE ORBIT-V	Coad collimator thresholds Wait for any update from \$parameterNam Load crystal collimator thresholds	Display Name	Load collimator thresholds	
Edit sequence properties				↑ □ × AD PC TABLE TUNE_TRIM AD PC TABLE CHROMA		Execution directive		
Storage Name*	TEST LBU			AD PC TABLE COUPLING AD PC TABLE OCTUPOLE F EV. GR. = 23 - TRIPLETS QUADS	 Coad crystal commator offesholds 	DEVICE_TYPE	[LHCCollimator] [LHC_COLL_LOAD_THRESHOLDS]	
Description	Test for ICALEPCS	Argument Name*	DEVICE	F EV. GR. = 23 - DISP SUP QUADS F EV. GR. = 23 - MATCHING QUAD		LSA_HW_COMMAND		
Name Description Typ			CURRENT_A ENERGY_GEV OI LANDAU_DAMPING	AD TCT COLL THRESHOLDS		Arguments DEVICE null LSA_DEVICE_GROUP null TIMING_USER null		• Val Var • Val Var • Val Var • Val Var
			JAPC_DEVICE_NAME	OK ECK TCT COLL PRIS AMMED ECK TCT COLL PRIS AMMED ECK TCT COLL PRIS AMMED ECK TCT COLL PRIS AMMED- ECK TEL-COLL-PRIS AMMED- ECK TEL-PRIS AMMED- ECK TEL-COLL-PRIS AMME	TASK "Load collimator thresholds" (NEW) Attributes (= read-only arguments): - DEVICE_TYPE = [LHCcollimator] - LSA_HW_COMMAND = [LHC_COLL_OAD_THRESHO Arguments: - DEVICE (VALUE) Type name: java.lang.String description: Device name type name: ISA DEVICE	IGNORE_CONTEXT_R	LOAD TCDQ BETASTAR ACTIVE IP LOAD TCDQ BETASTAR ACTIVE IP	

value: null

The sequence, and a sub-sequence, is a named list of tasks.

- Sequences are created with a dedicated editor
- Tasks & sub-sequences can be parameterised
- End-result, the sequence is generated Java code
- There are no limits on sequences length

@SequenceInfo(displayName="PREPARE COLLISIONS", description="PREPARE COLLISIONS FROM 3.5 M SQUEEZED OPTICS", categories="DEVELOPMENT") public class PREPARE_20COLLISIONS {

LOAD TCDO BETASTAR THRESHOLDS

LOAD TCDO COLL ENERGY THRESHOLD

public void exec() throws Exception {

_displayName('PREPARE FEEDBACKS FOR PHYSICS')new PREPARE_20FEEDBACKS_20FOR_20PHYSICS().exec(); _displayName('ENSURE START_COLLISIONS TABLE LOADED');CBCM.ensureEventTableLoaded('Start_Collisions'); _displayName('MOVE STATE/BEAM_MODE = ADJUST');new MOVE_20STATE_2/BEAM_5fMODE_2_0_3d_20ADJUST().exec(); _displayName('PREPARE SEPARATION BUMPS COLLAPSE');new PREPARE_20SEPARATION_20BUMPS_20COLLAPSE().exec(); _displayName('PREPARE SEPARATION BUMPS COLLAPSE');new PREPARE_20SEPARATION_20BUMPS_20COLLAPSE().exec(); _displayName('END SUBSEQUENCE BREAK');_break(),SEQ sendLogMessage('End subsequence break', (java.lang.Boolean)null);



The Sequencer GUIs:

Editor of sequences and the Executor (to control and monitor the execution).

PREPARE COLLISIONS 22	Tasks Sequences		ALL RF TASKS 🗱 📕 TEST DIALOG	Select sequence 🔹 🕫 🗙
PREPARE COLLISIONS			- 🔁 ALL RF TASKS	
PREPARE FEEDBACKS FOR PHYSICS	Refresh sequences		COMPARIANCE SETTINGS IN ALL RF FGC	AUT COAD WIDEDAND SETTINGS
DISARM FEEDBACKS			SWITCH RF POWER TO STANDBY	AFP-ALFA ROMAN POTS INSERTION (LHCf)
> 🛅 SWITCH ORBIT AND ENERGY FB OFF	COLL		> 🛅 SWITCH RF OFF	ALFA ROMAN POTS INSERTION - HIGHBETA2018
> 🛅 SET QFB OFF	DEVELOPMENT		TRIM ALL CAVITY Q TO 20000	ALFA XRP INSERTION - HIGHBETA @INJ
> 🛅 DISABLE RT TRIMS	COLLAPSE SEPARATION BUMPS	back of	TRIM ALL CAVITY Q TO 60000	ALICE CROSSING ANGLE FLIP
ENSURE START_COLLISIONS TABLE LOADED	DISABLING INJECTION AND INI COLL OUT	1000	TRIM RF TOTAL_VOLTAGE AND CAVITY Q FOR	ANTI-TELSCOPE AND LHB ROTATION SEQUENCE ARM BLMS BUFFERS
MOVE STATE/BEAM_MODE = ADJUST	INJECTION COLLIMATORS IN		TRIM RF TOTAL_VOLTAGE AND CAVITY Q FOR	ARM LBDS B1 AND B2 2015
MOVE TO STATE=ADJUST	LHC COLLISIONS AT INJECTION SEQUENCE	100.25	ARM LONGITUDINAL BLOW-UP	ARM LBDS B1 AND B2 FROM QUICK LAUNCH PANE
SET BEAM MODE=ADJUST	PREPARE COLLISIONS	- 1230T	B1: RESET LONGITUDINAL BLOW-UP B2: RESET LONGITUDINAL BLOW-UP	ARM LHC BIC B1 AND B2
▼	PREPARE TUNE FOR COLLISIONS		 b2: RESET LONGITUDINAL BLOW-OP COAD RF BLOW UP SETTINGS 	ARM LONGITUDINAL BLOW-UP
INCORPORATE 1.5M TRIMS INTO COLLISIONS			B1: ARM LONGITUDINAL BLOW-UP	ARM ORBIT FEEDBACKS
INCORPORATE RF FREQ 1.5M TRIMS INTO COLLISIONS	 EQUIPMENT TEST B1&B2: RAMP ALL COLLIMATORS (TEST ALIGNMENT) 	「たちろ	B1: ENABLE LONG BLOW-UP FEEDBACK	ARM TUNE FB SETTINGS
REGENERATE ACTUAL END BP FOR PHYSICS	COLLIMATOR INJECTION SETTING CHECKS (NO THRESHOLDS) FOR 450 GEV @INJ	20.23	B1: SET LONG BLOW-UP PAYLOAD 23	B1 : CHECK ALL COLL HAVE BIC INTERLOCK
MAKE LHC.USER.COLLISIONS RESIDENT	COLLIMATOR INJECTION SETTING CHECKS (NO THRESHOLDS) FOR 450 GEV (gin)	125	B2: ARM LONGITUDINAL BLOW-UP	B1&B2 RAMP SETTING TCDQ
- TOAD COLLISION EIINC COD ADT GAINS AND DUASE SUIT	COLLIMATOR RAMP TESTS			B1&B2: CHECK CRYSTALS HAVE BIC INTERLOCK
TASK "INCORPORATE 1.5M TRIMS INTO COLLISIONS" (10)	COLLIMATOR RAMP& COLLEZE TEST 6.5TEV - BS	2000		B1&B2: CHECK CRYSTALS HAVE ENERGY INTERLO
	COLLIMATOR ROMP&SQUEEZE TEST 0.5TEV - BS	-	▶ Run Suspend Step Skip Stop	B1&B2: CHECK TCDQ HAVE BIC INTERLOCK
Arguments: - SRC USER (VALUE)	SEND RAMP COLL TIMING EVT	The second	PREPARED	B1&B2: CHECK TCDQ HAVE ENERGY INTERLOCK
type name: java.lang.String	▼ MD		Console Details Result	B1&B2: CRYSTALS TO INJECTION SETTINGS
description: Accelerator user mapped to the source beam process	ATS: DRIVE COLLIMATORS BEFORE SOUEEZE	Charles		B1&B2: CRYSTALS TO PARKING
type enum: JAPC_SELECTOR value: LHC.USER.SQUEEZE-END	B1: SEND COLLIMATORS THREADING CLOSE IP1		H	B1&B2: RAMP SETTING COLLIMATORS DUMP PROT
VOIDE, LINE, OSER, SQUEEZE-END	B1: SEND COLLIMATORS THREADING CLOSE IP1 B1: SEND COLLIMATORS THREADING CLOSE IP2	Terrer		B1&B2: TCDQ TO PARKING
- DST_USER (VALUE)	B1: SEND COLLIMATORS THREADING CLOSE IP2 B1: SEND COLLIMATORS THREADING CLOSE IP3	E-35	Server logs	B1&B2: TCDQS TO INJECTION SETTINGS
type name: java.lang.String description: Accelerator user manned to the destination hearn process	B1: SEND COLLIMATORS THREADING CLOSE IP3	100	Sequence prepared : SequenceId = TEST CHANGING TIME TASKS@2	

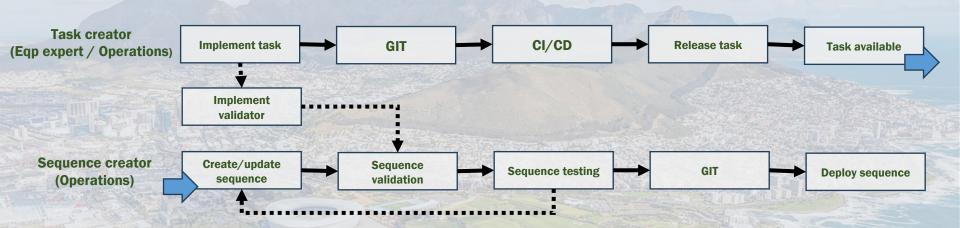
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Sequencer Editor

Sequencer Executor

User workflow

Two distinct stages when using the system: implementation of a task, and assembly of the sequence.



The tasks and sequences can be created by any user: equipment experts, operations, controls engineers, others. Once available in system repository, both entities can be used and require no detailed knowledge about the implementation details.



The Sequencer is used by several CERN machines, primarily in LHC, as well as across the injectors (SPS, PS, PSB, LEIR) and during HW Commissioning campaigns (HWC).

- > For HWC fully programmatic approach is used (Sequencer via API access)
- > The system is used in a variety of scenarios:
 - Hardware and machine commissioning
 - Machine Development specific procedures
 - ✓ Complete operational stages, e.g. RAMP, SQUEEZE in LHC
 - ✓ and more...

The type of tasks usually falls into one of the categories: check/wait, set/ensure e.g. "check if all Power Converters are ready" -> call HW status property "ensure Power Converters ready" -> check if PCs are ON, if not, set ON and wait for confirmation

The portfolio of tasks and sequences is growing close to 2000 (sub) sequences for LHC; over 350 task types; LHC nominal sequence \rightarrow close to 2100 tasks



Lessons learned

What works well

- Modular approach with configurable entities such as: Task/Sub-Sequences/Sequences
 - Enable re-use of entities, limiting copy & paste proliferation of task and sequences
 - Sub-sequences simplify workflow de-composition, from small HW-oriented procedures to rich operational sequences
- Separation of Implementation (tasks), from Edition and Execution of sequences
- With hindsight direct use of Java (no DSL or scripting)

and... what is missing to empower the users

- Sequences with loops, conditions, parallelism of sub-sequences
- > Tasks to return results or depend on each-other \rightarrow a double-edge sword (rapid growth of complexity)
- Complete API for programmatic interactions / embedding to other systems
- (possibly) Python task, and more...



The outlook

In the context of efficiency, and the objective to further automate and shorter the turn-around times of CERN machines, the Sequencer acts as a building block.

How to automatically fill LHC? How to automatically recover from HW/SW issues?

... and many more specific questions to answer

In this context, we plan to:

Invest more into it, re-think GUI and modernize, addressing known technological risks, to consider opening it, when justified by the external interest, rearchitect. The work to begin in 2024++ horizon, driven by the Efficiency working group@CERN.

How other labs could benefit from the Sequencer?



Sequencer is the main automation software solution for CERN accelerators operations.

It is a well-established system and the problems it addresses are not CERN specific.

Planned evolution and extensions aim to further increase the efficiency by helping to shorten the turn-around times.

We are embarking on making it more modular, extended, modernized. An opportunity to collaborate and let other labs profit.

