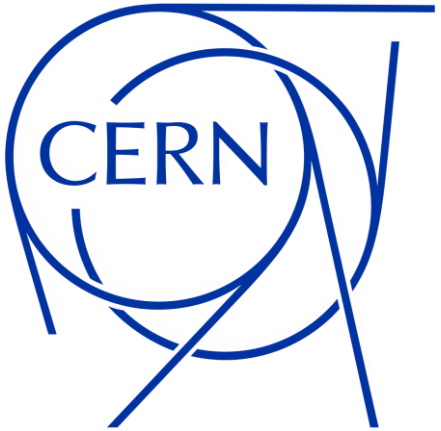


Primary Vertex dZ Cut Firmware for CMS p2gt

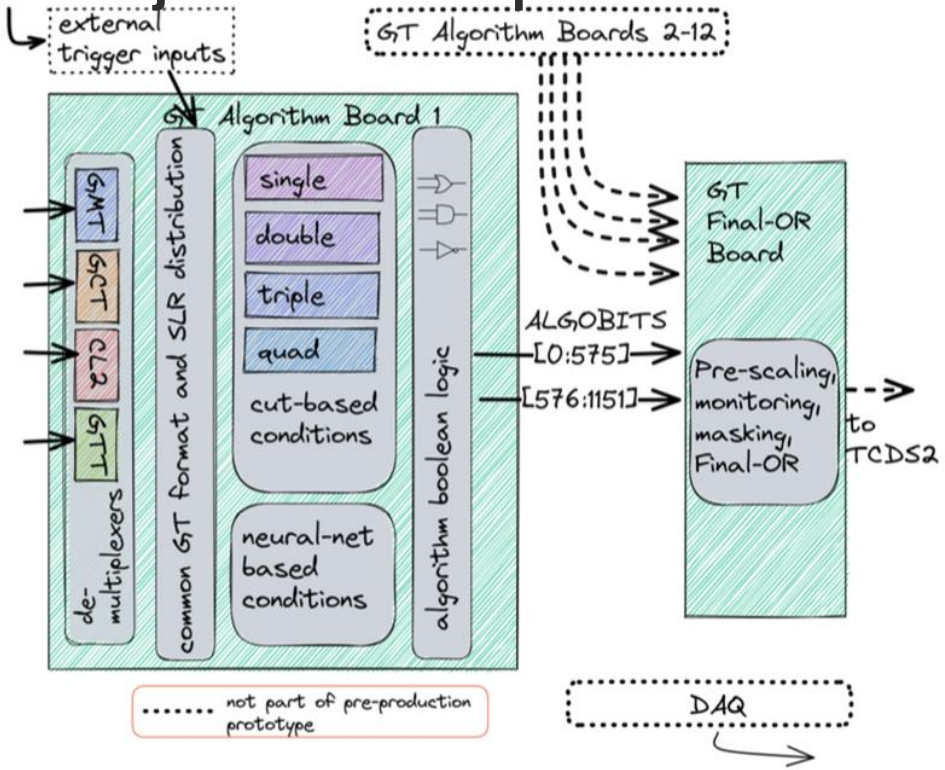
Claire Lundy

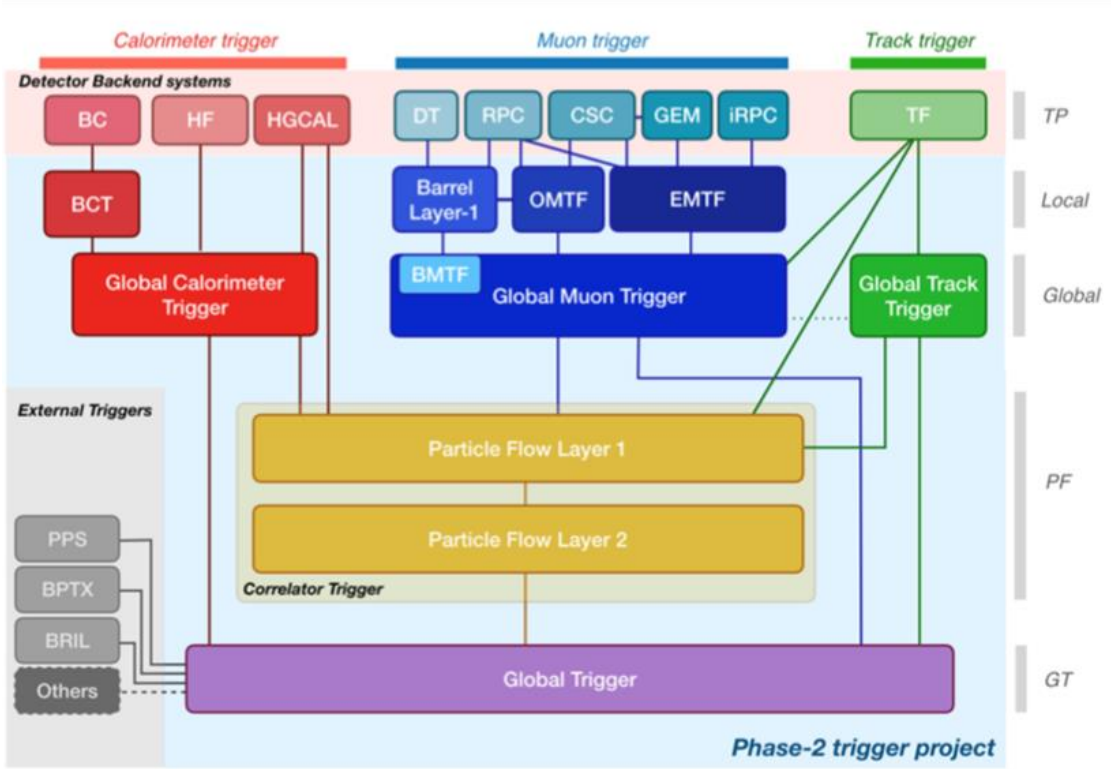
August 10th, 2023

Background

- luminosity upgrade => CMS L1T upgrade!

- GT applies cut-based algorithms to incoming object data and passes results to HLT





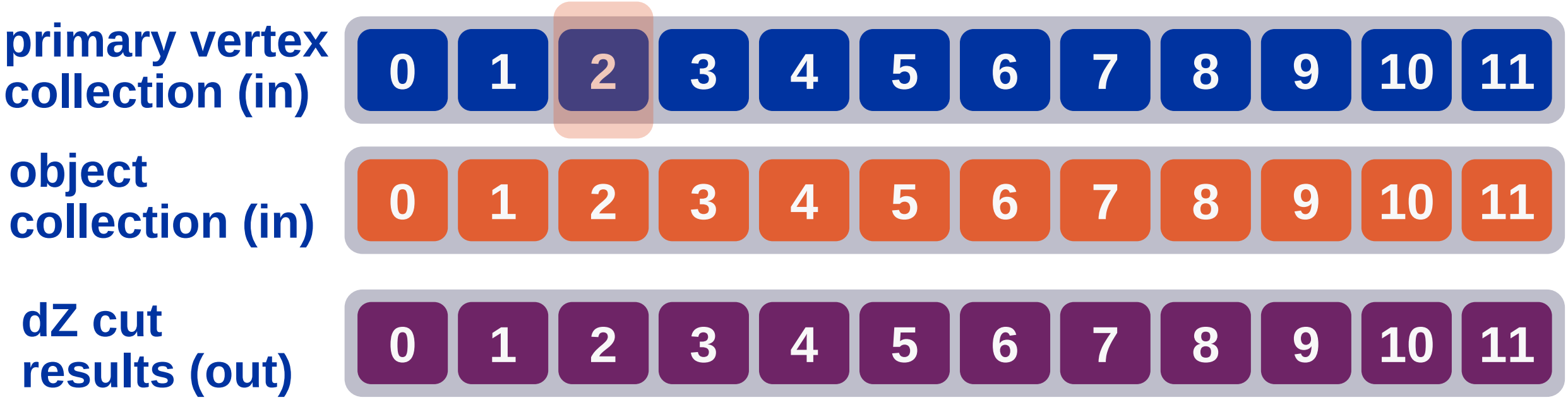
- within these cuts, we want one that finds the absolute difference in z-position between an object and a primary vertex

Primary Vertex dZ Cut

1. Grab pv z0 position from collection based on an index (0-11).

2. Compute |pv.z0 - obj.z0| for entire 12 obj collection.

3. Compare abs difference to a limit. Send 1 if it's within the limit, else 0.



dZ Cut Module

```
begin
  if rising_edge(clk_algo) then
    if valid_input = '1' then
      --grabs pv z0 position at z0 index
      if count = z0_idc then
        pv_z0 <= pv_object.z0;
      end if;

      if count = NUM_OBJECTS - 1 then
        count := 0;
      else
        count := count + 1;
      end if;

    else
      count := 0;
    end if;

  --aligning object z0 position with pv z0
  obj_z0_buffer(0) <= object_input.z0;
  obj_z0_buffer(obj_z0_buffer'high downto 1) <= obj_z0_buffer(obj_z0_buffer'high - 1 downto 0);

  pvdz := abs(signed(pv_z0) - resize(obj_z0_buffer(obj_z0_buffer'high), pvdz'length));

  if pvdz < pvdz_limit then
    pvdz_buffer(0) <= '1';
  else
    pvdz_buffer(0) <= '0';
  end if;

  --keeping total latency constant
  pvdz_buffer(pvdz_buffer'high downto 1) <= pvdz_buffer(pvdz_buffer'high - 1 downto 0);
  pvdz_result <= pvdz_buffer(pvdz_buffer'high);
```

```
type z0_reg_vector is array (natural range <>) of signed(17 downto 0);

signal pv_z0         : signed(17 downto 0);
signal obj_z0_buffer : z0_reg_vector(z0_idc downto 0);
signal pvdz_buffer   : std_logic_vector(NUM_OBJECTS - 1 - z0_idc downto 0);
```

```
  generic (
    pv_dz_cut_enabled  : boolean   := false;
    pvdz_limit         : natural   := 0;
    z0_idc             : natural range 0 to 11 := 0
  );
  port (
    clk_algo        : in std_logic;
    valid_input     : in std_logic;
    object_input    : in t_obj;
    pv_object       : in t_obj;
    pvdz_result     : out std_logic
  );
```

singleObjCondition

```
port (
  clk_algo          : in  std_logic;
  rst_algo          : in  std_logic;
  objects_valid_bx  : in  t_delayed_va
  objects_bx        : in  t_delayed_co
  algo_bit_out      : out std_logic;
  trigger_idcs      : out std_logic_ve
);
```

object collection → cut algorithms → algo_bit_out & trigger indices

singleObjCond Integration

```
185   pvdzCheck : entity work.p2gt_pvdzCheck
186     generic map (
187       pv_dz_cut_enabled => pv_dz_cut_enabled,
188       pvdz_limit        => pvdz_limit,
189       z0_idc            => z0_idc
190     )
191     port map (
192       clk_algo        => clk_algo,
193       valid_input     => valid,
194       object_input    => object,
195       pv_object       => pv_object,
196       pvdz_result     => pvdz_result
197     );
```

```
168   delay_results : process (clk_algo)
169   begin
170     if rising_edge(clk_algo) then
171       result_buffer <= ((and int_result_equal) and (and int_result_less) and (and int_result_greater) and object_input.valid and v
172         result_buffer(result_buffer'high downto 1);
173       result_buffer(result_buffer'high - ADVANCED_CHECKS_LATENCY) <= relIso_result and result_buffer(result_buffer'high - ADVANCED
174       result_buffer(result_buffer'high - PVDZ_LATENCY) <= pvdz_result and result_buffer(result_buffer'high - PVDZ_LATENCY + 1);
175       valid_buffer <= valid_input & valid_buffer(valid_buffer'high downto 1);
```

doubleObjCond Integration

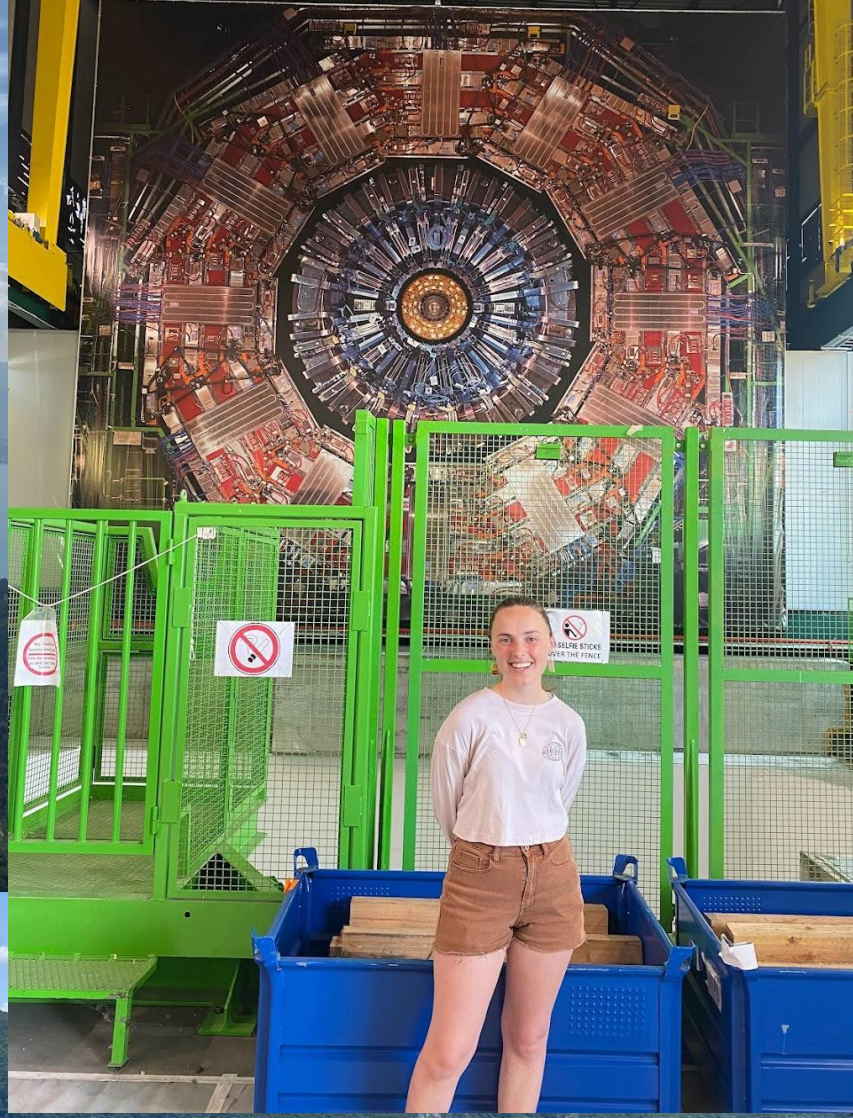
```
278   delay_pvdz_cuts : process (clk_algo)
279     variable timestep : natural range 0 to NUM_OBJECTS := 0;
280     variable pvdz_results1, pvdz_results2 : std_logic_vector(NUM_OBJECTS - 1 downto 0);
281     variable pvdz_cut_results : std_logic_vector(NUM_OBJECTS - 1 downto 0);
282
283   begin
284     if rising_edge(clk_algo) then
285       pvdz_results1 := pvdz_results(0) & pvdz_results1(pvdz_results1'high downto 1);
286       pvdz_results2 := pvdz_results(1) & pvdz_results2(pvdz_results2'high downto 1);
287
288       for i in NUM_OBJECTS - 1 downto 0 loop
289         pvdz_cut_results(i) := pvdz_results1_store(i) and pvdz_results2(0);
290       end loop;
291
292       int_result_pvdz_cuts <= pvdz_cut_results & int_result_pvdz_cuts(int_result_pvdz_cuts'high downto 1);
293
294       if objects_valid_buffer(objects_valid_buffer'high - PVDZ_LATENCY + 1) = '1' then
295         if timestep < NUM_OBJECTS - 1 then
296           timestep := timestep + 1;
297         else
298           timestep := 0;
299           pvdz_results1_store <= pvdz_results1;
300         end if;
```

```
217     pvdzCheck : entity work.p2gt_pvdzCheck
218       generic map (
219         pv_dz_cut_enabled => pv_dz_cut_enabled(i),
220         pvdz_limit        => pvdz_limit(i),
221         z0_idc            => z0_idc(i)
222       )
223       port map (
224         clk_algo        => clk_algo,
225         valid_input     => valid,
226         object_input    => objects(i),
227         pv_object       => pv_objects(i),
228         pvdz_result     => pvdz_results(i)
229       );
```

That’s all!

- dZ module is now written, integrated into single, double, and triple object condition files, and tested for each
- Missing for the quadruple object condition, but the framework is there…just needs a bit more testing.
- Big thanks to my supervisor, Benjamin Huber, and the P2GT group–it was a really cool project and a fun summer :)

Thanks!



References

Bortolato, G., Cepeda, M., Heikkilä, J., Huber, B., Leutgeb, E., Rabady, D., & Sakulin, H. (2023). *The Level-1 Global Trigger for Phase-2: Algorithms, configuration and integration in the CMS offline framework*.

CMS Collaboration. (2020). *The Phase-2 Upgrade of the CMS Level-1 Trigger*. 121–129, 188–194.

singleObjCond testing

```
69      L1TP2GTSingleObjectCond singleMu_14;
70      singleMu_14.setCollection(evt.getIntermediateObjects(GMT_Tk_Mu));
71      singleMu_14.setPtCut(i + 14, std::nullopt);
72      singleMu_14.setRegionsAbsEtaLowerBounds({0, 10});
73      singleMu_14.setRegionsMaxIso({7, 12});
74      singleMu_14.setPrimVertCut(std::nullopt, 1000, 0, evt.getIntermediateObjects(GTT_PrimaryVert));
75      evt.setTriggerBit(
76          singleMu_14.runAlgo(), ALGO_OUTPUT_CHANNEL_MAP.at(i), ALGO_OUTPUT_OFFSET_MAP.at(i) * NUM_ALGOS + 0);
77      if (singleMu_14.runAlgo()) {
78        ++m_triggersFired.at(ALGO_OUTPUT_OFFSET_MAP.at(i) * NUM_ALGOS + 0);
79      }
```

doubleObjCond testing

```
171     L1TP2GTDoubleObjectCond doubleMuTau2_9_er_8to88_3to33;
172     doubleMuTau2_9_er_8to88_3to33.setCollection1(evt.getIntermediateObjects(GMT_Sa_PromptMu)); //GMT_Sa_PromptMu
173     doubleMuTau2_9_er_8to88_3to33.setCollection2(evt.getIntermediateObjects(GCT_Jets)); //GCT_Jets
174     doubleMuTau2_9_er_8to88_3to33.setPtCuts(i + 2, std::nullopt, 9, std::nullopt);
175     doubleMuTau2_9_er_8to88_3to33.setEtaCuts(8, 88, 3, 33);
176     doubleMuTau2_9_er_8to88_3to33.setPrimVertCuts(
177         std::nullopt, 1000, 0, std::nullopt, 1000, 0, evt.getIntermediateObjects(GTT_PrimaryVert));
178     evt.setTriggerBit(doubleMuTau2_9_er_8to88_3to33.runAlgo(),
179                       ALGO_OUTPUT_CHANNEL_MAP.at(i),
180                       ALGO_OUTPUT_OFFSET_MAP.at(i) * NUM_ALGOS + 8);
181     if (doubleMuTau2_9_er_8to88_3to33.runAlgo()) {
182       ++m_triggersFired.at(ALGO_OUTPUT_OFFSET_MAP.at(i) * NUM_ALGOS + 8);
183     }
```