

# Advances towards a tracker based on APD sensors

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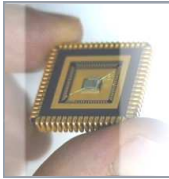
<sup>1</sup>Systems Instrumentation and Communications (SIC) – Dept. of Electronics

<sup>2</sup>Dept. of Structure and Constituents of Matter

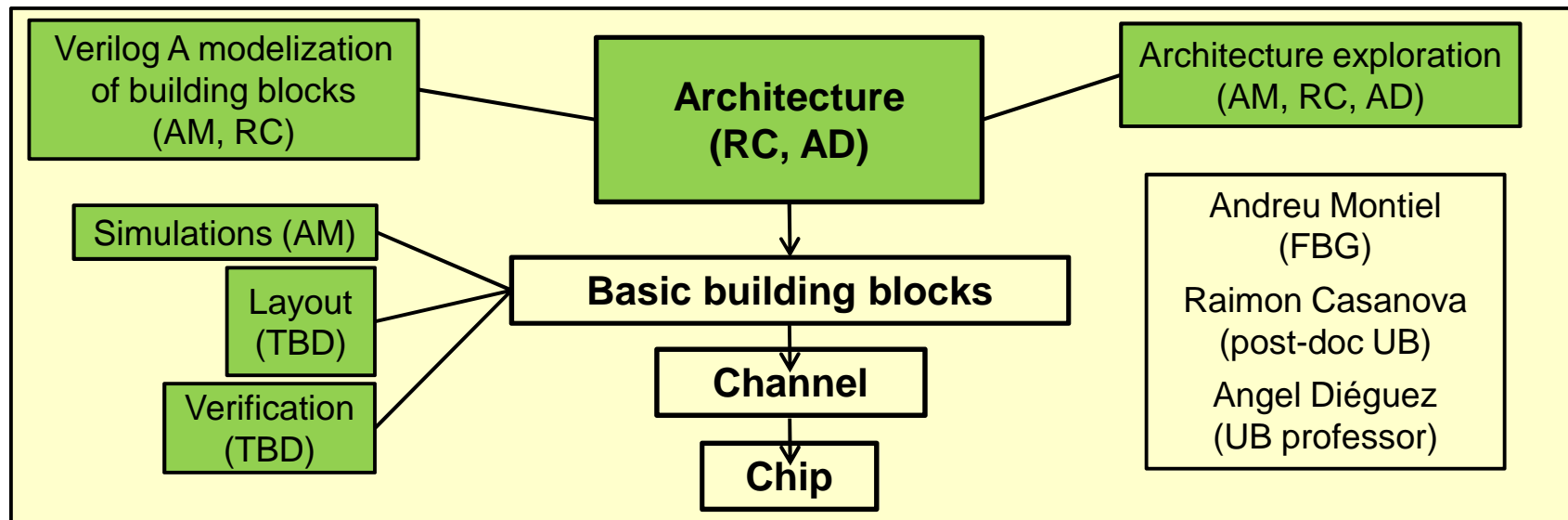
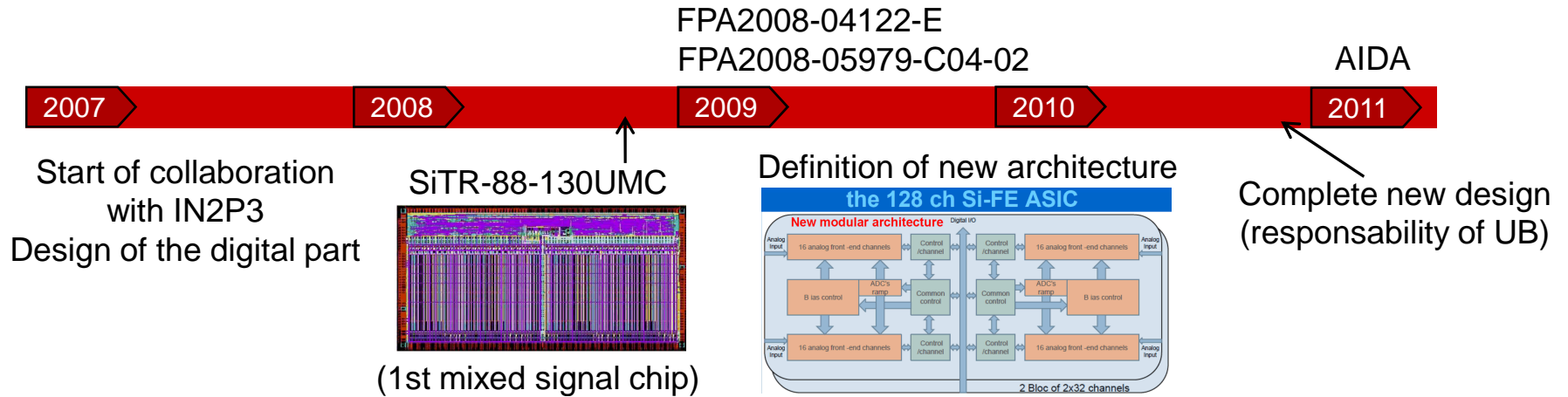
University of Barcelona (UB), Barcelona, Spain

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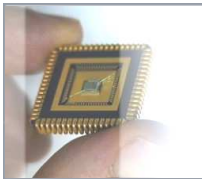
[evilella@el.ub.es](mailto:evilella@el.ub.es)



## SiLC timeline and current personnel responsible



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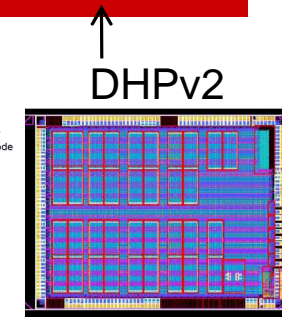
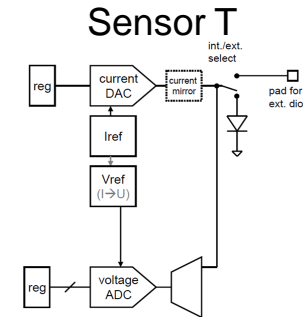
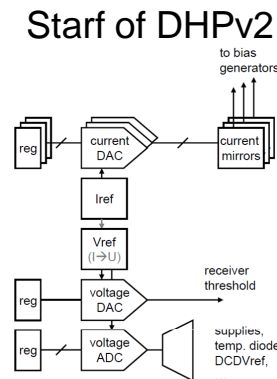
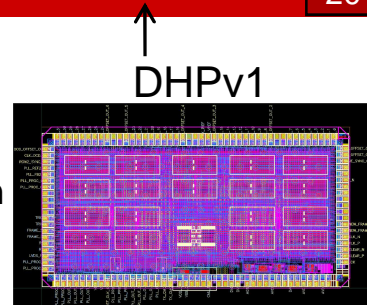
## DEPFETs timeline and current personnel responsible

FPA2008-05979-C04-02

FPA2010-21549-C04-01

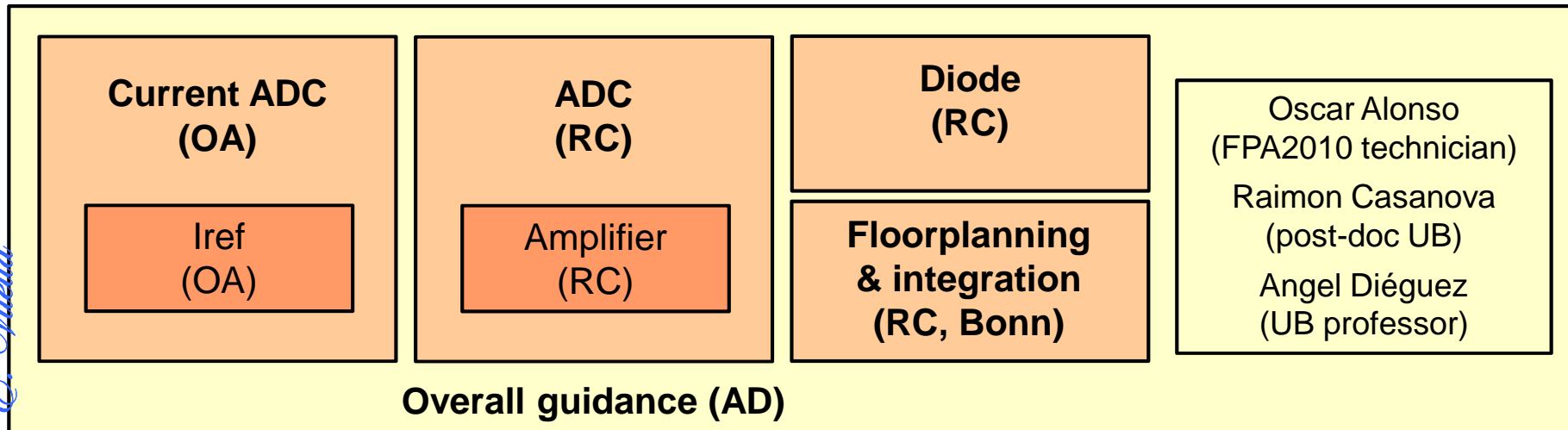


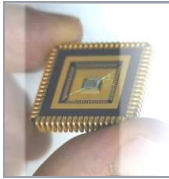
Slow control  
Digital (JTAG)  
Analog blocks for configuration  
and basic blocks (bandgap,  
DAC, ADC, amplifier)



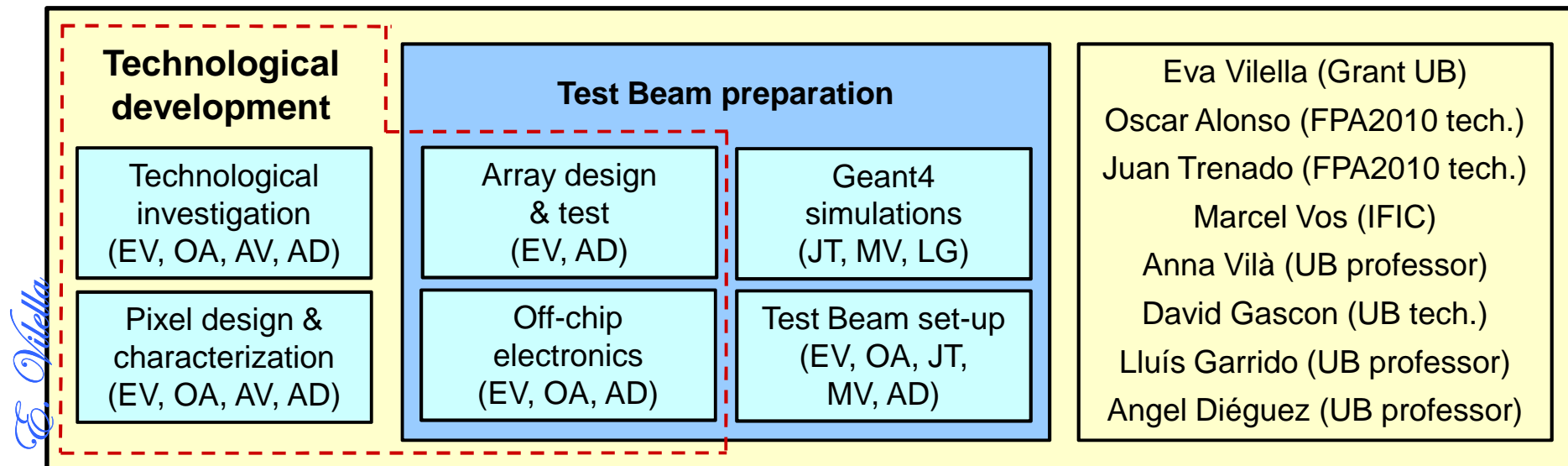
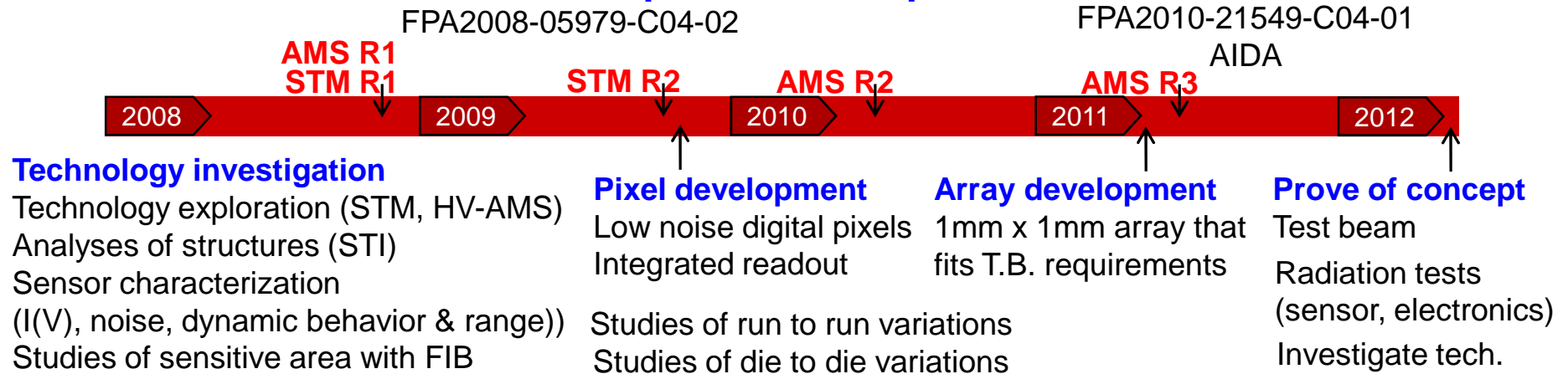
IBM 90nm  
LAST RUN!!!

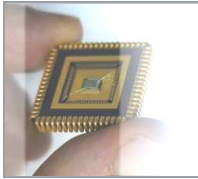
E. Villega





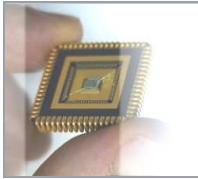
## ○ APDs timeline and current personnel responsible





## ► Outline

- AMS R2 – APDs Chip 2010
  - Readout electronics for low noise pixel detectors
  - 3 x 3 GAPD array
  - Results
- AMS R3 – FLC\_APD\_v1 2011
  - Submitted chip and circuits
  - 1mm x 1mm GAPD array
- Test beam preparation
- Conclusions



## ○ Geiger mode Avalanche Photodiodes (GAPDs)

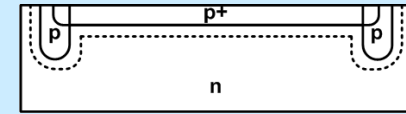
### • Pros

- ✓ High intrinsic gain
- ✓ Accurate time response with possible single BX detection
- ✓ Compatible with standard CMOS processes

### • Cons

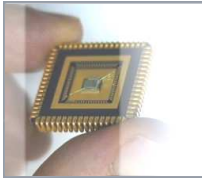
- × Afterpulses
  - × Dark counts
- } → Noise hits, indistinguishable from real events
- × Reduction of detector performance
  - × Increase of memory area to store the total hits

Avalanche photodiodes in standard CMOS technologies



## ○ It is mandatory to reduce noise hits! How?

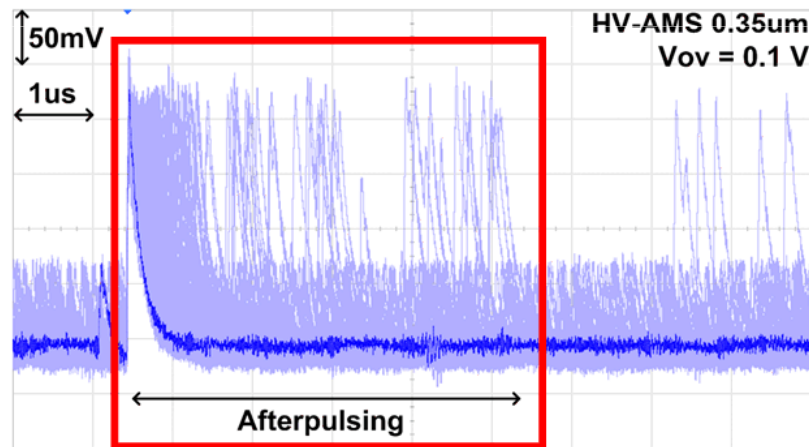
- Using HV-AMS 0.35 $\mu$ m technology
- Introducing readout electronics for low noise GAPD pixels



## ○ Intrinsic noise sources

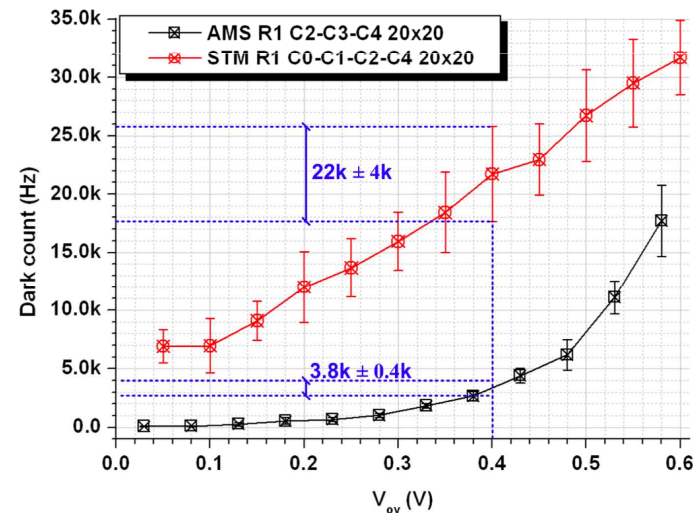
### Afterpulses.

- Correlated pulses due to the random release of carriers that were trapped during a previous avalanche.
- Depends on the trap density and  $I_{diode}$ .

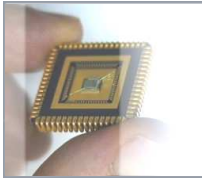


### Dark counts.

- Spurious avalanches caused by thermal or tunnel carriers.
- Depends on the technology, the sensitive area,  $V_{ov}$  and T.

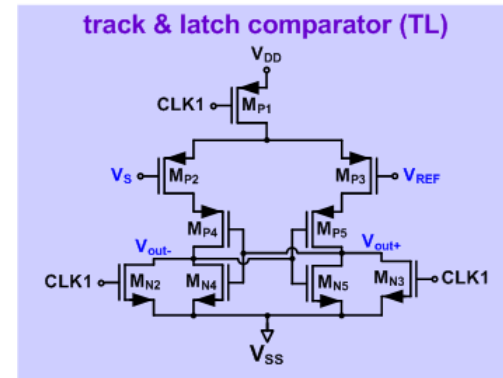
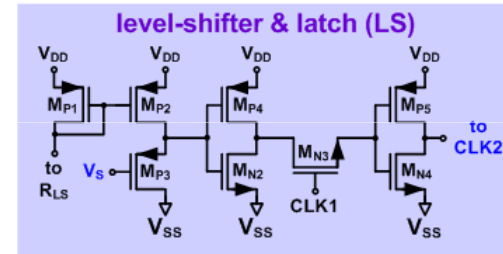
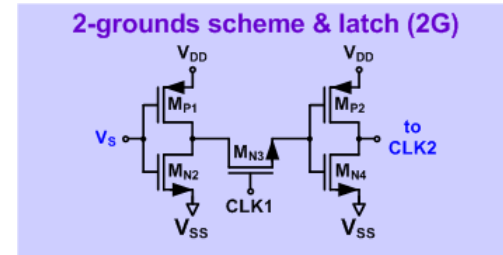
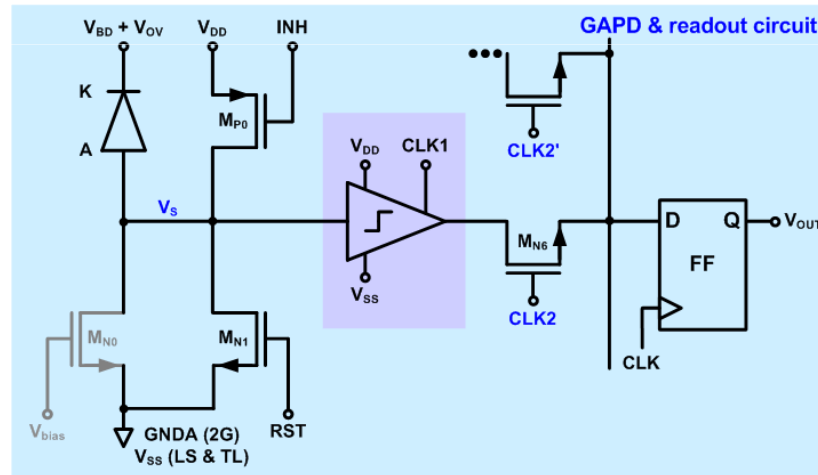
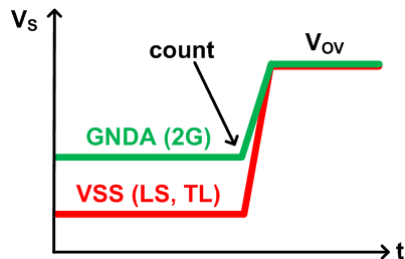
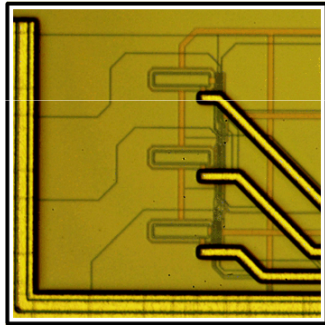


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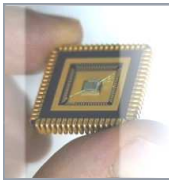
○ We developed readout circuits for low noise GAPD pixels

- Monolithically integrated with the sensor
- Comprised of quenching transistor and 3 different readout circuits
- Digital output



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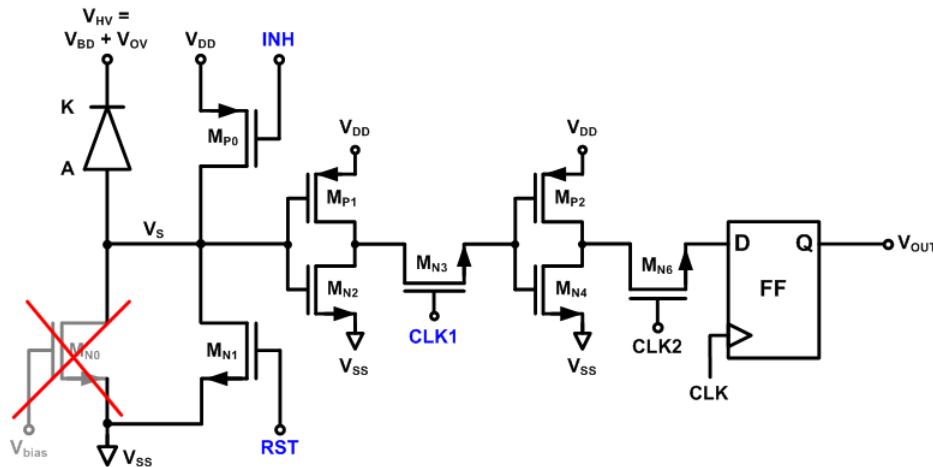
## Free running

- The APD is always active.

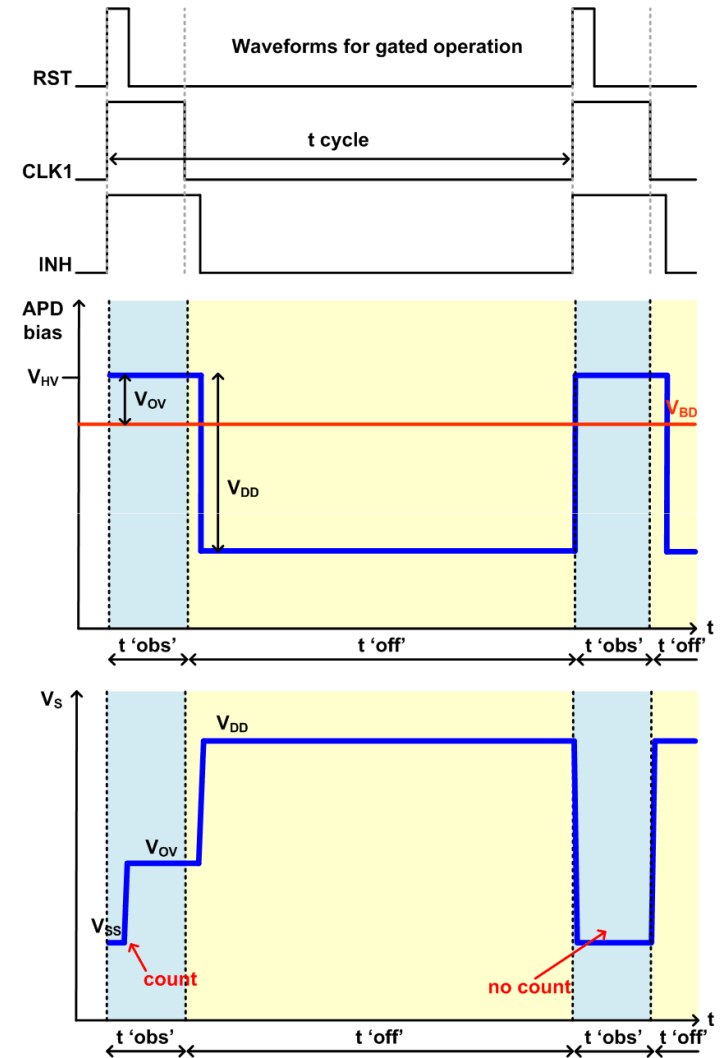
## Gated operation

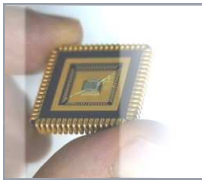
- The APD is active for short periods of time by using a triggering signal.

- Avoids afterpulsing
- Reduces dark count **Good!**
- Improves detector performance

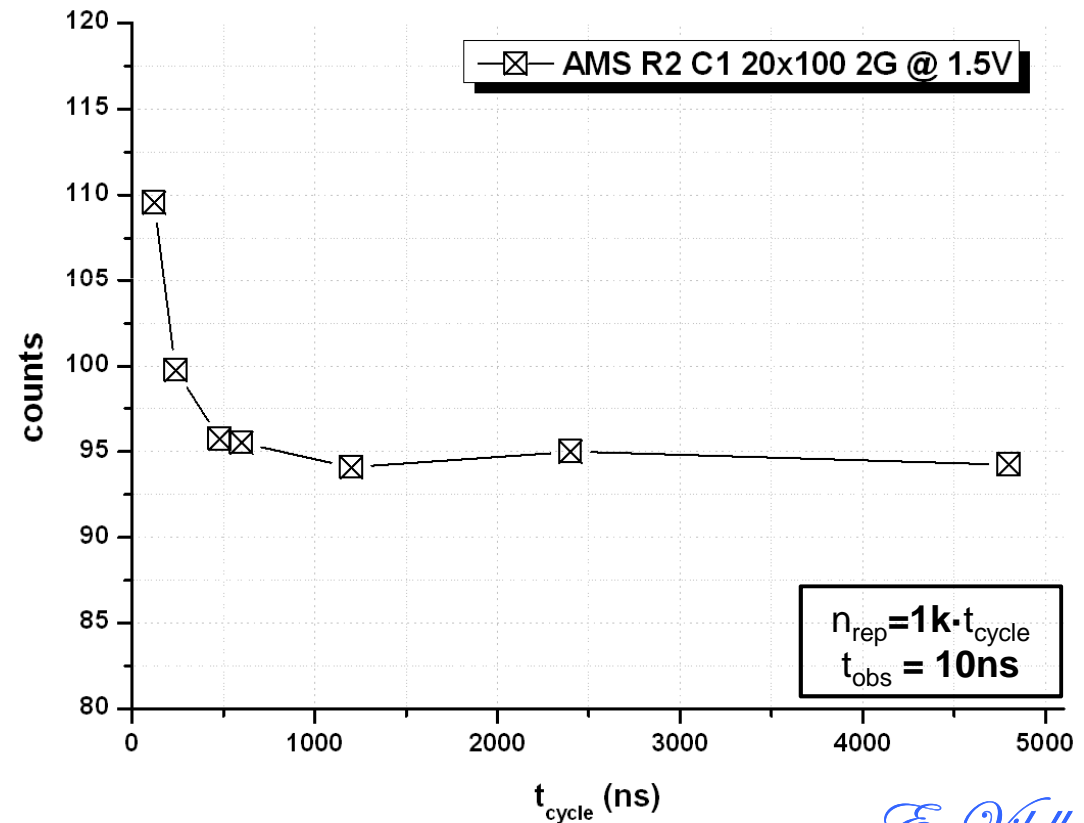
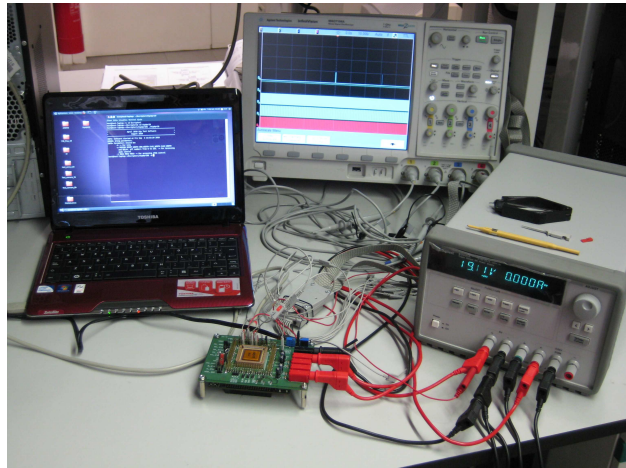


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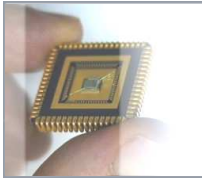




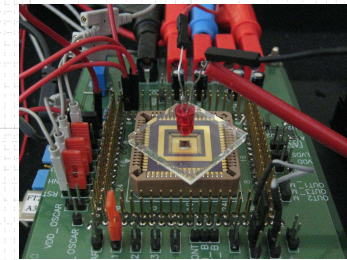
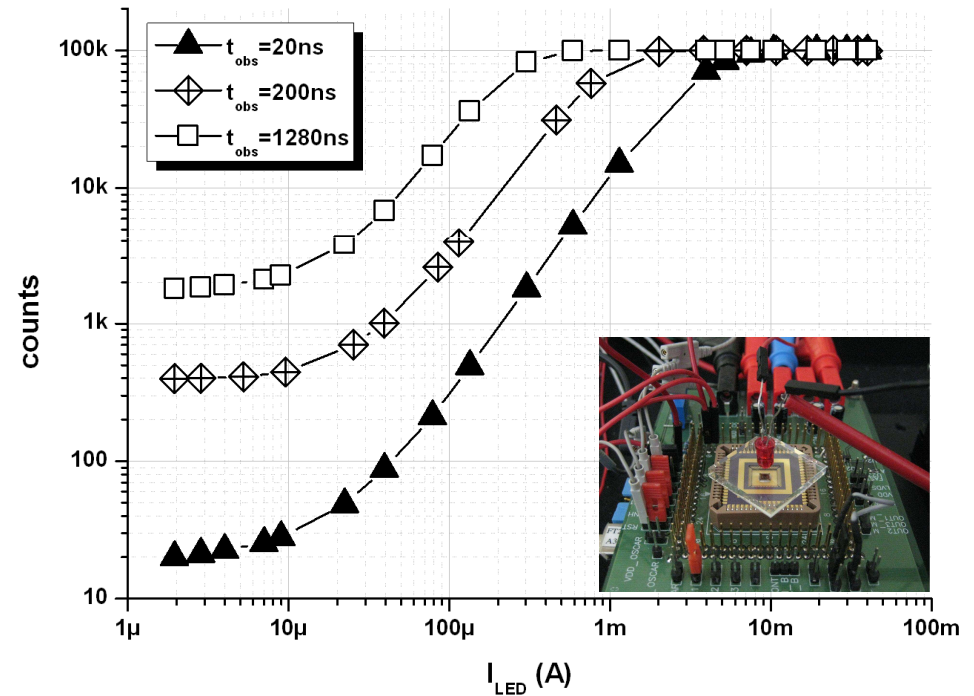
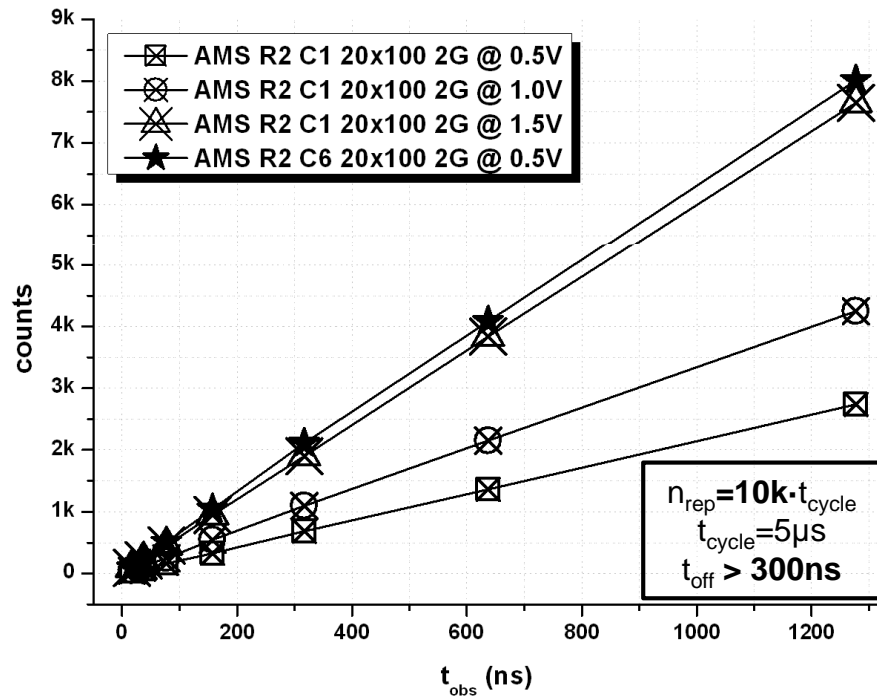
- It is possible to eliminate the afterpulsing probability by means of the gated operation.
  - Leaving long enough t 'off' periods of 300ns.



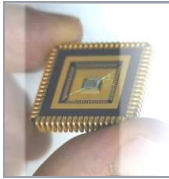
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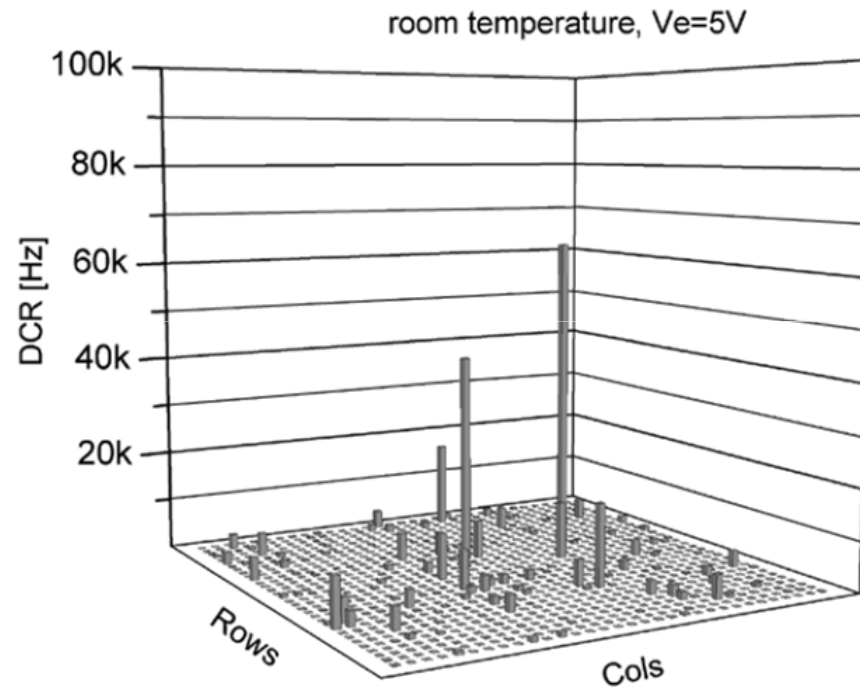
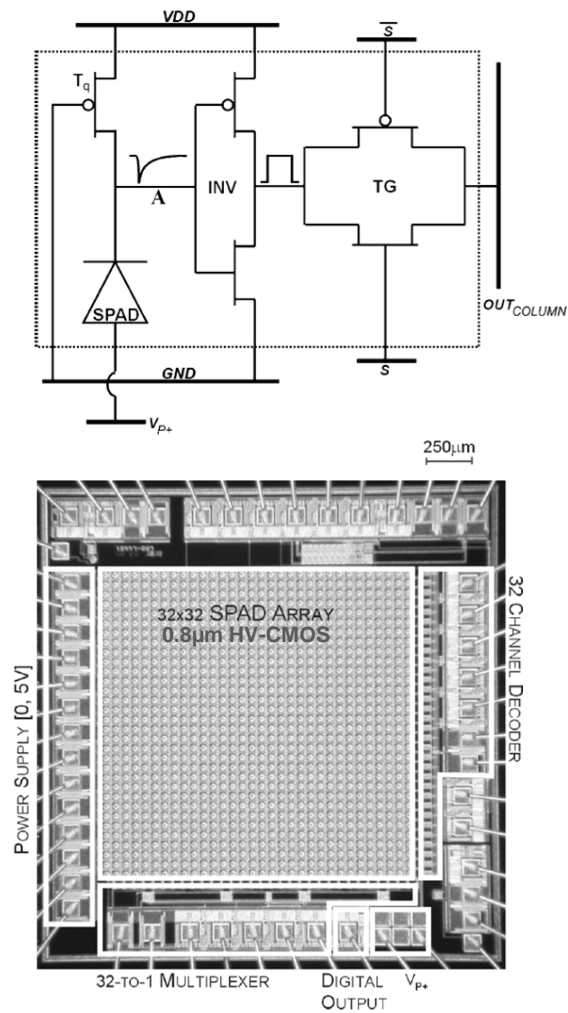
- Dark counts are reduced by using low overvoltages and short  $t_{obs}$ .
- Reducing noise hits, the dynamic range is extended.



Dynamic range increased from 9 to 14 bits

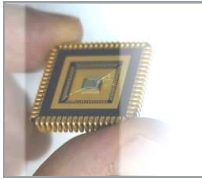


- Presence of dead pixels in GAPD arrays.

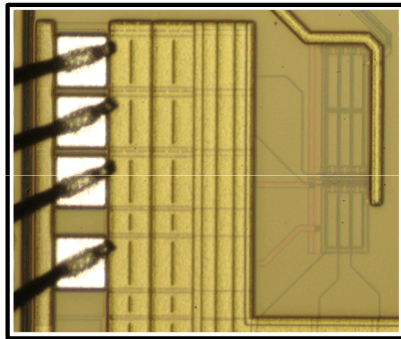


C. Niclass et al., "Design and characterization of a CMOS 3-D image sensor based on single photon avalanche diodes", IEEE Journal of Solid-State Circuits, vol. 40, no. 9, 2005.

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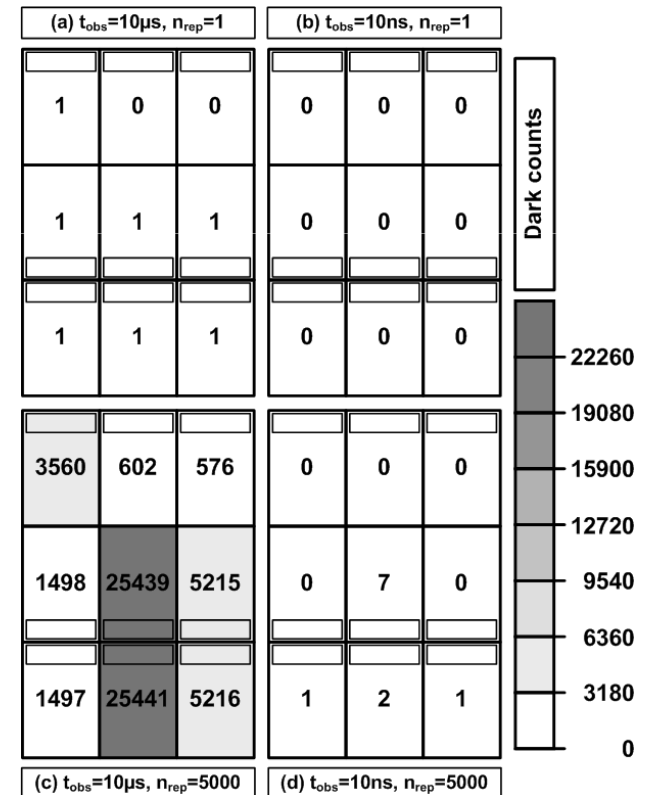
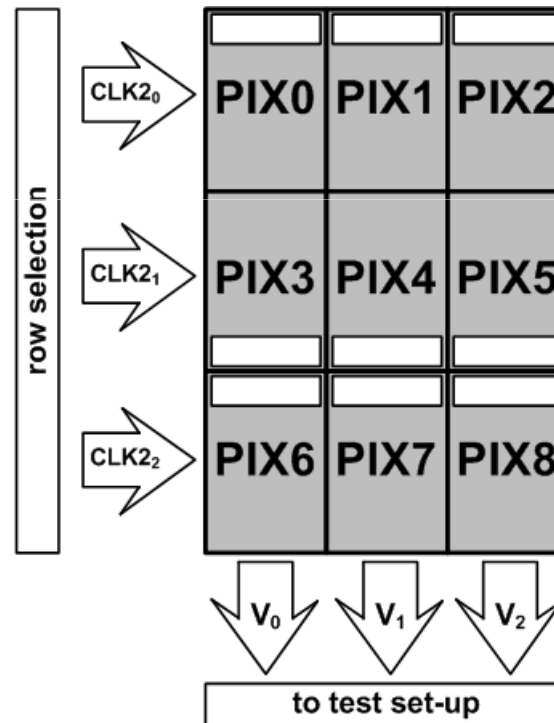


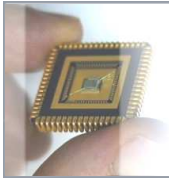
- The gated operation is also effective in reducing pixel-to-pixel disparities in GAPD arrays.
  - 3 x 3 GAPD array with level-shifter sensing circuit.
  - Sequential reading by columns (CLK2 acts as a row selector).



**No detector blindness because of the noise!**

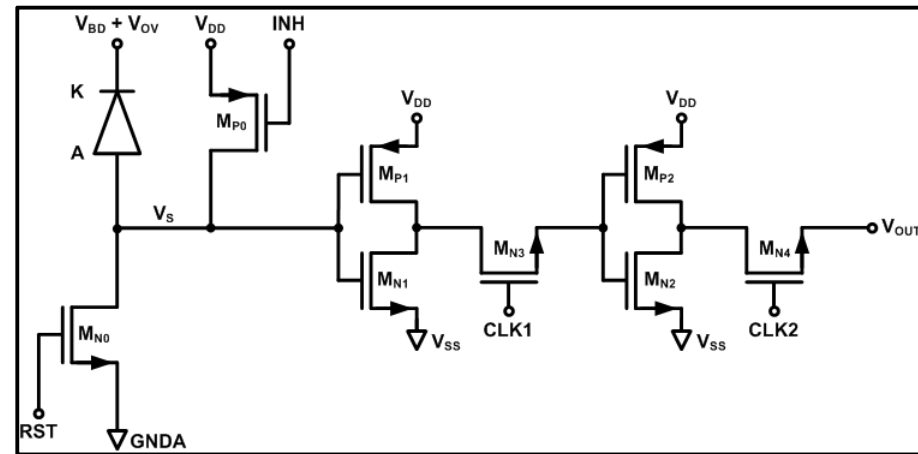
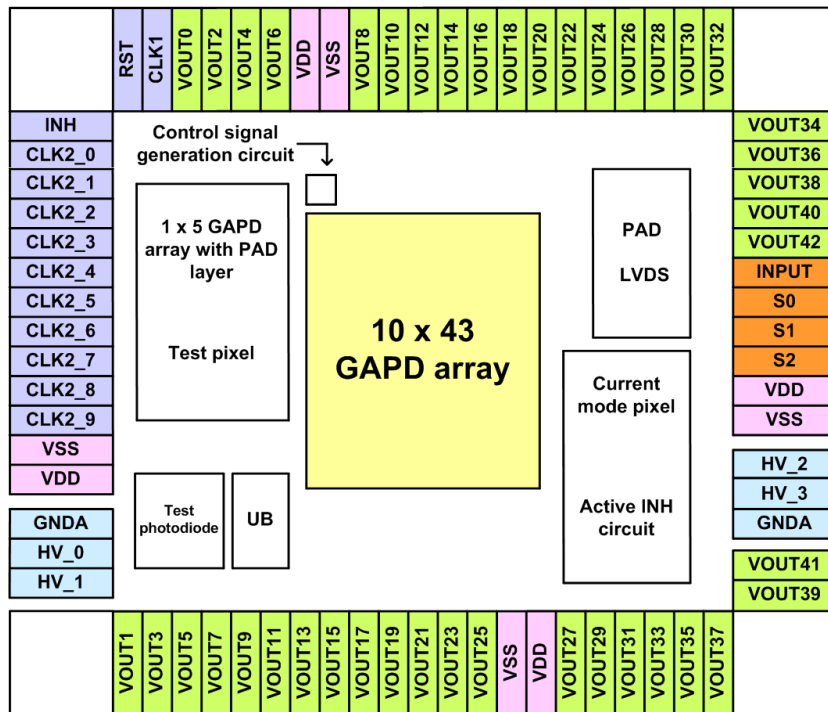
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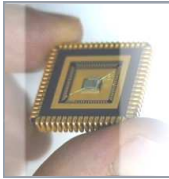


## ○ 10 x 43 GAPD array

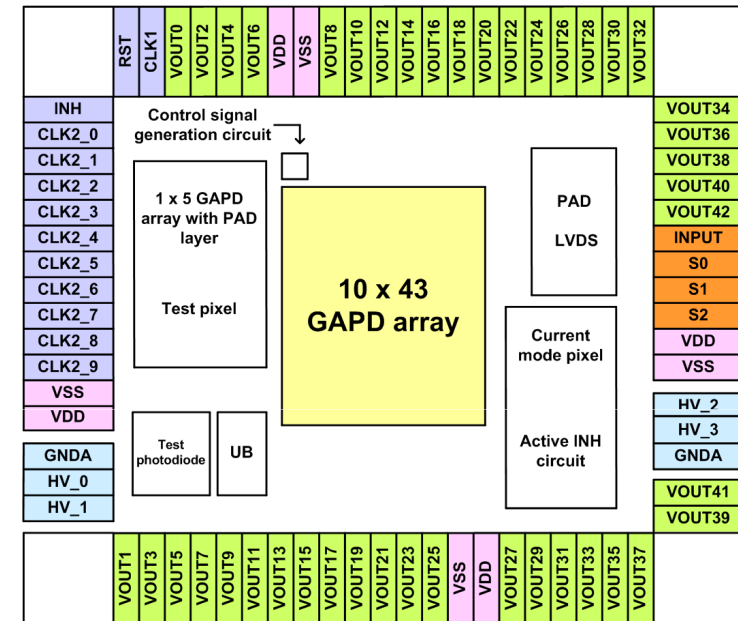
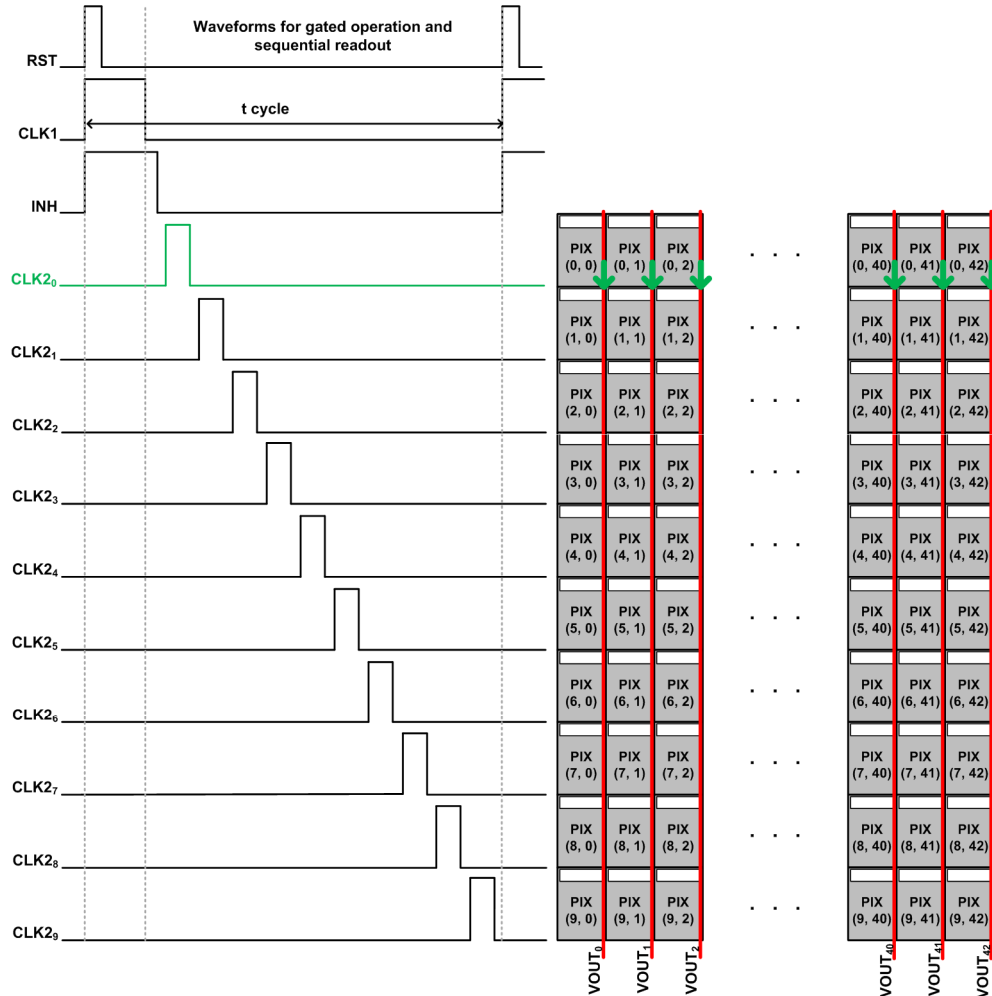
- Total occupied **area** = 1025 $\mu\text{m}$  x 1400 $\mu\text{m}$ .
- **Pixel** comprised of GAPD, quenching transistor, sensing element (2-grounds scheme), storage element (latch) and pass-gate to allow sequential reading.
- GAPD mode of operation is **gated acquisition**.



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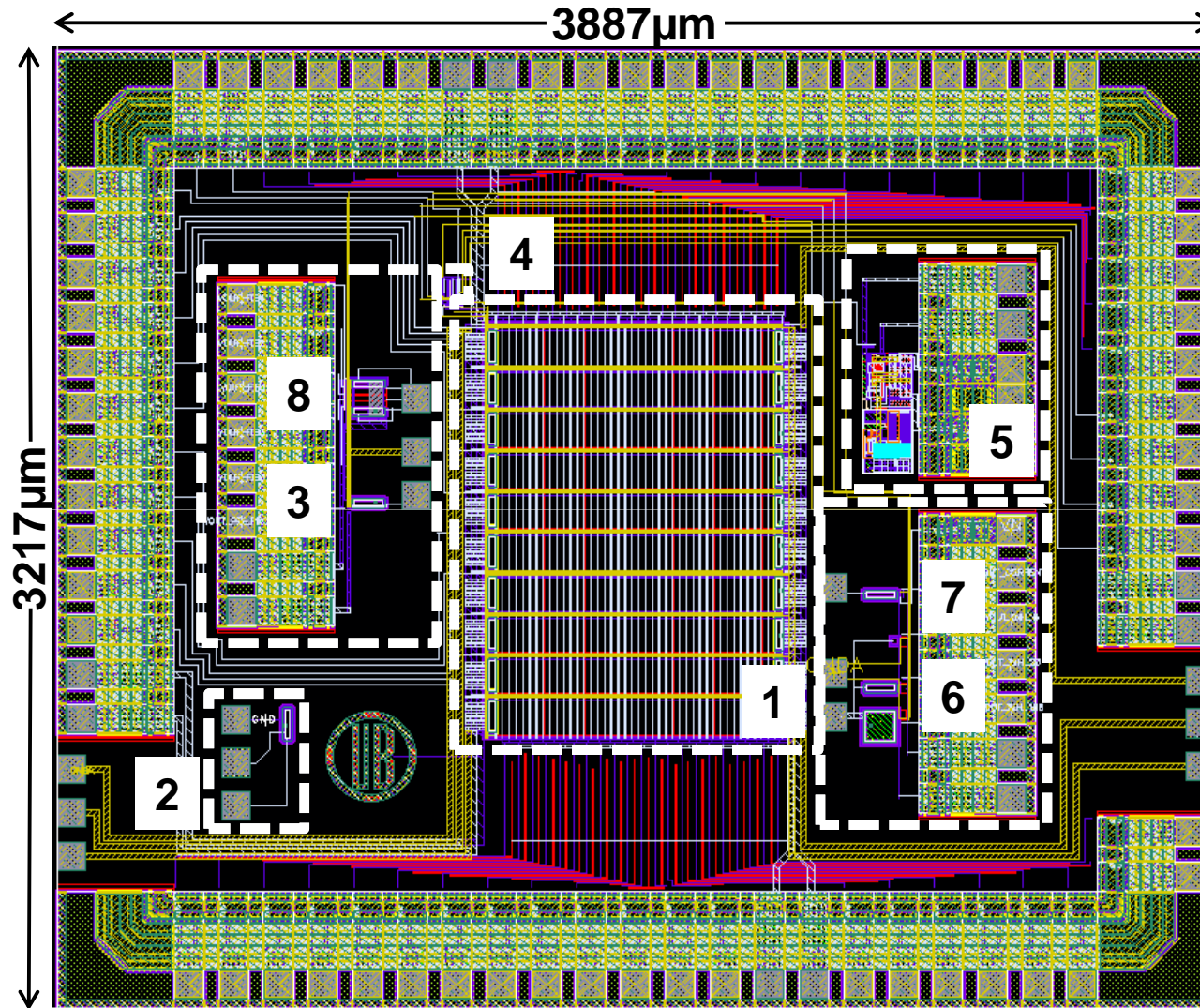
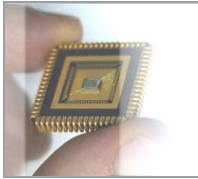


○ **Sequential reading by columns** (CLK2 acts as a row selector).



- GAPD bias (4) and ground (2)
- Electronics bias (4) and ground (4)
- Selection signals (4)
- Pixel control signals (13)
- Pixel outputs (43)

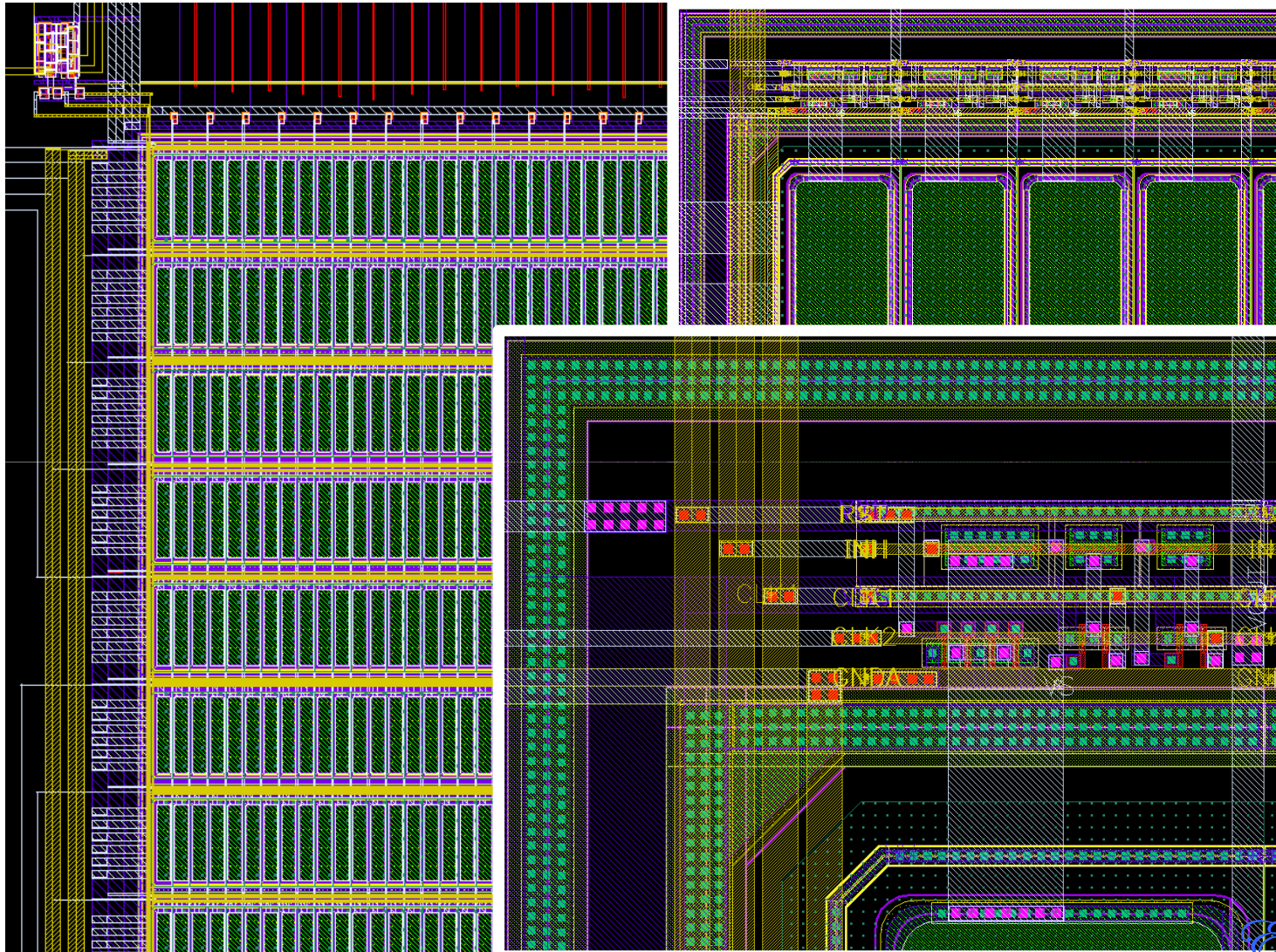
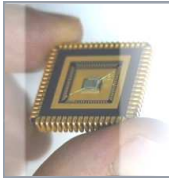
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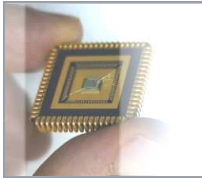
- 1. 10 x 43 GAPD array
- 2. Test photodiode
- 3. Test pixel
- 4. Control signal generation circuit
- 5. Pad LVDS
- 6. Active inhibit pixel
- 7. Current mode pixel
- 8. 1 x 5 GAPD array with PAD layer

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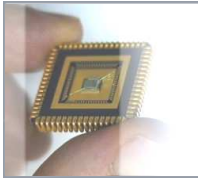


### ○ GAPD array particle detection efficiency?

- Next steps
  - Test beam at **DESY** with 6GeV electrons (2011)  
Distinguish detection between **neighbour pixels**
  - Test beam at **CERN** with 120GeV pions (2012)  
Distinguish detection in an **specific region of the pixel**

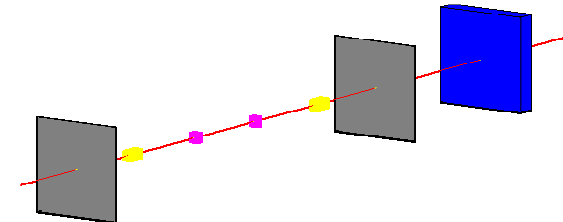
### ○ Meanwhile working on the test set-up...

- Main worries → Distortion in the particle path caused by test set-up materials
- Different ideas to reduce total material thickness
  - FLC\_APD\_v1 with thin silicon wafer of 250 $\mu$ m
  - No chip package & wire bond the chip directly to the PCB
  - PCB perforated under the chip
- Running simulations with **Geant4**
  - Software to simulate the passage of particles through matter



## Geant4 studies

- Performed using 2 silicon wafers, 2 aluminum layers and 2 or 4 scintillators (test beam set-up)
- Sources → electrons (6GeV) and pions (120GeV)



## Results so far

	Detector at 2cm	X-Mean ( $\mu\text{m}$ )	X-Sigma ( $\mu\text{m}$ )	Y-Mean ( $\mu\text{m}$ )	Y-Sigma ( $\mu\text{m}$ )	Peak ( $\mu\text{m}$ )	R-Sigma ( $\mu\text{m}$ )
electrons	T.B. with 2 Sc.	-0.0801	16.2	0.008922	16.2	13	12.48
	T.B. with 4 Sc.	0.1474	17.6	0.07584	17.68	13	13.34
pions	T.B. with 2 Sc.	0.008092	0.7767	0.001725	0.7814	0.7	0.5769
	T.B. with 4 Sc.	0.0001457	0.8606	0.003204	0.8625	0.8	0.6955

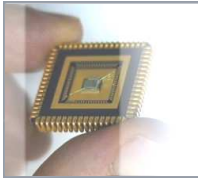
  

	Detector at 10cm	X-Mean ( $\mu\text{m}$ )	X-Sigma ( $\mu\text{m}$ )	Y-Mean ( $\mu\text{m}$ )	Y-Sigma ( $\mu\text{m}$ )	Peak ( $\mu\text{m}$ )	R-Sigma ( $\mu\text{m}$ )
electrons	T.B. with 2 Sc.	-0.2033	16.91	0.18	46.88	35	39.25
	T.B. with 4 Sc.	0.3413	50.18	-0.06107	50.39	37	41.75
pions	T.B. with 2 Sc.	0.004929	2.092	0.00374	2.1	1.8	1.614
	T.B. with 4 Sc.	0.000984	2.301	0.00865	2.308	1.9	1.749

## We need distortion lower than pixel width (20 $\mu\text{m}$ )!

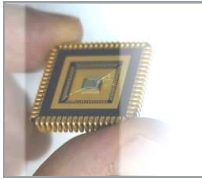
- T.B. at DESY (electrons) → distortion is  $\sim 16\mu\text{m}$
- Complicated to characterize (further studies are needed)
- T.B. at CERN (pions) → distortion is  $\sim 0.5\mu\text{m}$
- To measure detector resolution and active regions we need 1-2 $\mu\text{m}$  precision

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## ○ Conclusions

- The gated acquisition is the best mode of operation for synchronized systems.
  - Avoids afterpulses and reduces dark count.
  - Eliminates dead pixels.
  - Uniformzes noise characteristics.
- We expect to receive the 1mm x 1mm GAPD array next August.
  - It will allow us to test if GAPD arrays with HV-AMS 0.35 $\mu$ m standard technology are efficient in particle detection.



# Thank you for your attention

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Questions and comments are welcome