

Characterization of small Scalable Readout System



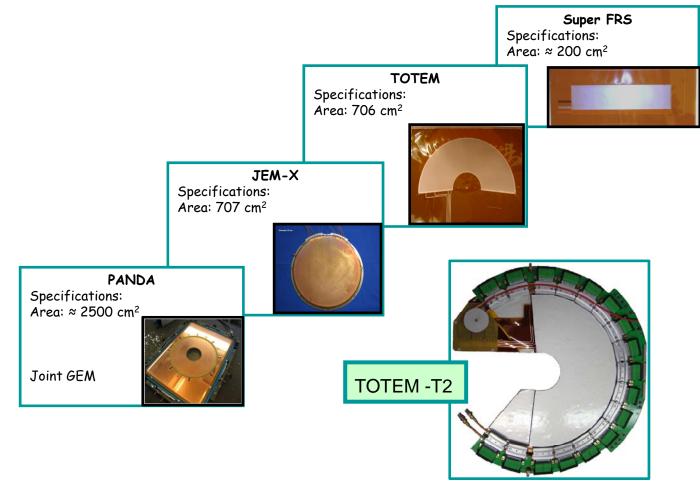
OUTLINE

- Introduction and Motivation
- Small Scalable Readout System
- Installation, Configuration and First Tests
- TODO List



INTRODUCTION

The Helsinki Institute of Physics is currently trying to develop a methodology for characterization of the GEM foils. This will includes: hole uniformity studies, classification of defects and long term stability. This is a common effort together with CERN.

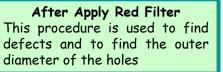


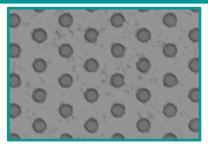


Characterization of Small Scalable Readout System at HIP

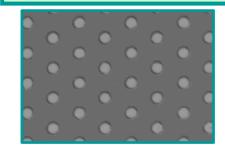
MOTIVATION



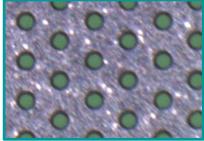




After Apply Green Filter This procedure is used to find blind holes and to measure the inner diameter of the holes



New System Based on 9 Mpix camera with integrated telecentric optics for this setup one pixel corresponds to 1.7 x 1.7 microns





Francisco García - RD51 Collaboration Meeting at GSI - WG5 Electronics

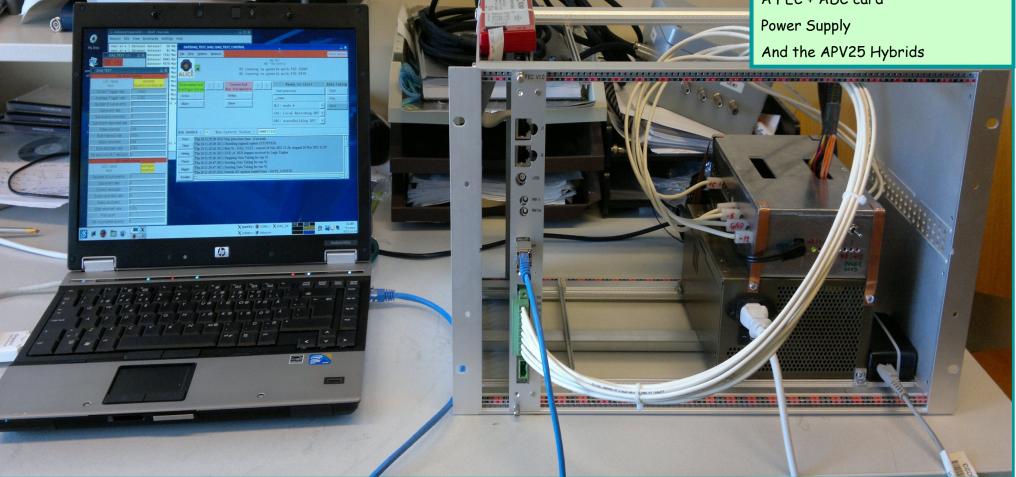


Characterization of Small Scalable Readout System at HIP

Small Scalable Readout System

The system consists of: Computer with DATE installed

A FEC + ADC card





X	DAQ_TEST <2>				
GD (C (1)				
SYN STAI STAI STAI RUN	_RUNNING CHRONOUS RTING RTING_PDSREC RTING_EVB NING				
RUN	X DAQ_IEST	_ X			
STC	LIC status	aloneldc	2		
STC	LDC name host	localhost.localdomain			
LD	Current Trigger rate	0.000			
NO	Average Trigger rate	1724.138			
SYN	Number of sub-events	50000			
STA STA	Sub-event rate	0			
STA	Sub-events recorded	50004			
STA	Sub-event recorded rate	0			
WAI	Bytes injected	1007219376			
RUN	Byte injected rate	0 B/s			
STC	Bytes recorded	1007219376			
STC	Byte recorded rate	0 B/s			
STC	Nb. evts w/o HLT decision	0			
	GDC status				
3	GDC name host	alonegdc localhost			
	Number of sub-events	0			
	Sub-event rate	0			
	Events recorded	0			
64	Event recorded rate	0			
	Bytes recorded	0			
	Byte recorded rate	0 B/s			
	File count	0			
	Nb. incomplete events	0			

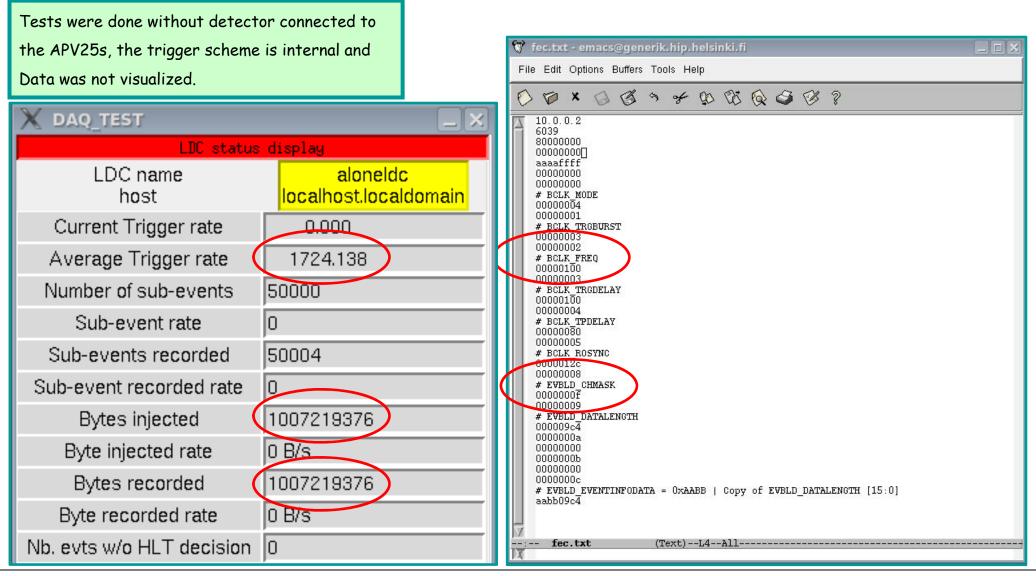
DATE installation and Slow Control by Filippo Costa \rightarrow Learn how to run the DAQ and a setup configuration files.

AMORE installation by Kondo Gnanvo \rightarrow Get some scripts for data visualization and analysis.

Electronics Configuration by Sorin Martoiu \rightarrow APV25, FEC and ADC parameters.

-	1			00000004								
D	X DATEDAQ_TEST_DAQ::DAQ_TEST_CONTROL											
	<u>File View</u>	<u>O</u> ptions	<u>W</u> indows						Status updated			
	ALICE DAQ_TEST DAQ - Run Control HI running on generik with PID 9700 RC running on generik with PID 9477											
	Disconn	ected	$\langle \rangle$	Connected	<		Ready to start		Data Taking			
	Configu	ation		Run Parameters			Start processes		Start			
	Define			Define			🗆 EDM		Stop			
	Show			Show			HLT: mode A	v	Abort			
2							LDC: Local Recording ON	v				
1							GDC: eventBuilding OFF	v				
RUN NUMBER : 229 Run Control Status : READY												
	Trace	Wed 06 13	3:36:59 (HI) Stop processes time :	5 sec	onds			7			
	Clear	Clear Wed 06 13:36:58 (RC) Run stopped Wed 06 13:36:58 (RC) Disabling logbook update (STOPPED)										
	Debug	J										
	Pause	Pause Wed 06 13:36:54 (RC) END_of_RUN request received by Logic Engine Wed 06 13:36:53 (aloneldc) End of run requested from ALONELDC										
	Bigger	Wed 06 13:36:24 (RC) Starting Data Taking for run 229										
	Wed 06 13:36:23 (HI) Current RC options loaded from : DATE_CONFIG Smaller											







In both cases the file size increases linearly with the increasing

of amount of triggers.

EVBL_CHMASK:

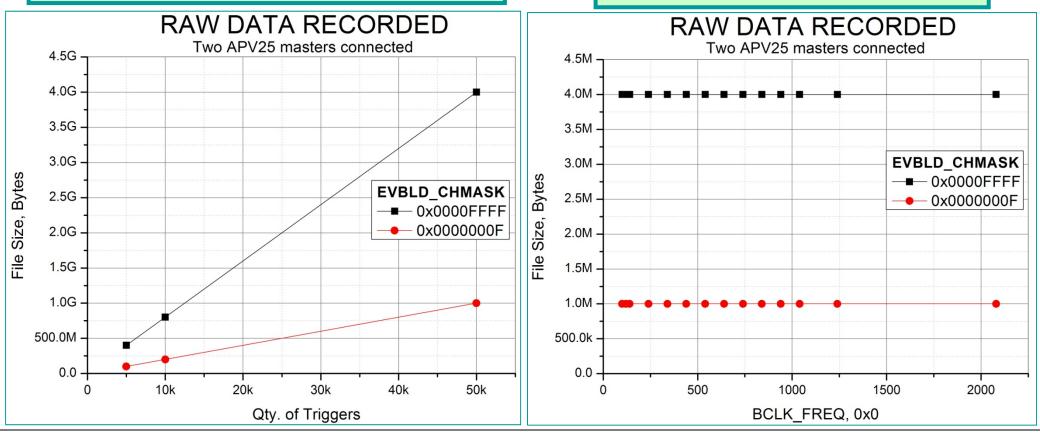
0000FFFF = ... 1111 1111 1111 1111 \rightarrow All 16 APVs data

0000000F = ... 0000 0000 000 1111 \rightarrow 4 APVs data

The File size remains the same, even when the

triggers generated by the FEC increase

 $Tr = \frac{1}{(BCLK_FREQ * 64 * 25)}$

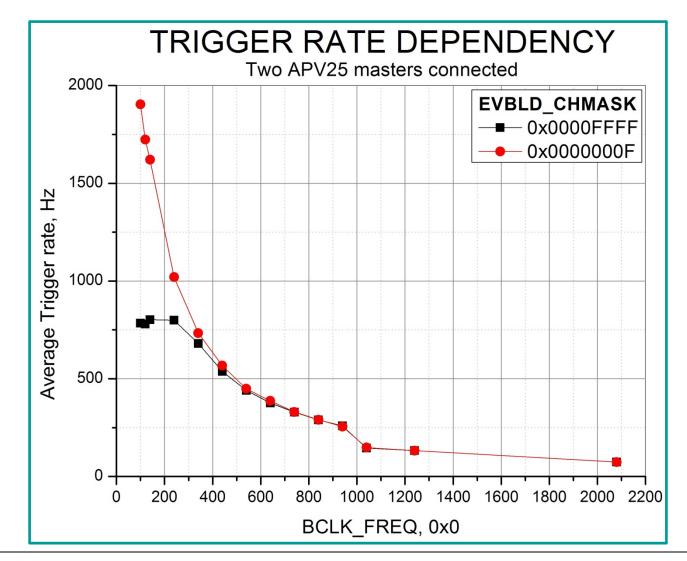




This is the comparison of the average trigger rate for the two cases in study.

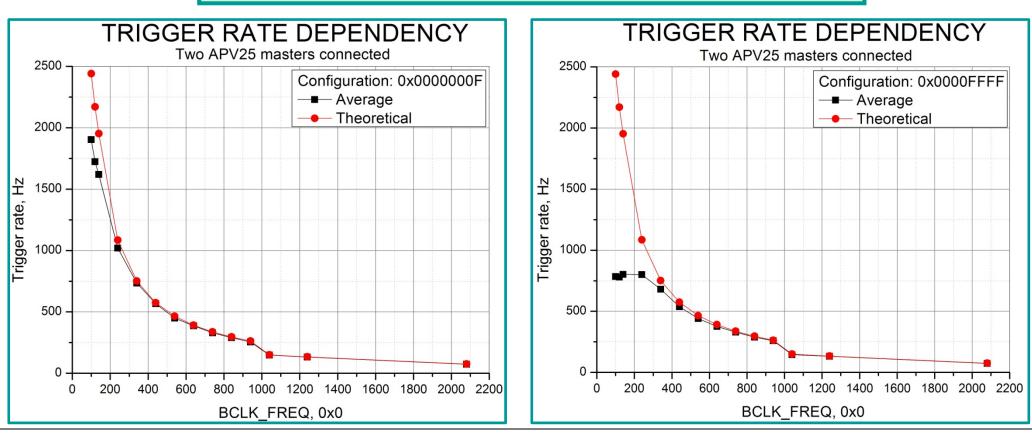
In black the configuration on the FEC was to read the data from 16 APV25 chips and in red was for only 4 APV25s.

The maximum average trigger for the configuration of 4 APV25s was very close to 2 kHz in contrast for the case of 16 APV25s the maximum was reached at 784 Hz





For the case of small quantity of APV25 chips connected to a FEC, the mean trigger rate is almost in complete agreement with the theoretical one. For the case of 16 APV25 chips, which is the maximum number of chips that one FEC can served, there is a saturation around 800 Hz, nevertheless in all cases no data losses were observed.





TODO

- Run Amore with the scripts provided by Kondo
- Compare file size and consistency of the data
- Customize all the parameters for my setup
- Investigate Common mode and pedestal corrections
- Work in the latency scan