



Boris Keil for the SLS 2.0 BPM & Orbit Feedback Team :: GFA :: Paul Scherrer Institute SLS 2.0 BPM And Fast Orbit Feedback System

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Introduction BPMs Mechanics Electronics Test Results Fast Orbit Feedback Summary & Outlook



Swiss Light Source Upgrade Project: SLS 2.0

<u>SLS 1.0:</u>

- 3rd generation synchrotron light source
- User operation since 2001
- Last beam Sept. 30, 2023

SLS 2.0:

- <u>1st beam 1/2025</u>
- <u>New storage ring</u>: >40x higher hard X-ray brilliance
- <u>Replace ageing hardware (BPM</u> electronics from 2001, ...)
- Keep linac, booster





| <u>Parameter</u> | <u>Units</u> | <u>SLS 1.0</u> | <u>SLS 2.0</u> | |
|------------------|--------------|------------------------------------|----------------|--|
| Circumference | m | 288 | | |
| Beam Current | mA | 400 | | |
| Injection Charge | nC | ~0.15 (Single Bunch Top-Up @ 3 Hz) | | |
| Beam Energy | GeV | 2.4 | 2.7 | |
| Main RF | MHz | 499.637 | 499.654 | |
| Harmonic No. | # | 480 | | |
| Hor. Emittance | pm | 5030 | 131-158 | |
| Vert. Emittance | pm | 5-10 | 10 | |
| Ring BPMs | # | 75 | 136 | |
| Ring Beam Pipe | | Stainless Steel Copper (NEC | | |



SLS 2.0 BPM Requirements

 $\sigma_{Y} \sim 5 \ \mu m$ nominal, may be reduced/adjusted

| Parameter | <u>Goal</u> | % of σ_{γ} |
|--|----------------|------------------------|
| Position Noise (0.1 Hz - 1 kHz BW), 400 mA | <50 nm RMS | 1% |
| Position Noise (0.1 Hz - 0.5 MHz BW), 400 mA | <1 µm RMS | 20% |
| Position Noise (0.5 MHz BW), 0.15nC, 1 Bunch | <50 µm RMS | - |
| | <100 nm / hour | 2% |
| Electronics Drift (400mA beam, constant) | <400 nm / week | 8% |
| | <1 µm / year | 20 % |
| | <250 nm / hour | 5% |
| Overall Drift (Electronics + Cables + Mechanics) | <1 µm / week | 20% |
| | <2.5 µm / year | 50% |
| Beam Current Dependence (Const. Fill. Patt.) | <100 nm / 4 mA | 2% |



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BPM Mechanics: Support

Water cooled copper block reduces position drift

Double steel plates, filled with sealed compound of balsawood & viscoelastic glue







BPM Mechanics: Performance

Center of BPM block would move even if bottom side did not: Stainless steel CTE ~16 ppm/°C \rightarrow distance of ~30 mm & dT ~ 10°C causes dY ~ 5 μ m.

| Simulated beam-induced BPM pickup <u>center</u> motion | <u>ΔΧ [μm]</u> | <u>ΔΥ [μm]</u> |
|--|----------------|----------------|
| Motion from 0 mA to 400mA beam current | -11.3 | 4.7 |
| Motion @ Top-up injection (400404mA) | < 0.1 | <0.05 |

- Assume worst case (400 mA, 9 ps bunch length, 3HC off)
- Additional drift due to air & water:
 - Simulation: $\Delta Y \sim 5 \mu m/^{\circ}C$ water temperature change
 - SLS 1.0 water often ~0.03°C peak-peak (\rightarrow 150nm), but not always ...
 - Being improved for SLS 2.0 (variable RPM for cooling machines, ...)
- Beam, air & water cause <u>common drift of all BPMs \rightarrow less critical (X-ray angle ...)</u>



BPM Mechanics: Production Status



- All BPM electrodes produced
- Most BPM blocks produced
- Beam pipe assembly in progress
- No vacuum problems so far







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SLS2 BPM Electronics: "DBPM3" (PSI Design)



Generic 19" back-end (AMD/Xilinx Zyng UltrasScale+ MPSoC: CPUs & FPGA on same chip), already used in SwissFEL (IBIC'22 TUP12)



Redundant power supply module



DBPM3 SLS 2.0 RFFE Block Schematics





<u>Mechanics</u> \rightarrow stable air & water & beam current (top-up).

<u>RF cables</u> (differential drift relevant):

- <u>Passive</u> methods:
 - <u>Equalize cable properties</u> (measure & sort by TOF/attenuation)
 - Thermal cable bundle isolation
 - Cable trays below floor (lower temp. variation)
- <u>Active</u> methods (so far):
 - <u>Pilot</u> tone

<u>Electronics:</u> <u>Pilot</u> tone, <u>crossbar switch</u>, active <u>temp. regulation</u> (DBPM3: 14 heating zones per RFFE ...), choose low-drift components, ...

<u>All:</u> Optional <u>feed-forward</u> correction on temperature & humidity sensors



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GUI: Settings for DBPM3 Test @ SLS 1.0 Ring





Python GUI: DBPM3 DDC Filters

Programmable during operation via EPICS ...

| Downconverter Con | ntrol | | Downconverter Stages | | | | | | land to Street O county F | |
|--------------------------------|---------|-------------------|--|------------------------|--------------|-------------------|----------------------|--------|-------------------------------------|-----------------------------|
| General Control | | | Stage 0 | | | | | 0. | Input to Stage U ouput - F | requency Response |
| Enable DWC | | Readback | CIC Ratio | 24 - 1024 | 104 🖨 | (| DDC Stage 0: | -25 | | |
| Save all Settings | | Load all Settings | FIR Ratio | 1 - 8 | 2 🔤 | | 1 04 MSDS (- | -50 | | _ |
| ADC Freq. [MHz] | 433.030 | : | FIR Taps (Order + 1) Resulting Output Rate [kHz | 1 - 208] | 208 | | 1.04 v 3P3 (- | 寶 -75 | | |
| DDS Signal | | | FIR Cutoff [kHz] | 0 - 520.469 | 500.000 \$ | | turn-by-turn), | -100 · | | |
| Frequency [MHz] Phase [degree] | | FIR Estop [kHz] | 500.000 - 520.469 | 518.000 1 | \mathbf{i} | <u>0.5 MHz BW</u> | -125 | | | |
| 66.620000 | \$ | 0.000000 | Stonband Suppression [dB] | | 100.000 | |) | -150 | | |
| | | Apply | FIR Design Method | ✓ min. Latency | blackman 👻 | | | -175 | | <u> </u> |
| DDS Pilot 0 | | | Plot Coefficients | Plot Frequen | cy Response | | | | –7500 –5000 –2500 0 Frequency [| 2500 5000 7500 [kHz] |
| Frequency [MHz] | | Phase [degree] | Stage 1 | | | | | | Input to Stage 1 ouput - F | Frequency Response |
| 67.150415 | \$ | 0.000000 | CIC Ratio | 1 - 1024 | 13 🗘 | (| | C | | |
| | | Apply | FIR Ratio | 1 - 8 | 4 🗘 | | DDC Stage 1 : | -25 | | |
| DDS Pilot 1 | | | FIR Taps (Order + 1) | 1 - 500 | 50 🗘 | / | 20 kSPS 3.3 | -50 | | |
| Erequency [MUz] | | | Resulting Output Rate [kHz | 1 | 20.018029 | | | 留 -75 | | |
| 67.150415 | | ↑ 0.000000 | FIR Cutoff [kHz] | 0 - 10.009 | 2.502 💲 | | KHZ BW (Tast | -100 | | |
| | | Apply | FIR Fstop [kHz] | 2.502 - 10.009 | 2.502 | | orbit feedback | -125 | | |
| | | | Stopband Suppression [dB] | | 130.000 🗘 | | data") | | | \^^ |
| | | | FIR Design Method | 🖌 min. Latency | flattop 👻 | | | -150 | | |
| | | | Plot Coefficients | Plot Frequen | ncy Response | | | -175 | -150 -100 -50 0 | 50 100 150 |
| | | | Stage 2 | | | | | | Frequency | [KHZ] |
| | | | CIC Ratio | 1 - 1024 | 125 💲 | | | | Input to Stage 2 ouput - | Frequency Response |
| | | | FIR Ratio | 1 - 8 | 8 🗘 | | | | 0 | |
| | | | FIR Taps (Order + 1) | 1 - 500 | 25 💲 | | DDC Stage 2 | -2 | 5 | |
| | | | Resulting Output Rate [kHz |] | 0.020018 | < | DDC <u>Stage Z</u> . | -5 | 0 | |
| | | | FIR Cutoff [kHz] | 0 - 0.010 | 0.00250 💲 | | 20 SPS, 11 Hz | [8] −7 | 5 | |
| | | | FIR Fstop [kHz] | 0.003 - 0.010 | 0.00250 | | BW ("slow | -10 | | |
| | | | Stopband Suppression [dB] | ✓ min. Latency | 130.000 💲 | | data") | -12 | 5 V | |
| | | | FIR Design Method | | flattop 👻 | | | -15 | D | |
| | | | Plot Coefficients | Plot Frequen | ncy Response | | | -17 | 5 | |
| | | | Apply I | Downconverter Settings | | | | | -0.15 -0.10 -0.05 0.00 Frequency |) 0.05 0.10 0.15 y [kHz] |



DBPM3 Electronics: 1-Bunch Position Resolution

DBPM3 electronics beam test @ SLS 1.0

- Booster (button BPMs): ~<u>36μm</u> RMS noise @ <u>0.15nC</u>
- Transfer line (res. striplines): ~<u>9μm</u> RMS @ <u>0.15 nC</u>



Special single bunch mode: DBPM3 firmware can zero ADC data outside signal peak area \rightarrow improves resolution using normal DDCs





DBPM3 Electronics Test @ SLS Storage Ring

<u>Test Setup:</u>

- DBPM3 RFFE Rev. C (received 8/2023): First version with pilot output
- 400 mA SLS 1.0 ring BPMs beam signal (last beam 9/2023)
 - Sum signal of 4 buttons combined with DBPM pilot output
 - Then <u>split to 4 RFFE channels</u> (test electronics drift only → <u>short cables</u>)
 - Simulates centered beam
- 1 <u>pilot</u> , f_{pilot} = f_{beam} <u>+ 0.531 MHz</u>
- ADC: 433 MSPS, 50% full scale (25% beam + 25% pilot)
- <u>2x2 crossbar</u> switches @ <u>130 kHz</u>
- <u>k_x/k_y = 7.1mm/7.2mm</u> (SLS 2.0 ring)
- Water cooled 19" rack





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DBPM3 Drift Test: SLS Hall Ambient Conditions





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SLS 2.0 Ring Fast Orbit Feedback (FOFB)

| FOFB Feature | <u>Units</u> | <u>SLS 1.0</u> | <u>SLS 2.0</u> |
|-------------------------------|--------------|-----------------------|-----------------------|
| BPMs in FOFB Loop | # | 73 | 115+ |
| Horizontal Dipole Correctors | # | 73 | 115+ |
| Vertical Dipole Correctors | # | 73 | 115+ |
| Corrector Magnet Type | | Sextupole Add-on Coil | Separate Dipoles |
| Min. Beam Size @ BPM | μm | ~5 | ~5 |
| Data Network Topology | | Ring (12 DSP Engines) | Tree (Central Engine) |
| BPM Bandwidth | kHz | 0.8* | ~3* |
| Corrector Power Supply Bandw. | kHz | ~3 | 3-5** |
| Correction Rate | kHz | 4 | 20-100 |
| Loop OdB Bandwidth | Hz | ~100 | ~350400 *** |

* BPM bandwidth programmable (Hz ... MHz)

** Magnet + beam pipe bandwidth >> 10 kHz. SLS 2.0 magnet power supply: Bandwidth programmable (within limits ...)

*** Depending on programmed BPM & magnet PS bandwidth.

<u>SLS 1.0</u> experience: <u>No significant perturbations > 150 Hz</u>, but higher BPM & corrector bandwidth adds more noise to beam -> targeting 350 Hz 0dB bandwidth, little motivation to go higher (and space/cost constraints ...).



FOFB Dipole Corrector Magnets

SLS 2.0: Magnets very close (price for keeping old tunnel & circumference ...) → laminated shield to minimize intermagnet field crosstalk

Magnet bandwidth ~ inverse square of <u>lamination thickness</u>. SLS 2.0: <u>0.35mm</u> -> bandwidth limited by power supply (trade-off strength vs. noise), not magnet/beam pipe











Data transfer from/to "FOFB Engine": Tree topology

- Can be scaled/extended (size, performance)
- Allows mix of different monitors & actuators (e-BPM, photon BPM, magnet PS, ...)
- Uses fiber optic links (50MBaud POF for magnet PS, 5-10 Gbps SFP+ for everything else, PSI custom protocol)



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Summary & Outlook

BPM Status:

- Mechanics: Production nearly finished, installation in progress.
- Electronics:
 - Latest version: Promising results, but only few pcs tested at SLS 1.0 shortly before dark time
 - Forced to change PCB soldering company (old one shut down CH site ...)
 - <u>Risk reduction</u>: Want more time for test & possible improvements of final version.
 - Production of <u>1st generation of new BPM electronics</u> has started, use <u>for most ring BPMs</u> <u>in 2025</u>, keep old SLS 1.0 BPM electronics in uncritical booster/linac/TL one more year
 - Produce <u>2nd generation</u> in 2025 & <u>install in ring 2026</u>, move 1st generation to <u>booster/linac/TL</u>, get rid of old SLS 1.0 hardware.

FOFB Status:

- Magnets and power supplies produced, installation starting
- New central SLS 2.0 FOFB engine & fiber links to BPMs/PS successfully tested at SLS 1.0 with beam
 - So far algorithm in software (CPU of Zynq UltraScale+ MPSoC) → 4 kHz correction rate @ SLS1
 - Presently moving algorithm to FPGA part of MPSoC \rightarrow expect 20+ kHz corr. rate for SLS 2.0



Wir schaffen Wissen – heute für morgen

Thanks to:

...

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and many others in the SLS 2.0 project team & PSI support groups





Appendix: Supplementary Slides ...



BPM: Button Temperature

Simulation of button electrode temperature (rare worst case: 400 mA, 3HC off, 9 ps bunch length)









BPM: Thermal Stress of Borosilicate Glass





BPM Button Electrode Sorting

- <u>Transfer impedance</u> of all button electrodes <u>measured pre-welding</u>
- <u>Sorted</u> by impedance: <u>Reduce</u> contribution to position <u>offset</u> from ~50 μm to <u>~1 μm</u>





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SLS2 BPM Electronics: Prototype Test @ SLS1





FOFB Bandwidth & Noise Considerations

- Higher FOFB 0dB bandwidth improves damping of high-frequent perturbations, but also adds noise to beam
- Perfect machine: No perturbations \rightarrow best orbit stability when FOFB is off \bigcirc
- Real-world machine: Optimal choice of FOFB bandwidth depends on perturbation spectrum
- SLS 1.0: Little perturbations > 150 Hz, mainly 50 Hz harmonics & sub-100 Hz vibrations
- SLS 2.0: Space & cost & time constraints → choice of single dipole orbit corrector used both for static and dynamic orbit corrections
 - Corrector must be strong enough for larger static corrections (to be reduced by mechanical realignment)
 - Corrector must be fast (for FOFB) but not add to much noise to beam
 - Expect 350 Hz 0dB bandwidth to be good compromise between damping perturbations & adding noise.





Simulation of SLS 2.0 FOFB correction bandwidth of 324 Hz, assuming 3 kHz BPM and corrector bandwidth, and 0.5ms system latency (= conservative assumptions, may get better). 0dB bandwidth can be increased > 400 Hz, at expense of adding more noise to beam.