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Spatial resolution of RSD2 array of pixels measured at a beamtest

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This contribution presents the first study of the spatial resolution of an array of RSD2 450 μm pitch pixels. The results have been obtained in the latest test beam at the DESY 6 GeV/c electron line. The readout board is based on the FAST2 ASIC, a 16-channel fast amplifier chip developed in Torino. The results demonstrate that resistive readout yields a spatial resolution smaller than 5% of the pixel size and a 100% fill factor over an area covered by multiple pixels.

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