**Detector and SEE experiments** 

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## Introduction

- 1. About the new HISPANoS neutron beam now open for experiments at the National Center of Accelerators, Sevilla, Spain
- 2. On a first experiment about detection of neutron induced nuclear reactions in silicon photodiodes and its intrincacies
- 3. Neutron induced Single Event Effects in an Intel MAX10 FPGA (55 nm)



Preparation of the Neutron Beam Line for the experiment





#### SEU FPGA experiment





HiSPANoS is the first Accelerator-based neutron source in Spain and it is installed at the 3 MV Tandem Accelerator. Operates since 2013 in continuous mode and since 2018 in pulsed mode.



#### **Neutron production targets/mechanisms**

Monoenergetic and broad energy neutron beams

For our experiments we choose Fast Neutrons between up to 10 MeV, <sup>2</sup>H on <sup>9</sup>Be target (<sup>9</sup>Be(d,n)<sup>10</sup>B)



#### Projectiles

- <sup>1</sup>H, <sup>2</sup>H up to 6 MeV
- <sup>4</sup>He up to 6 MeV
  Continuous mode
- Up to 10 uA Pulsed mode
- 1 2 ns pulso wi
- 1-2 ns pulse width
- 32,5 kHz 2 MHz
- 1-4 m flight path

Depation	Q-value (MeV)	Eth (MeV)	Target			
Reaction			Material	Thickness	Diameter	Neutron spectra
<sup>2</sup> H(d,n) <sup>3</sup> He	3,27	0,0	D/Ti	546 μg/cm²	30 mm	Quasi-monoenergetic 2,2 – 6,1 MeV
<sup>9</sup> Be(p,n) <sup>9</sup> B	-1,85	2,06	Be	500 µm	25 mm	Continuum up to 4 MeV
<sup>9</sup> Be(d,n) <sup>10</sup> B	4,36	0,0				Continuum up to10 MeV
<sup>7</sup> Li(p,n) <sup>7</sup> Be	-1,64	1,88	Li	500 μm	25 mm	Continuum up to 4 MeV
<sup>7</sup> Li(d,n) <sup>8</sup> Be	15,03	0,0				Continuum up to 20 MeV





energy (MeV)

Be(d,n)

5,75

 $\sim 10$ 

#### **Monoenergetic and Broad Energy Neutron Beams**



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https://indico.cern.ch/event/1334364/ 43rd RD50, Nov 28th to Dec 1st, CERN, 2023

 $5,4(0,5)\cdot 10^{6}$ 



Nuclear data relevant to single event upsets in semiconductor memories induced by cosmic-ray neutrons and protons, Y. Watanabe, H. Nakashima, Proc. of 2006 Symposium on Nuclear Data, Jan 25-26, 2007, SND2006-III.03 Incidence of multiparticle events on soft error rates caused by n-Si Nuclear Reactions, F.Wrobel et al, IEEE TNS 47(6), 2000



#### **Expected Nuclear reactions in silicon**



SRIM range/straggling simulation, IEL, NIEL <sup>28</sup>Si 200 keV in silicon bulk



SRIM range/straggling simulation, IEL, NIEL <sup>25</sup>Mg 1 MeV in silicon bulk



Ionizing Energy Loss, IEL. Non-Ionizing Energy Loss, NIEL

SRIM simulations for Si, Mg and Al ions at different possible kinetic energies from a nuclear reaction gives a hint about LET (minimum) and range in the Silicon Detector Bulk: the ion gives all its energy to the bulk and is trapped.



SRIM range/straggling simulation, IEL, NIEL <sup>27</sup>AI 750 keV in silicon bulk

lon	IEL	LET (MeV/cm <sup>2</sup> -mg)	Mean Range
	(eV/Å)		Long./Lateral (µm)
<sup>28</sup> Si (elastic	~30	~1.3	~0.27/0.06
recoil)			
<sup>25</sup> Mg (from	~50	~2.1	~1.4/0.3
<sup>28</sup> Si(n,a) <sup>25</sup> Mg)			
<sup>27</sup> Al (from	~40	~1.7	~1.1/0.2
<sup>28</sup> Si(n,p) <sup>27</sup> Al)			

#### **Photodiode Experiment**

- Key to successful result is to achieve a very low noise, high speed pre-amplifier.
  - Battery-powered pre-amp board (remove AC noise).
  - Single trans-impedance amplifier, 500MHz BW, femtoA input bias, femtoF input cap (from Analog Devices Inc.).
  - VREF-filtered voltage regulators for low noise (uV) high ripple rejection (68dB).
  - Custom board layout to minimize parasitic capacitances
- Multiple diodes to maximize exposed cross section/oscilloscope channel usage.
- Hamamatsu S1336 series PIN detectors, 20 μm depletion depth
  R = 4.7kO







**Photodiode Experiment** 

Vout

Direct measurement (Trans Impedance Amplifiers) is useful to get signals in a straight manner but it is very sensitive to Electro Magnetic Interference. There is plenty of EMI in the accelerator room, with an amplitude no less than 30 mV. The EMI signal is the same in both detectors but the ionization signal, at an instant, happens in only one.

Time (s)

1e-7



#### **Photodiode Experiment**

Just for calibration we put the same setup under gamma radiation from a Co60 source (accelerator shut off). We detect no spike from gamma photons so we concluded the dataset from the experiment with the accelerator on was due to neutron induced nuclear reactions in the photodetectors silicon bulk.

Digital Signal Processing of the data set showed the same conclusion:







**Photodiode Experiment** 



#### **FPGA Experiment**

The second experiment was oriented to evaluate the usefulness of HISPANoS for Single Event Upset experiments. The target was a MAX10 FPGA card, with another MAX10 FPGA out of beam as local controller. The MAX10 target had simple digital design: a memory controller and a RAM matrix, made with the flip-flop pool of the FPGA. A simple word (fff...) was recorded in the FPGA RAM memory. The memory controller readouts the RAM and send the data stream to the controller FPGA, from there to the control computer on a safe place. We used two uarts as a double check in the data transfer.





In this experiment the difficult part is at the digital design. Irradiation data analysis is very easy (just a disagreement in the fff... word received).



#### **FPGA Experiment**

With the Target FPGA close to the neutron source we detected SEUs at a rate of one every couple of minutes, sometimes even a total failure of the readout (an indication of SEU in the memory controller). No stuck bits in any case were seen, with the Target FPGA in pristine condition after scrubbing (or reconfiguration). For future experiments we will design a Shift Register structure in the Target FPGA, without the internal memory controller block, now possible because the MAX10 is insensitive to Single Event Effects Stuck Bits.







## Conclusions

- We learned about how to make neutron detection experiments in the new HISPANoS neutron beam facility at CNA, Sevilla
- The photodiode experiment opens the way to more sophisticated detector experiments in the facility. As a plus, we got straight signals from neutron (up to 10 MeV) induced nuclear reactions in silicon.
- The MAX10 FPGA shows no stuck bits under neutron irradiation. New Neutron Single Event Effects experiments are planned.

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# Thanks for your attention! fpalomo@us.es

