The perfectly parallel program: Design philosophy for parallel programming (2/2)

Tuesday, 16 April 2024 10:00 (1 hour)

- Relating the fundamental restrictions and capabilities of hardware parallelism to software engineering
- Defining what makes an algorithm “parallel” or not
- Considerations for designing a parallel algorithm with respect to control flow and memory access
- Legacy code, and how to go about re-designing existing software for novel hardware

Attended school

Number of exercise hours

Number of lecture hours

Presenter: WETTERSTEN, Zenny (CERN)