

AdaptivePerf: an architectureagnostic code profiler

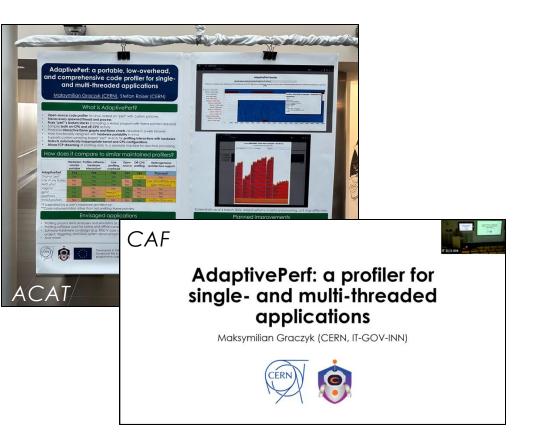
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CHEP'24, Kraków, 22 October 2024



You may have heard this before...

- This year: ACAT, CERN Compute & Accelerator Forum (CAF), RISC-V Summit Europe, ...
- There have been exciting developments, so please keep listening!
- Updates are coloured in green.



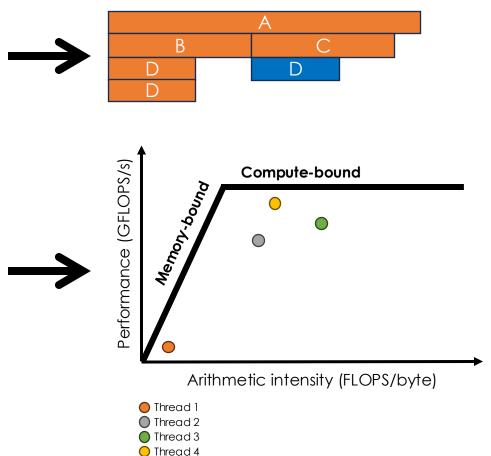
Intro: SYCLOPS



- A project funded from the European Union HE research and innovation programme (grant agreement No 101092877) about hardware acceleration with open standards using SYCL and RISC-V: <u>https://www.syclops.org</u>
- CERN project tasks:
 - 1. Implementing SYCL support in ROOT and cling + demonstrating it on a Lorentz vector calculation example.
 - 2. Benchmarking and profiling + integration testing of all SYCLOPS use cases (ROOT, genomics analysis, and autonomous systems).
- AdaptivePerf is part of task 2, but its applications and development plans **extend beyond SYCLOPS**.

Intro: Flame graphs and roofline modelling

- Flame graphs: plots showing hierarchical data as merged stacked blocks, with their width being proportional to the corresponding data frequency. The x-axis can be either non-time-ordered or time-ordered.
- Roofline modelling: a benchmarking and profiling method which uses machine-specific performance bounds, program arithmetic intensity, and program floating-point operations per second to determine whether a code is memory- or compute-bound.

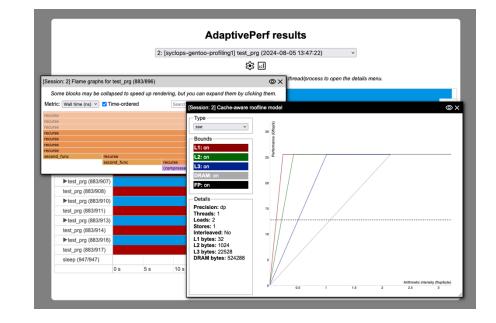


What is AdaptivePerf?

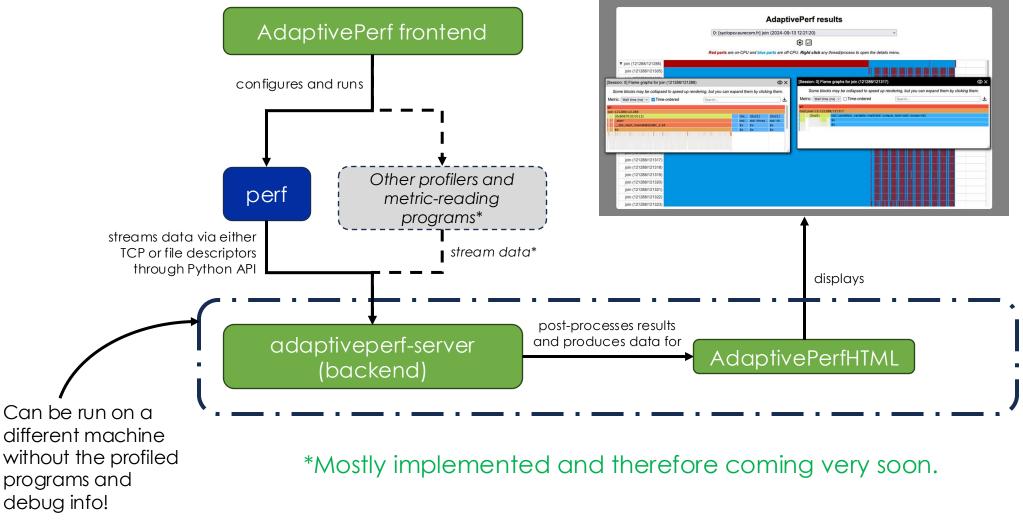
- Open-source and language-agnostic code profiler for Linux, originally built on top of "perf" with custom patches
- Samples both on-CPU and off-CPU activity
- Traces every spawned thread and process
- Minimises risk of broken profiled stacks for programs compiled with frame pointers by detecting inappropriate kernel and CPU configurations automatically
- Produces data for rendering interactive flame graphs and charts in a web browser with a new GUI and cache-aware roofline plots produced by the CARM tool from INESC-ID
- Main functionality designed with hardware portability in mind (tested on x86-64, arm64, and RISC-V)
- Supports custom sampling-based "perf" events for **profiling** interactions with hardware
- Allows TCP streaming of profiling data to a separate machine for real-time processing

NB: AdaptivePerf is **not** a continuous profiler! It profiles single workloads and is not meant for 24/7 monitoring.

The main target audience is SW and HW developers optimising their software and/or hardware, also as part of software-hardware co-design for specific applications.



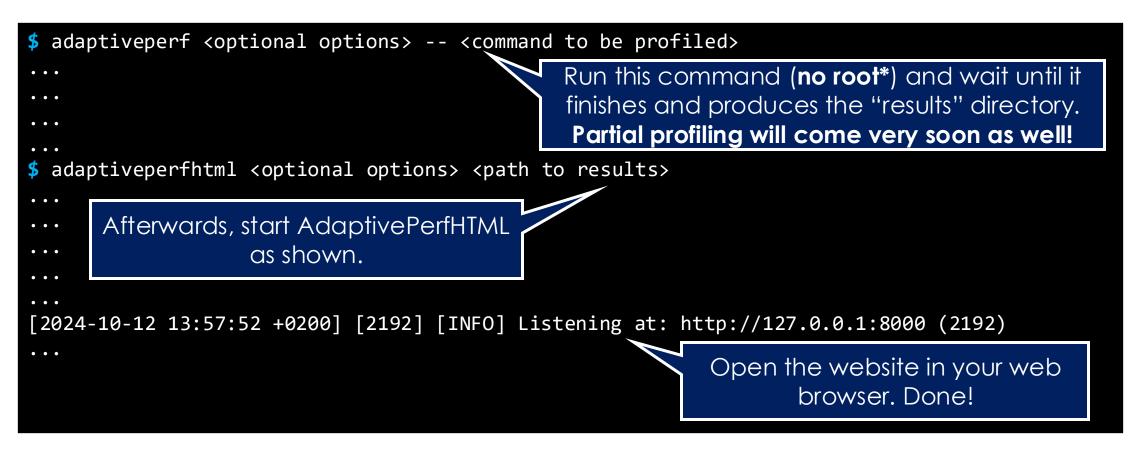
How does AdaptivePerf work under the hood?



How to download AdaptivePerf?

- It's open-source and you can get it for free from our GitHub: <u>https://github.com/AdaptivePerf</u>.
- AdaptivePerf is available as an early development version, in form of a source code, a Gentoo-based VM image, and Docker + Apptainer images with frame pointers (FPs).
- DEB + RPM, a CVMFS setup, and pre-built non-Linux server binaries will follow soon.
- There are 2 parts:
 - AdaptivePerf: the main program (frontend + backend) licensed under GNU GPL v2 only. It also includes the patched "perf".
 - AdaptivePerfHTML: the web server for displaying profiling results as an interactive website, licensed under GNU GPL v3.

Quick start with AdaptivePerf



*Under certain circumstances.

AdaptivePerf results

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Red parts are on-CPU and blue parts are off-CPU. Right click any thread/process to open the details menu.

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AdaptivePerf results	6	_
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If you have a PDF version of the slides or the video does not work, you can play the demo here: https://indico.cern.ch/event/1338689/contributions/6010664/attachments/2951142/5187591/Video%20demo.mp4

AdaptivePerf results

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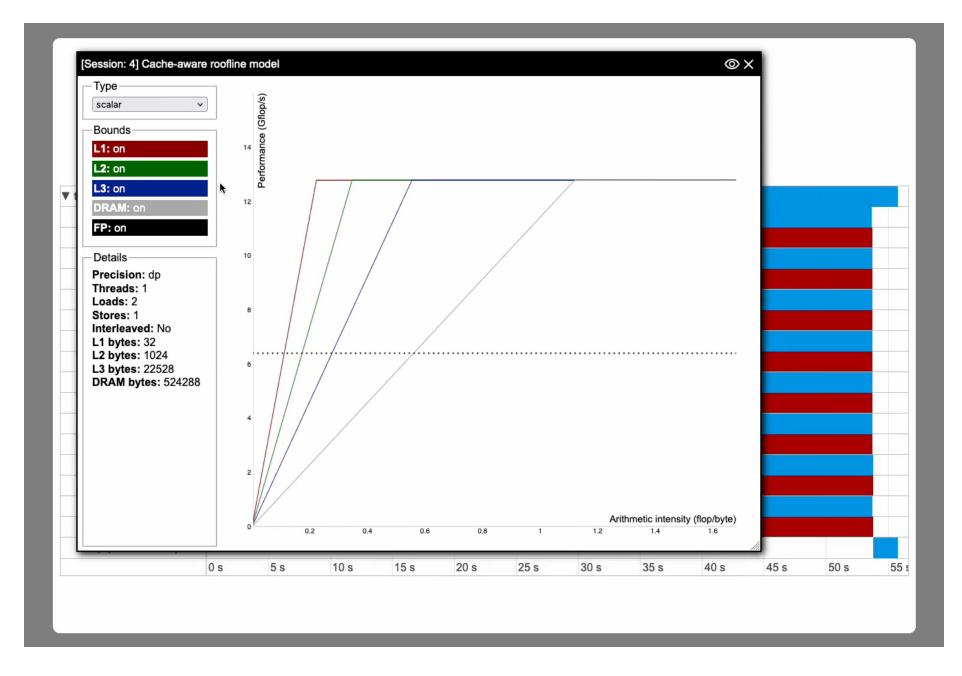
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AdaptivePerf results

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Work in progress: profiling heterogeneous and custom architectures

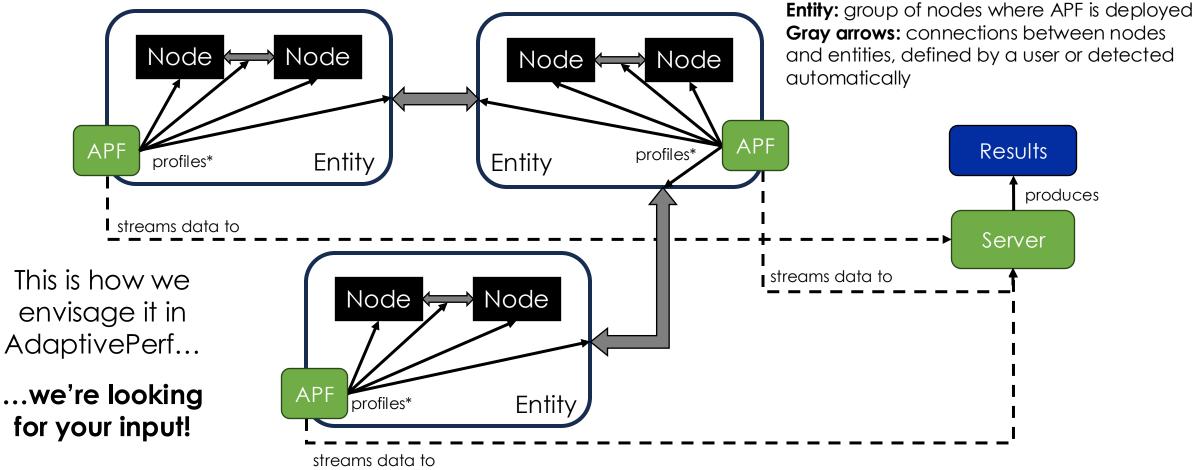
- In the context of the changing tech landscape, we want AdaptivePerf to help advance computing at CERN and other physics experiments.
- Given the variety of platforms (CPUs with ISAs, GPUs, FPGAs, ASICs etc.), the goal of AdaptivePerf is delivering a single and unified profiling-based platform for comparing and customising off-the-shelf and in-house software and hardware architectures <u>across the entire spectrum</u>:



AdaptivePerf talk at CHEP'24

Work in progress: profiling heterogeneous and custom architectures APF: AdaptivePerf frontend instance Node: arbitrary hardware unit defined by a

*using built-in methods or methods defined by a user



user or detected automatically

Future plans

- Exporting profiling results to a well-documented SQL database
- Looking into profiling memory usage by threads/processes
- Adding profiling on a lower level and with more debug info, e.g. showing line numbers, going down to LLVM IR / MLIR / assembly etc.
- Downloading debug symbols automatically if not present
- Removing or weakening the frame pointer compilation requirement

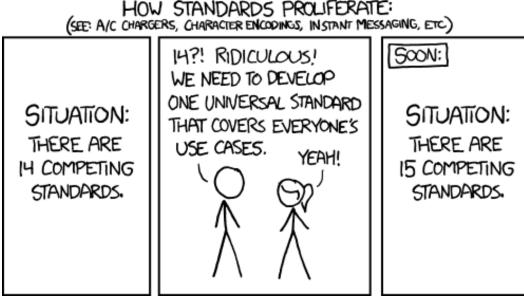
Thank you!

Any questions or comments?

Feel free to reach out to me at the conference or by e-mail: <u>maksymilian.graczyk@cern.ch</u>

Extra slides

Sounds familiar?



From https://imgs.xkcd.com/comics/standards.png

AdaptivePerf will **NOT** be the (n + 1)-th competing profiler (to some extent). It will be a unified platform where existing profilers can be integrated easily and any remaining gaps can be filled in by AdaptivePerf itself or a user.

How does AdaptivePerf compare to other similar and maintained profilers?

	Hardware- vendor- portable	Runs on RISC-V	Profiles software- hardware interaction ¹	Low profiling overhead	Open-source	Off-CPU profiling	Flexible support of heterogeneous and custom architectures	Flexible support of multi-node systems
AdaptivePerf	Yes	Yes	Yes	Yes	Yes	Yes	WIP ²	WIP ²
Original "perf"	Yes	Yes	Yes	Yes	Yes	Limited	No	No
Intel VTune Profiler	No	No	Yes	Yes	No	Yes	Intel only, NF ³	MPI only, NF ³
AMD µProf	No	No	Yes	Yes	No	Yes	AMD GPUs only, NF ³	MPI only, NF ³
valgrind	Yes	Yes, as a fork	No	No	Yes	No	No	No
gprof	Yes	Yes	No	Needs Cl⁴	Yes	No	No	No
gperftools	Yes	Yes	No	Needs Cl ^₄	Yes	No	No	No
NVIDIA profilers	No	No	Yes	No	No	Yes	NVIDIA GPUs only, NF ³	NF ³
TAU	Yes	No	Yes	Yes	Yes	Yes	GPUs only, NF ³	MPI only, NF ³
HPCToolkit	Yes	No	Yes	Yes	Yes	Yes	GPUs only, NF ³	MPI only, NF ³

¹ If supported by a user's hardware architecture.

² Work in progress.

³ Non-flexible because any architectural updates can be implemented only by the profiler developers or via code contributions if open-source.

⁴ Code instrumentation other than not omitting frame pointers.

How to tackle the frame pointer compilation requirement?

- For example, by DWARF processing whenever frame pointers cannot be used, see: <u>https://www.polarsignals.com/blog/posts/2022/11/29/profiling-</u> <u>without-frame-pointers</u> (this is more compact than what "perf" currently does).
- Full removal may be unnecessary, see: <u>https://brendangregg.com/blog/2024-03-17/the-return-of-the-frame-pointers.html</u>.