Conference on Computing in High Energy and Nuclear Physics



Contribution ID: 538 Type: Talk

Simulating the Carbon Cost of Grid Sites

Thursday 24 October 2024 11:30 (30 minutes)

We present first results from a new simulation of the WLCG Glasgow Tier-2 site, designed to investigate the potential for reducing our carbon footprint by reducing the CPU clock frequency across the site in response to a higher-than-normal fossil-fuel component in the local power supply. The simulation uses real (but historical) data for the UK power-mix, together with measurements of power consumption made at Glasgow on a variety of machines, and is designed to provide a tool to inform future procurements and the operation of sites. The output of the simulation, combined with considerations of embedded carbon, can also be used to inform and optimise the policy for replacing older hardware with more energy efficient devices. The rate of transition to more energy efficient hardware must be balanced against the embedded carbon in the manufacture of new machines, and frequency modulation must be balanced against both the loss of site throughput and the accounting of embedded carbon. Frequency modulation can also be used to reduce power requirements to address short-term supply issues, irrespective of the carbon content.

Primary authors: BORBELY, Albert Gyorgy (University of Glasgow (GB)); BRITTON, David (University of Glasgow (GB)); SPITERI, Dwayne (University of Glasgow); SIMILI, Emanuele (University of Glasgow (GB)); STEWART, Gordon; SKIPSEY, Samuel Cadellin; LLOYD, Steve (University of London (GB))

Presenter: BRITTON, David (University of Glasgow (GB))

Session Classification: Plenary session

Track Classification: Plenary